



# New Product Announcement

## AP7346

### AP7346 – Dual High Accuracy Current Limit LDO with Enable

AP7346 is a dual, high-accuracy current-limit LDO regulator with enable, high-accuracy output voltage, low  $R_{DS(ON)}$ , high PSRR, low output noise and low quiescent current.

It is available in fixed output voltage variants from 1.2V to 3.6V in various combinations. It is available with an output discharge function (D-suffix).

AP7346 provides high PSRR (75dB @ 1kHz) without the need for an external bypass capacitor and is ideal for noise sensitive applications such as audio, video and RF subsystems.

It's low power consumption combined with excellent line and load transient responses, makes the AP7346 ideally suited for low-power handheld communication equipment.

AP7346 is packaged in the small footprint, low profile DFN1212-6 package and is ideally suited to high-density PCB layouts.



#### The Diodes' Advantage

- **Wide Input Voltage: 1.7V to 5.25V**  
Offers lower operation down to 1.7V supporting operation over whole range of Li-on battery cells.
- **High Accuracy current limit 132mA to 182mA**  
Fingerprint module power supply and I/O supply
- **High PSRR (75dB @ 1kHz) and low noise (60µVrms @ 10Hz - 100kHz)**  
Suitable for noise sensitive applications such as audio, video and RF sub-systems
- **1.0% Output Accuracy at room temperature, 1.5% over -40 to 85°C**  
Maintains accuracy over all conditions
- **Miniaturized package, DFN1212-6 (1.2mmx1.2mmx0.4mm)**  
The small package dimensions and thin height meets small footprint and low profile requirements of the latest portable equipment
- **Low Quiescent Current at 35µA**  
Offers power saving in low-power and battery operated products

#### Circuit Functions

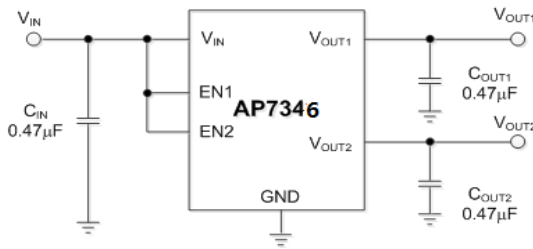
- Enable for shutdown with auto-discharge
- Fold-back short protection

#### Applications

- Finger print module
- Smart phone/tablets
- Portable video
- Wireless adaptor/RF supply
- Cameras
- Portable media player
- Wireless communication

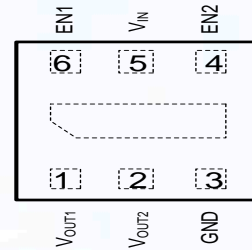


### Typical Application Circuit



### Package

#### Top View



X2-DFN1212-6

### Product Portfolio

Part Number	V <sub>IN</sub> (V)	V <sub>OUT1</sub> (V)	V <sub>OUT2</sub> (V)	I <sub>OUT</sub> (mA)	V <sub>DROP</sub> @ Full Load (mV)	I <sub>q</sub> (µA)	PSRR @1kHz (dB)	Noise (µVrms)	Output Accuracy (%)	Enable	BYPASS Cap	Package
AP7346	1.7 to 5.25	1.2, 1.5, 1.8, 2.5, 2.8, 3.0, 3.3, 3.6	1.2, 1.5, 1.8, 2.5, 2.8, 3.0, 3.3	130/130	300/300	35	75	60	1	Yes	No	DFN1212-6

#### AP7346X - XXXX XXX - X

