

PI7C9X112SL PCIe®-to-PCI® Reversible Bridge Power Consumption in Normal Mode

Power Consumption of PI7C9X112SL

The following table lists the power consumption of each power rail used by the PI7C9X112SL PCIe-to-PCI reversible bridge. This information can be taken into consideration for system design and power budget.

Normal mode	33MHz				66MHz			
	Power Saving Disabled		Power Saving Enabled		Power Saving Disabled		Power Saving Enabled	
	mA	mW	mA	mW	mA	mW	mA	mW
VDDA (1.0V)	59	59	59	59	59	59	59	59
VDDP (1.0V)	66	66	66	66	65	65	65	65
VDDAUX and VDDCAUX (1.0V)	36	36	36	36	35	35	35	35
VTT (1.0V)	29	29	29	29	29	29	29	29
VDDC (1.0V)	204	204	172	172	181	181	142	142
VD33 (3.3V)	48	158	48	158	112	368	112	368
VAUX (3.3V)	<1	<1	<1	<1	<1	<1	<1	<1
Total (mW)	552		520		737		697	

Test Conditions:

- 33MHz: Tested on 4 IBM Token Ring cards.
- 66MHz: Tested on 3 Intel Dual Port Gigabit Ethernet cards.