



BSS123W

N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{DSS}	R _{DS(ON)}	I _D T _A = +25°C
100V	6.0Ω @ V _{GS} = 10V	170mA

Description and Applications

This MOSFET is designed to minimize the on-state resistance (RDS(ON)) yet maintain superior switching performance, making it ideal for high-efficiency power-management applications.

- Small servo motor controls
- Power MOSFET gate drivers
- Switching applications

Features and Benefits

- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- High Drain-Source Voltage Rating
- Totally Lead-Free & Fully RoHS compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- This part is qualified to JEDEC standards (as references in AEC-Q) for High Reliability.

https://www.diodes.com/quality/product-definitions/

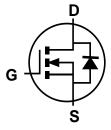
 An automotive-compliant part is available under separate datasheet (<u>BSS123WQ</u>)

Mechanical Data

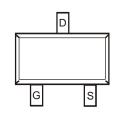
- Package: SOT323
- Package Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Terminals: Matte Tin Finish Annealed over Alloy 42 Leadframe (Lead Free Plating). Solderable per MIL-STD-202, Method 208 63
- Weight: 0.006 grams (Approximate)







Equivalent Circuit



Top View

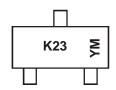
Ordering Information (Note 4)

Orderable Part Number	Dackers	Packing		
	Package	Qty.	Carrier	
BSS123W-7-F	SOT323	3000	Tape & Reel	

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



K23 = Product Type Marking Code YM = Date Code Marking Y or Y = Year (ex: L = 2024) M = Month (ex: 9 = September)

Date Code Key

Year	2002	-	2024	2025	2026	2027	2028	2029	2030	2031	2032	2033
Code	N	-	L	М	Ν	Р	R	S	Т	U	٧	W
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Drain-Source Voltage		VDSS	100	V
Drain-Gate Voltage $R_{GS} \le 20k\Omega$		V _D GR	100	V
Gate-Source Voltage	Continuous	Vgss	±20	V
Drain Current (Note 5)	Continuous Pulsed	I _D I _{DM}	170 680	mA

Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

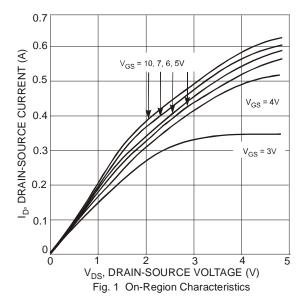
Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 5)	P_D	200	mW
Thermal Resistance, Junction to Ambient (Note 5)	Reja	625	°C/W
Operating and Storage Temperature Range	TJ, TSTG	-55 to +150	°C

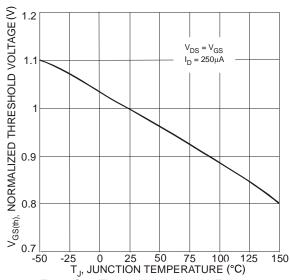
Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

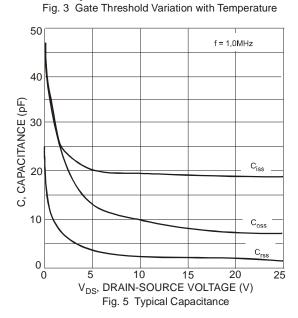
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 6)	•					
Drain-Source Breakdown Voltage	BVDSS	100			V	$V_{GS} = 0V, I_{D} = 250\mu A$
Zero Gate Voltage Drain Current	IDSS	_		1.0 10	μA nA	V _{DS} = 100V, V _{GS} = 0V V _{DS} = 20V, V _{GS} = 0V
Gate-Body Leakage, Forward	IGSSF	_		50	nA	V _G S = 20V, V _D S = 0V
ON CHARACTERISTICS (Note 6)						
Gate Threshold Voltage	V _{GS(th)}	8.0	1.4	2.0	V	$V_{DS} = V_{GS}$, $I_D = 1mA$
Static Drain-Source On-Resistance	RDS(ON)	_		6.0 10	Ω	$V_{GS} = 10V, I_{D} = 0.17A$ $V_{GS} = 4.5V, I_{D} = 0.17A$
Forward Transconductance	grs	80	370	_	mS	V _{DS} = 10V, I _D = 0.17A, f = 1.0kHz
Drain-Source Diode Forward Voltage	VsD	_	0.84	1.3	V	V _G S = 0V, I _S = 0.34A
DYNAMIC CHARACTERISTICS (Note 7)						
Input Capacitance	C _{iss}	_	29	60	pF	
Output Capacitance	Coss	_	10	15	pF	V _{DS} = 25V, V _{GS} = 0V, f = 1.0MHz
Reverse Transfer Capacitance	Crss	_	2	6	pF	
SWITCHING CHARACTERISTICS(Note 7)						
Turn-On Rise Time	t _r	_		8	ns	
Turn-Off Fall Time	tf	_	_	16	ns	$V_{DD} = 30V, I_D = 0.28A,$
Turn-On Delay Time	t _D (ON)	_	_	8	ns	$R_{GEN} = 6.0\Omega$, $V_{GS} = 10V$
Turn-Off Delay Time	tD(OFF)		_	13	ns	

- 5. Part mounted on FR-4 board with recommended pad layout, which can be found on our website at http://www.diodes.com.
- 6. Short duration pulse test used to minimize self-heating effect.
- 7. Guaranteed by design. Not subject to production testing.









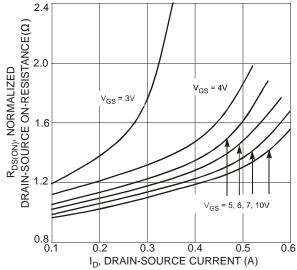


Fig. 2 On-Resistance Variation with Gate Voltage and Drain-Source Current

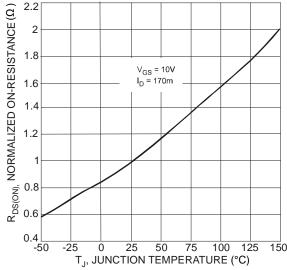


Fig. 4 On-Resistance Variation with Temperature

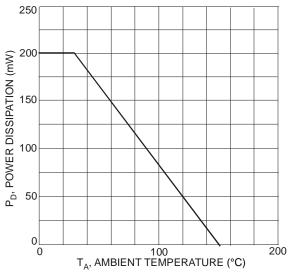


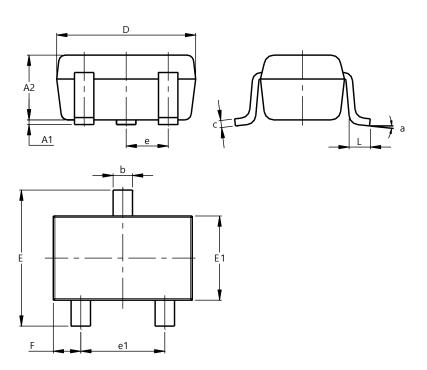
Fig. 6 Power Derating Curve, Total Package



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT323

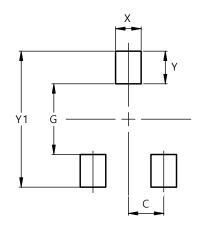


SOT323						
Dim	Min	Max	Тур			
A1	0.00	0.10	0.05			
A2	0.90	1.00	0.95			
b	0.25	0.40	0.30			
C	0.10	0.18	0.11			
D	1.80	2.20	2.15			
Е	2.00	2.20	2.10			
E1	1.15	1.35	1.30			
е	0.650 BSC					
e1	1.20	1.40	1.30			
F	0.375	0.475	0.425			
١	0.25	0.40	0.30			
а	0°	8°				
All Dimensions in mm						

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT323



Dimensions	Value (in mm)
C	0.650
G	1.300
Х	0.470
Y	0.600
Y1	2.500



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