



D10V0S1US2LP1610

1 CHANNEL HIGH SURGE TVS DIODE

Product Summary

	-	
VBR (Min)	IPP (Max)	Vc Typ at IPP Max
11.4V	100A	17.5V

Description

This new generation TVS is designed to protect sensitive electronics from the damage due to ESD and surge. The combination of small size and high ESD surge capability makes it ideal for use in portable applications such as cellular phones, digital cameras, and MP3 players.

Applications

- Cellular handsets
- Portable electronics
- Computers and peripherals

Features

- Low Profile Package (0.50mm typical) and Ultra-Small PCB Footprint Area (1.9mm × 1.3mm max) Suitable for Compact Portable Electronics
- Provides ESD Protection per IEC 61000-4-2 Standard: Air ±30kV, Contact ±30kV
- Provides Surge and Lightning Protection per IEC 61000-4-5 Standard: IPP max 100A
- One Channel of ESD and Surge Protection
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please <u>contact us</u> or your local Diodes representative. <u>https://www.diodes.com/quality/product-definitions/</u>

Mechanical Data

- Package: U-DFN1610-2
- Package Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: NiPdAu over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (4)
- Weight: 0.003 grams (Approximate)



Device Schematic

Ordering Information (Note 4)

Part Number	Baakaga	Marking	ing Reel Size (inches) Tape Width (mm)		Pao	Packing	
Part Nulliber	Package	Warking	Reel Size (Inches)	rape width (mm)	Qty.	Carrier	
D10V0S1US2LP1610-7	U-DFN1610-2 (Type B)	10S	7	8	10,000	Tape & Reel	

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.

2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information

	10S
	YM 🚽
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10S = Product Type Marking Code YM = Date Code Marking Y = Year (ex: L = 2024)



Date Code Key

		0004	0005						0004			0004
Year	2023	2024	2025	2026	2027	2028	2029	2030	2031	2032	2033	2034
Code	K	L	М	N	Р	R	S	Т	U	V	W	Х
	1			-		1						
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec



Top View

U-DFN1610-2 (Type B)



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	Conditions
Peak Pulse Power Dissipation	Ppp	1750	W	8/20µs (Note 6)
Peak Pulse Current	IPP	100	A	8/20µs, per Figure 3
ESD Protection – Contact Discharge	Vesd_contact	±30	kV	Standard IEC 61000-4-2
ESD Protection – Air Discharge	Vesd_air	±30	kV	Standard IEC 61000-4-2

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 5)	PD	300	mW
Thermal Resistance, Junction to Ambient $T_A = +25^{\circ}C$	Reja	417	°C/W
Operating and Storage Temperature Range	TJ, TSTG	-55 to +150	°C

Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

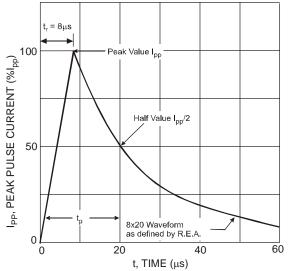
Characteristic	Symbol	Min	Тур	Max	Unit	Test Conditions
Reverse Working Voltage	Vrwm	_	_	10	V	—
Reverse Current	I _R	_	_	0.1	μA	$V_R = V_{RWM} = 10V$
Reverse Breakdown Voltage	V _{BR}	11.4	_	14.0	V	I _R = 1mA
Deverse Clamping Voltage (Note 6)		_	14.5	—	v	I _{PP} = 50A, t _P = 8/20µs
Reverse Clamping Voltage (Note 6)	Vc	_	17.5	—	v	IPP = 100A, tP = 8/20µs
Dynamic Resistance	Rdyn	—	0.05	—	Ω	Surge, t⊳ = 8/20µs
Junction Capacitance	CJ		270		pF	V _R = 0, f = 1MHz

Notes: 5. Device mounted on FR-4 PCB pad layout (2oz copper) as shown on Diodes Incorporated's suggested pad layout, which can be found on our website at http://www.diodes.com/package-outlines.html.

6. Clamping voltage value is based on an 8x20 $\!\mu s$ peak pulse current (I_PP) waveform.



D10V0S1US2LP1610



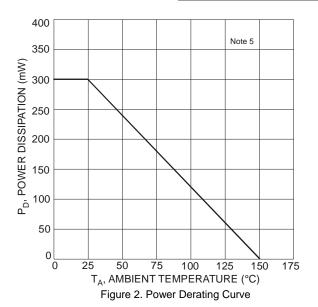
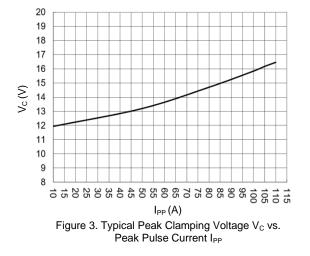


Figure 1. Typical 8x20µs Pulse Waveform

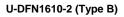


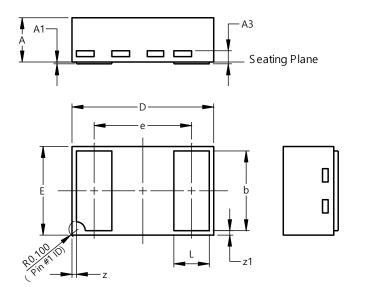
Note: 5. Device mounted on FR-4 PCB pad layout (2oz copper) as shown on Diodes Incorporated's suggested pad layout, which can be found on our website at http://www.diodes.com/package-outlines.html.



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.



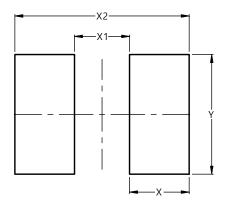


U-DFN1610-2 (Type B)							
Dim	Min	Max	Тур				
Α	0.45	0.55	0.50				
A1	0.00	0.05	0.015				
A3	-	-	0.127				
b	0.85	0.95	0.90				
D	1.55	1.65	1.60				
ш	0.95	1.05	1.00				
e	-	-	1.10				
L	0.35	0.45	0.40				
z	0.050 REF						
z1	(0.050 REF					
All C)imens	ions in	mm				

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

U-DFN1610-2 (Type B)



Dimensions	Value (in mm)
Х	0.650
X1	0.600
X2	1.900
Y	1.300



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