



4 CHANNEL LOW CAPACITANCE TVS DIODE ARRAY

Product Summary

V _{BR} (MIN)	IPP (MAX)	CT (TYP)
6V	5A	0.85pF

Features and Benefits

- IEC 61000-4-2 (ESD): Air ±25kV, Contact ±25kV
- 4 Channels of ESD Protection
- Low Channel Input Capacitance of 0.85pF Typical
- Typically Used at High Speed Ports such as USB 2.0 (Note 10), IEEE1394, Serial ATA, DVI, HDMI, PCI
- 4 Channels of ESD Protection
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- The D1213A-04TSQ range of parts are suitable for automotive applications requiring specific change control; these parts are AEC-Q101 qualified, PPAP capable, and manufactured in IATF 16949 certified

https://www.diodes.com/quality/product-definitions/

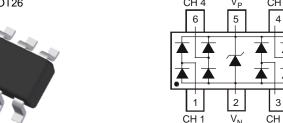
Description and Applications

This new generation TVS is designed to protect sensitive electronics from the damage due to ESD. The combination of small size and high ESD surge capability makes it ideal for use in portable applications such as:

- **USB Modules**
- HDMI[™] Ports
- **LVDs**

Mechanical Data

- Package: TSOT26
- Package Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish annealed over Copper Leadframe (Lead Free Plating) Solderable per MIL-STD-202, Method 208 @3
- Weight: 0.013 grams (Approximate)



Device Schematic

TSOT26



Top View

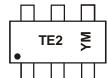
Ordering Information (Note 4)

Part Number	Dackers	Packing		
Part Number	Package	Qty. Car		
D1213A-04TSQ-7	TSOT26	3,000	Tape & Reel	

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + CI) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



TE2 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: K = 2023)

M = Month (ex: 8 = August)

Date Code Key

Year	20	23	2024	1	2025		2026		2027	20	028	2029
Code	ŀ	<	L		M		N		Р		R	S
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	Conditions
Operating Supply Voltage	VP - VN	6.0	V	-
DC Voltage at any Channel Input	=	$(V_N - 0.5)$ to $(V_P + 0.5)$	V	-
Peak Pulse Current	IPР	5	Α	8/20µs, Per Figure 3
ESD Protection – Contact Discharge	V _{ESD_Contact}	±25	kV	Standard IEC 61000-4-2
ESD Protection – Air Discharge	V_{ESD_Air}	±25	kV	Standard IEC 61000-4-2

Thermal Characteristics

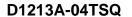
Characteristic	Symbol	Value	Unit
Power Dissipation (Note 5)	PD	300	mW
Thermal Resistance, Junction to Ambient (Note 5)	R _θ JA	417	°C/W
Operating and Storage Temperature Range	TJ, TSTG	-55 to +150	°C

Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

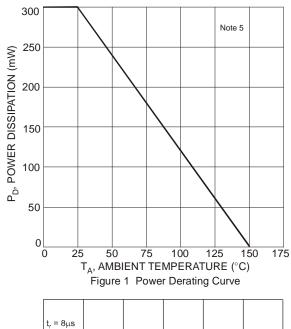
Characteristic	Symbol	Min	Тур	Max	Unit	Test Conditions
Operating Supply Voltage	VP	-	3.3	5.5	V	_
Operating Supply Current (Note 6)	lρ	-	-	8.0	μΑ	$(V_P - V_N) = 3.3V$
Channel Leakage Current (Note 6)	IR	-	0.1	1.0	μΑ	$V_P = 5V$, $V_N = 0V$
Reverse breakdown voltage	V _{BR}	6.0	-	-	V	I _R = 1mA
Clamping Voltage, Positive Transients	V _{CL1}	-	10.0	-	V	I _{PP} = 1A (Notes 7 & 8)
Clamping Voltage, Negative Transients	V _{CL2}	-	-1.7	-	V	IPP = -1A (Note 7 & 8)
Forward Voltage for Top Diode	V _{FD1}	0.60	0.80	0.95	V	I _F = 8mA, any channel to V _P
Forward Voltage for Bottom Diode	V _{FD2}	0.60	0.80	0.95	V	I _F = 8mA, V _N to and channel
Dynamic Resistance	RDYN	-	0.9	-	Ω	IPP = 1A (Notes 7 & 8)
Channel Input Capacitance	Ст	-	0.85	1.2	pF	$V_{IN} = 1.65V, V_P = 3.3V, V_N = 0V, f = 1MHz$

Notes:

- 5. Device mounted on FR-4 PCB pad layout (2oz copper) as shown on Diodes Incorporated's (Diodes). suggested pad layout, which can be found on our website at https://www.diodes.com/design/support/packaging/diodes-packaging/diodes-package-outlines-and-pad-layouts/.
- 6. Short duration pulse test used to minimize self-heating effect.
- 7. Clamping voltage value is based on an 8x20µs peak pulse current (Ipp) waveform.
- 8. Measured from any channel to Vn.9. Measured from Vp to Vn.
- 10. For information on the impact of Diodes' USB 2.0 compatible ESD protectors on signal integrity including eye diagram plots, please refer to AN77 at the following URL: https://www.diodes.com/assets/App-Note-Files/AN77.pdf







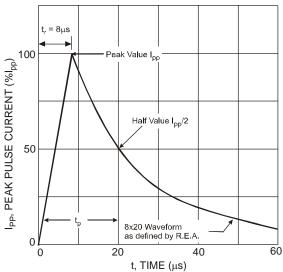
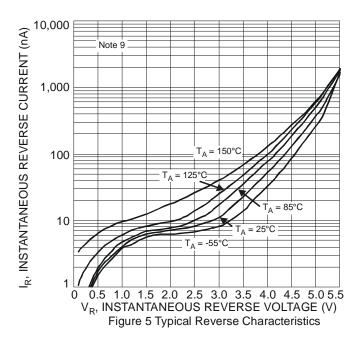


Figure 3 Pulse Waveform



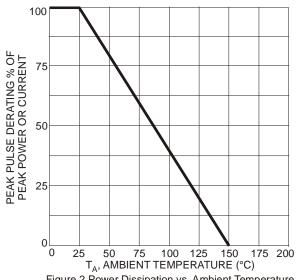
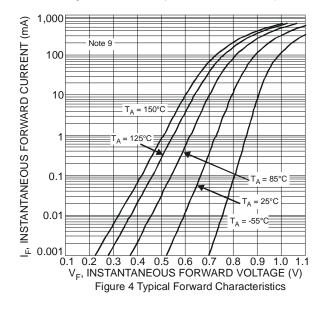


Figure 2 Power Dissipation vs. Ambient Temperature



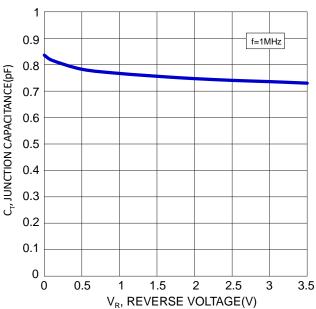
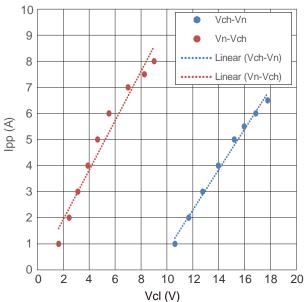


Figure 6 Typical Total Capacitance vs. Reverse Leakage



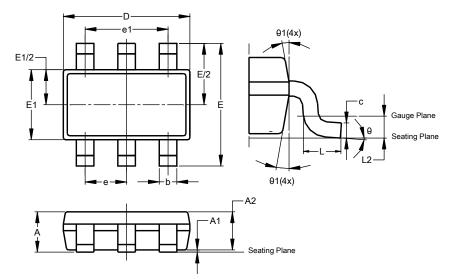


Vcl (V) Figure 7 Typical 8*20µs Clamping Voltage Performance



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

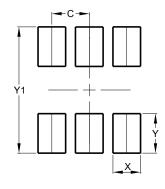


TSOT26							
Dim	Min	Max	Тур				
Α	1	1.00	-				
A 1	0.010	0.100	-				
A2	0.840	0.900	_				
D	2.800	3.000	2.900				
Е	2.800 BSC						
E1	1.500	1.700	1.600				
b	0.300	0.450	_				
O	0.120	0.200	-				
е	0.950 BSC						
e1	1.900 BSC						
L	0.30	0.50	_				
L2	0.250 BSC						
θ	0°	8°	4°				
θ1	4°	12°	_				
All Dimensions in mm							

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

TSOT26



Dimensions	Value (in mm)			
	111111)			
С	0.950			
X	0.700			
Y	1.000			
V1	3 200			



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