



HIGH-VOLTAGE SURFACE-MOUNT SWITCHING DIODE ARRAY

Features

- Two Series Diode Circuits Connect to Form Full Wave Bridge
- Fast Switching Speed: Maximum of 50ns
- High Reverse Breakdown Voltage Rating: 350V
- Low Reverse Current: Maximum of 100nA when $V_R = 240V$ at Room Temperature
- New Cutting-Edge Process Technology Used
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please <u>contact us</u> or your local Diodes representative. https://www.diodes.com/quality/product-definitions/)

Mechanical Data

- Package: SOT26
- Package Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020D
- Terminals: Matte Tin Plated Leads (Lead-Free Plating).
 Solderable per MIL-STD-202, Method 208 (23)
- Polarity: See Diagram
- Weight: 0.016 grams (Approximate)





Top View



Top View Internal Schematic

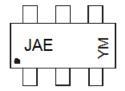
Ordering Information (Note 4)

| Part Number | Paakaga | Packing | | |
|----------------|---------|---------|-------------|--|
| Fait Number | Package | Qty. | Carrier | |
| DHVSD3004BRM-7 | SOT26 | 3,000 | Tape & Reel | |

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



JAE = Product Type Marking Code YM = Date Code Marking; A Bar on Top of the 'Y' Denotes Assembly & Test Site

Y = Year (ex: L = 2024) M = Month (ex: 9 = September)

Date Code Key

| Year | 2022 | 2023 | 2024 | 2025 | 2026 | 2027 | 2028 | 2029 | 2030 | 2031 | 2032 | 2033 |
|-------|------|------|------|------|------|------|------|------|------|------|------|------|
| Code | J | K | L | М | N | Р | R | S | Т | U | V | W |
| Month | Jan | Feb | Mar | Apr | May | Jun | Jul | Aug | Sep | Oct | Nov | Dec |
| Code | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | a | 0 | N | D |



Maximum Ratings (@TA = +25°C unless otherwise specified.)

| Characteristic | Symbol | Value | Unit | |
|---|---|-------|-------------------|---|
| Repetitive Peak Reverse Voltage | VRRM | 350 | V | |
| Working Peak Reverse Voltage DC Blocking Voltage | V _{RWM} V _R | 300 | V | |
| Forward Continuous Current (Note 5) | lF | 225 | mA | |
| Peak Repetitive Forward Current (Note 5) | I _{FRM} | 625 | mA | |
| Non-Repetitive Peak Forward Surge Current | @ t = 1.0µs @ t = 10ms @ t = 1.0s | IFSM | 4.0 1.0 0.5 | А |

Thermal Characteristics

| Characteristic | Symbol | Value | Unit |
|---|----------------|-------------|------|
| Power Dissipation (Note 5) | P _D | 350 | mW |
| Thermal Resistance Junction to Ambient Air (Note 5) | Reja | 357 | °C/W |
| Operating and Storage Temperature Range | TJ, TSTG | -55 to +150 | °C |

Electrical Characteristics (@TA = +25°C unless otherwise specified.)

| Characteristic | Symbol | Min | Тур | Max | Unit | Test Condition |
|------------------------------------|--------|-----|-----|------|------|--|
| Reverse Breakdown Voltage (Note 6) | V(BR)R | 350 | _ | _ | V | I _R = 150μA |
| | | _ | _ | 0.87 | | I _F = 20mA |
| Forward Voltage | VF | | — | 1.05 | V | IF = 100mA |
| | | _ | _ | 1.20 | | $I_F = 200 \text{mA}$ |
| Reverse Current (Note 6) | 1- | _ | _ | 100 | nA | V _R = 240V |
| Reverse Current (Note 6) | IR | _ | _ | 35 | μΑ | $V_R = 240V, T_J = +150$ °C |
| Total Capacitance | Ст | _ | _ | 5.0 | pF | V _R = 0V, f = 1.0MHz |
| Poverse Receivery Time | + | | | 50 | no | $I_F = I_R = 10mA$ |
| Reverse Recovery Time | trr | | | 30 | ns | $I_{rr} = 0.1 \text{ x } I_{R}, R_{L} = 100\Omega$ |

Notes

- 5. Part mounted on FR-4 board 1inch squared cu pad layout.
- 6. Short duration pulse test used to minimize self-heating effect.



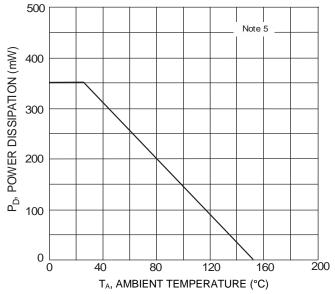


Figure 1. Power Derating Curve, Total Package

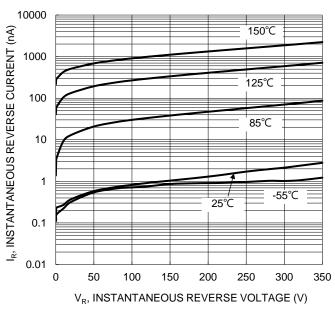


Figure 3. Typical Reverse Characteristics, Per Element

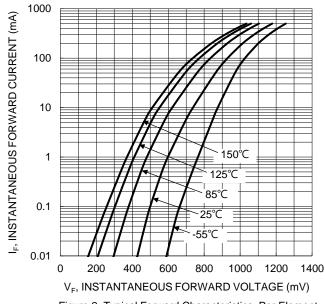


Figure 2. Typical Forward Characteristics, Per Element

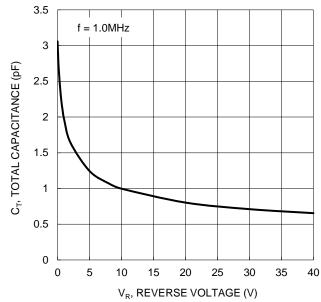


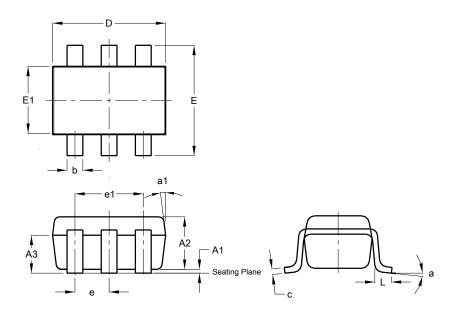
Figure 4. Typical Total Capacitance, Per Element



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT26

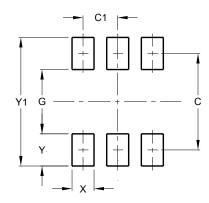


| SOT26 | | | | | | |
|----------------------|-------|------|------|--|--|--|
| Dim | Min | Max | Тур | | | |
| A1 | 0.013 | 0.10 | 0.05 | | | |
| A2 | 1.00 | 1.30 | 1.10 | | | |
| A3 | 0.70 | 0.80 | 0.75 | | | |
| b | 0.35 | 0.50 | 0.38 | | | |
| C | 0.10 | 0.20 | 0.15 | | | |
| D | 2.90 | 3.10 | 3.00 | | | |
| е | - | - | 0.95 | | | |
| e1 | - | - | 1.90 | | | |
| Е | 2.70 | 3.00 | 2.80 | | | |
| E1 | 1.50 | 1.70 | 1.60 | | | |
| ١ | 0.35 | 0.55 | 0.40 | | | |
| а | - | - | 8° | | | |
| a1 | - | - | 7° | | | |
| All Dimensions in mm | | | | | | |

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT26



| Dimensions | Value (in mm) |
|------------|---------------|
| С | 2.40 |
| C1 | 0.95 |
| G | 1.60 |
| Х | 0.55 |
| Y | 0.80 |
| Y1 | 3 20 |



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