



DUAL P-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{DSS}	Rds(on) max	ID MAX @TA = +25°C
-20V	0.75Ω @ V _{GS} = -4.5V	-0.63A
-20V	1.05Ω @ V _{GS} = -2.5V	-0.54A

Description

This new generation MOSFET is designed to minimize the on-state resistance (RDS(ON)) yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

- Battery operated systems and solid-state relays
- Drivers: relays, solenoids, lamps, hammers, displays, memories, transistors, etc.
- Power supply converter circuits

Features and Benefits

- Low On-Resistance
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Complementary Pair MOSFET
- Ultra-Small Surface Mount Package
- **ESD Protected**
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- The DIODES™ DMP2900UDWQ is suitable for automotive applications requiring specific change control; this part is AEC-Q101 qualified, PPAP capable, and manufactured in IATF 16949 certified facilities.

https://www.diodes.com/quality/product-definitions/

Mechanical Data

- Package: SOT363
- Package Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish Annealed over Alloy 42 Leadframe (Lead Free Plating). Solderable per MIL-STD-202, Method 208@3
- Terminal Connections: See Diagram
- Weight: 0.006 grams (Approximate)

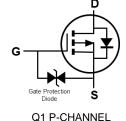


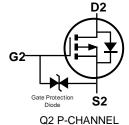


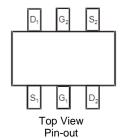
Top View



SOT363







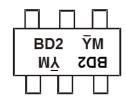
Ordering Information (Note 4)

Part Number	Package	Packing		
Fait Number	Fackage	Qty.	Carrier	
DMP2900UDWQ-7	SOT363	3,000	Tape & Reel	
DMP2900UDWQ-13	SOT363	10,000	Tape & Reel	

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



BD2 = Product Type Marking Code $\overline{Y}M$ = Date Code Marking \overline{Y} = Year Code (ex: J = 2022) M = Month Code (ex: 9 = September)

Date Code Key

Date Code Rey												
Year	2022	2023	2024	2025	2026	2027	2028	2029	2030	2031	2032	2033
Code	J	K	L	М	N	0	Р	R	S	Т	U	V
	1	1		1	1	1	1	l <u>.</u>	_			_
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec



Maximum Ratings (@ $T_A = +25$ °C, unless otherwise specified.)

Characteris	Symbol	Value	Unit		
Drain-Source Voltage	VDSS	-20	V		
Gate-Source Voltage	Vgss	±6	V		
Continuous Drain Current (Note 6) V _{GS} = -4.5V	lo	-0.63 -0.5	А		
Maximum Continuous Body Diode Forward Current	Is	-0.42	Α		
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%	6)		Ірм	-2.5	Α

Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

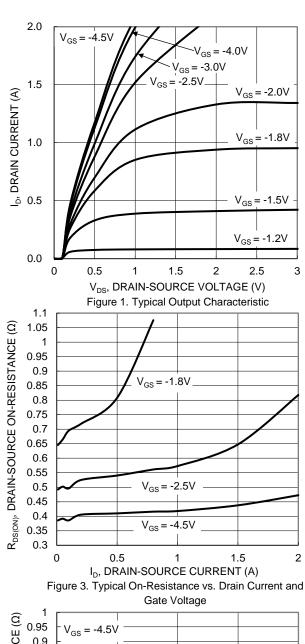
Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)	T _A = +25°C	PD	0.37	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	$R_{\theta JA}$	340	°C/W
Total Power Dissipation (Note 6)	T _A = +25°C	PD	0.46	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	Reja	272	°C/W
Operating and Storage Temperature Range		TJ, TSTG	-55 to +150	°C

Electrical Characteristics (T_A = +25°C, V_{BIAS} = 5V, V_{IN} = 1.05V, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)	•				•	
Drain-Source Breakdown Voltage	BV _{DSS}	-20	_	_	V	$V_{GS} = 0V, I_{D} = -250\mu A$
Zero Gate Voltage Drain Current @Tc = +25°C	IDSS	_	_	-100	nA	V _{DS} = -20V, V _{GS} = 0V
Gate-Source Leakage	Igss	_	_	±2.0	μΑ	$V_{GS} = \pm 4.5V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)	•				•	
Gate Threshold Voltage	V _{GS(TH)}	-0.5	_	-1.0	V	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$
			0.4	0.75		$V_{GS} = -4.5V, I_{D} = -430mA$
Static Drain-Source On-Resistance	RDS(ON)	_	0.53	1.05	Ω	$V_{GS} = -2.5V, I_{D} = -300mA$
			0.7	1.5		V _{GS} = -1.8V, I _D = -150mA
Diode Forward Voltage (Note 7)	V _{SD}	_	-0.7	-1.2	V	V _{GS} = 0V, I _S = -150mA
DYNAMIC CHARACTERISTICS (Note 8)						•
Input Capacitance	Ciss	_	49	_	pF	
Output Capacitance	Coss	_	12	_	pF	V _{DS} = -16V, V _{GS} = 0V, -f = 1.0MHz
Reverse Transfer Capacitance	C _{rss}	_	3.4	_	pF	-1 = 1.0WH1Z
Total Gate Charge	Qg	_	0.7	_	nC	
Gate-Source Charge	Qgs	_	0.1	_	nC	$V_{GS} = -4.5V$, $V_{DS} = -10V$, $I_{D} = -250$ mA
Gate-Drain Charge	Qgd	_	0.1	_	nC	- ID = -250IIIA
Turn-On Delay Time	t _D (ON)	1	16	_	ns	\/ 40\/ \/ 45\/
Turn-On Rise Time	t _R	_	15	_	ns	V _{DS} = -10V, V _{GS} = -4.5V,
Turn-Off Delay Time	tD(OFF)		213	_	ns	$R_g = 10\Omega$, $R_L = 47\Omega$
Turn-Off Fall Time	tF	_	89	_	ns	10 - 200HA

Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
Short duration pulse test used to minimize self-heating effect.
Guaranteed by design. Not subject to production testing.





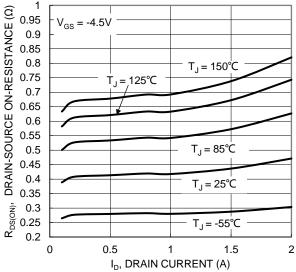


Figure 5. Typical On-Resistance vs. Drain Current and Junction Temperature

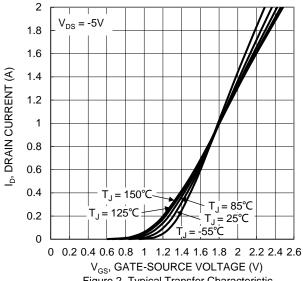
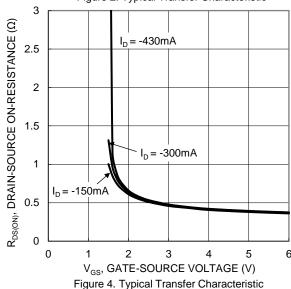


Figure 2. Typical Transfer Characteristic



1.8 R_{DS(ON)}, DRAIN-SOURCE ON-RESISTANCE (NORMALIZED) 1.6 $V_{GS} = -2.5V, I_{D} = -300mA$ 1.4 1.2 $V_{GS} = -1.8V, I_{D} = -150 \text{mA}$ 1 8.0 0.6 -50 -25 0 25 50 75 100 125 150 T_J, JUNCTION TEMPERATURE (°C)

Figure 6. On-Resistance Variation with Junction Temperature





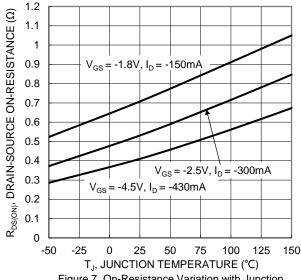
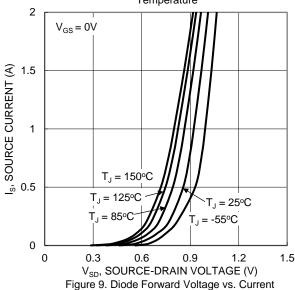


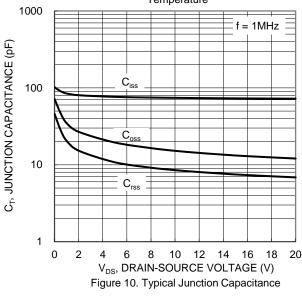
Figure 7. On-Resistance Variation with Junction Temperature

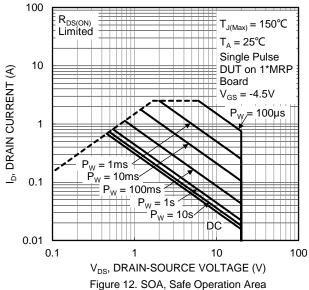


6 5 4 $V_{GS}(V)$ 3 $V_{DS} = -10V, I_{D} = -250mA$ 2 1 0 0 0.5 1.5 2 2.5 Q_g (nC) Figure 11. Gate Charge

1.1 $V_{\text{GS(TH)}},$ GATE THRESHOLD VOLTAGE (V) 1 0.9 $I_D = 1 \text{mA}$ 0.8 0.7 $I_D = 250 \mu A$ 0.6 0.5 0.4 0.3 0.2 100 125 -50 25 50 75 T_J, JUNCTION TEMPERATURE (°C)

Figure 8. Gate Threshold Variation vs. Junction Temperature







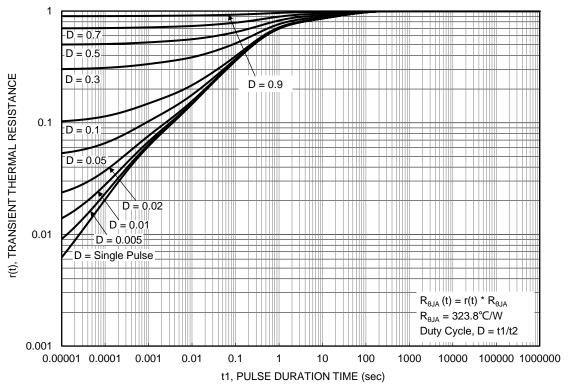


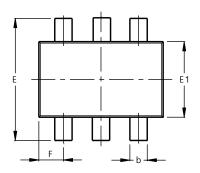
Figure 13. Transient Thermal Resistance

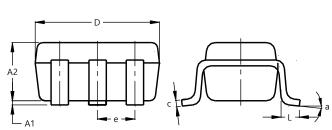


Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.





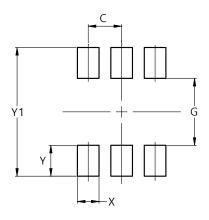


SOT363						
Dim	Min	Max	Тур			
A1	0.00	0.10	0.05			
A2	0.90	1.00	0.95			
b	0.10	0.30	0.25			
С	0.10	0.22	0.11			
D	1.80	2.20	2.15			
Е	2.00	2.20	2.10			
E1	1.15	1.35	1.30			
е	C	.650 B	SC			
F	0.40	0.45	0.425			
L	0.25	0.40	0.30			
а	0°	8°				
All I	Dimen	sions	in mm			

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT363



Dimensions	Value (in mm)
•	` '
С	0.650
G	1.300
Х	0.420
Υ	0.600
Y1	2.500



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