



DMP6050SPSW

60V P-CHANNEL ENHANCEMENT MODE MOSFET PowerDI5060-8

Product Summary

BV _{DSS}	Rds(on)	I _D Tc = +25°С	
-60V	50mΩ @ V _{GS} = -10V	-26A	
	$70 \mathrm{m}\Omega @ \mathrm{V}_{\mathrm{GS}} = -4.5 \mathrm{V}$	-22A	

Description and Applications

This new generation 60V P-channel enhancement mode MOSFET is designed to minimize $R_{DS(ON)}$ yet maintain superior switching performance. This device is ideal for use in notebook battery power managements and load switches.

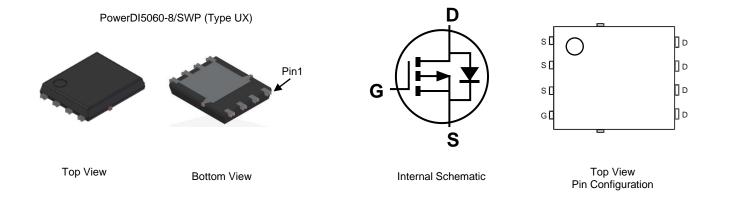
- Notebook battery power managements
- DC-DC converters
- Load switches

Features and Benefits

- Thermally Efficient Package-Cooler Running Applications
- High Conversion Efficiency
- Low RDS(ON) Minimizes On-State Losses
- Low Input Capacitance
- Fast Switching Speed
- <1.1mm Package Profile Ideal for Thin Applications
- Wettable Flank for Improved Optical Inspections
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please <u>contact us</u> or your local Diodes representative. <u>https://www.diodes.com/quality/product-definitions/</u>

Mechanical Data

- Package: PowerDI[®]5060-8
- Package Material: Molded Plastic, "Green" Molding Compound; UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Finish Matte Tin Annealed over Copper Leadframe; Solderable per MIL-STD-202, Method 208 🕲
- Weight: 0.097 grams (Approximate)



Ordering Information (Note 4)

Ordershie Port Number	Paakaga		cking	
Orderable Part Number	Package	Qty.	Carrier	
DMP6050SPSW-13	PowerDI5060-8/SWP (Type UX)	2500	Tape & Reel	

Notes: 1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.

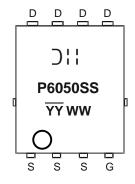
2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.



Marking Information



 \bigcirc **! !** = Manufacturer's Marking P6050SS = Product Type Marking Code $\overleftarrow{YY}WW$ = Date Code Marking \overleftarrow{YY} = Last Two Digits of Year (ex: 24 = 2024) WW = Week Code (01 to 53)

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			Vdss	-60	V
Gate-Source Voltage			Vgss	±20	V
Continuous Drain Current (Note 6) V_{GS} = -10V	Steady State	T _A = +25°C T _A = +70°C	ID	-5.7 -4.5	А
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)			I _{DM}	-45	A
Maximum Continuous Body Diode Forward Current (Note 6)			ls	-2.4	A
Pulsed Body Diode Forward Current (10µs Pulse, Duty Cycle = 1%)			lsм	-45	A
Avalanche Current (Note 8) L = 0.1mH			I _{AS}	-25	A
Repetitive Avalanche Energy (Note 8) L = 0.1mH		Eas	32	mJ	

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 5)	PD	1.3	W
Thermal Resistance, Junction to Ambient @ $T_A = +25$ °C (Note 5)	Reja	95	°C/W
Power Dissipation (Note 6)	PD	2.4	W
Thermal Resistance, Junction to Ambient @ $T_A = +25$ °C (Note 6)	R _{0JA}	52	°C/W
Thermal Resistance, Junction to Case @ $T_C = +25^{\circ}C$ (Note 7)	Rejc	2.4	°C/W
Operating and Storage Temperature Range	TJ, TSTG	-55 to +150	°C

 Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
Thermal resistance from junction to soldering point (on the exposed drain pad). Notes:

8. I_{AS} and E_{AS} ratings are based on low frequency and duty cycles to keep T_J = +25°C.



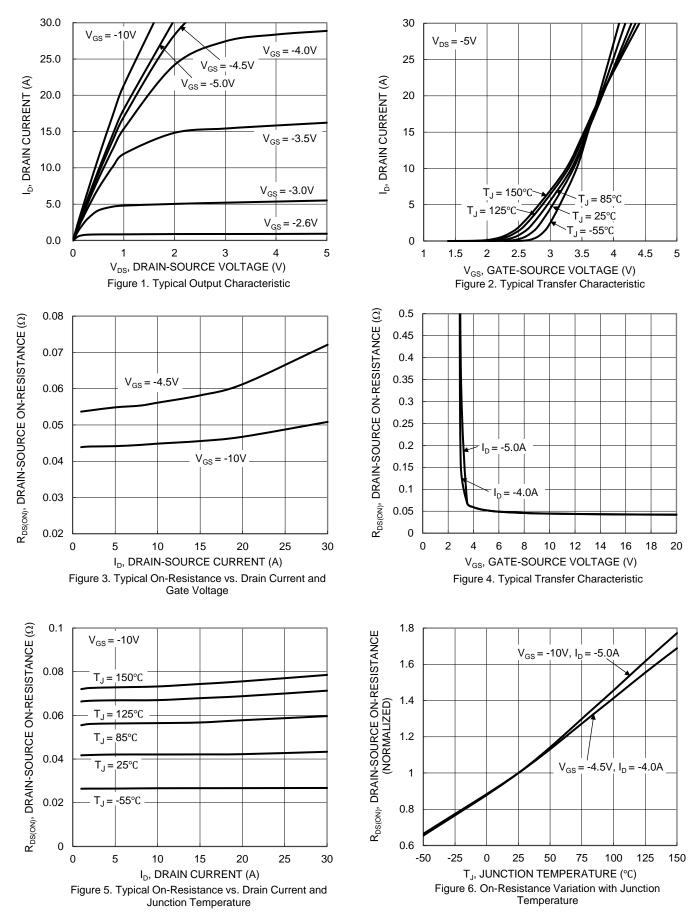
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

			_				
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 9)		1	1	1	1		
Drain-Source Breakdown Voltage	BVDSS	-60	—	—	V	$V_{GS} = 0V, I_{D} = -250\mu A$	
Zero Gate Voltage Drain Current	IDSS	—	—	-1	μA	$V_{DS} = -60V$, $V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}	_	_	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 9)							
Gate Threshold Voltage	Vgs(th)	-1.0	_	-3.0	V	$V_{DS} = V_{GS}$, $I_D = -250 \mu A$	
Static Drain-Source On-Resistance	Proven	_	43	50	mΩ	$V_{GS} = -10V, I_D = -5A$	
	R _{DS(ON)}		53	70	11152	$V_{GS} = -4.5V, I_D = -4A$	
Diode Forward Voltage	Vsd	_	-0.7	-1.2	V	Vgs = 0V, Is = -1A	
DYNAMIC CHARACTERISTICS (Note 10)	·					·	
Input Capacitance	Ciss	—	2163	—	pF	V _{DS} = -30V, V _{GS} = 0V, - f = 1.0MHz	
Output Capacitance	Coss	_	88	_	pF		
Reverse Transfer Capacitance	Crss	_	58	_	pF		
Gate Resistance	Rg	_	13	_	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1MHz$	
Total Gate Charge (V _{GS} = -10V)	Qg	_	30	_	nC		
Total Gate Charge (V _{GS} = -4.5V)	Qg		14	_	nC		
Gate-Source Charge	Qgs		5	_	nC	$V_{DS} = -30V, I_{D} = -5A$	
Gate-Drain Charge	Q _{gd}		4.6	_	nC	7	
Turn-On Delay Time	t _{D(ON)}	_	4.7		ns	$V_{GS} = -10V, V_{DS} = -30V,$ $R_G = 3\Omega, I_D = -5A$	
Turn-On Rise Time	tR		2.7		ns		
Turn-Off Delay Time	tD(OFF)		73		ns		
Turn-Off Fall Time	tF		25		ns		
Body Diode Reverse-Recovery Time	t _{RR}		18	_	ns	I _F = -5A, di/dt = 100A/µs	
Body Diode Reverse-Recovery Charge	Qrr	_	12	_	nC	I _F = -5A, di/dt = 100A/µs	

 Short duration pulse test used to minimize self-heating effect.
Guaranteed by design. Not subject to product testing. Notes:



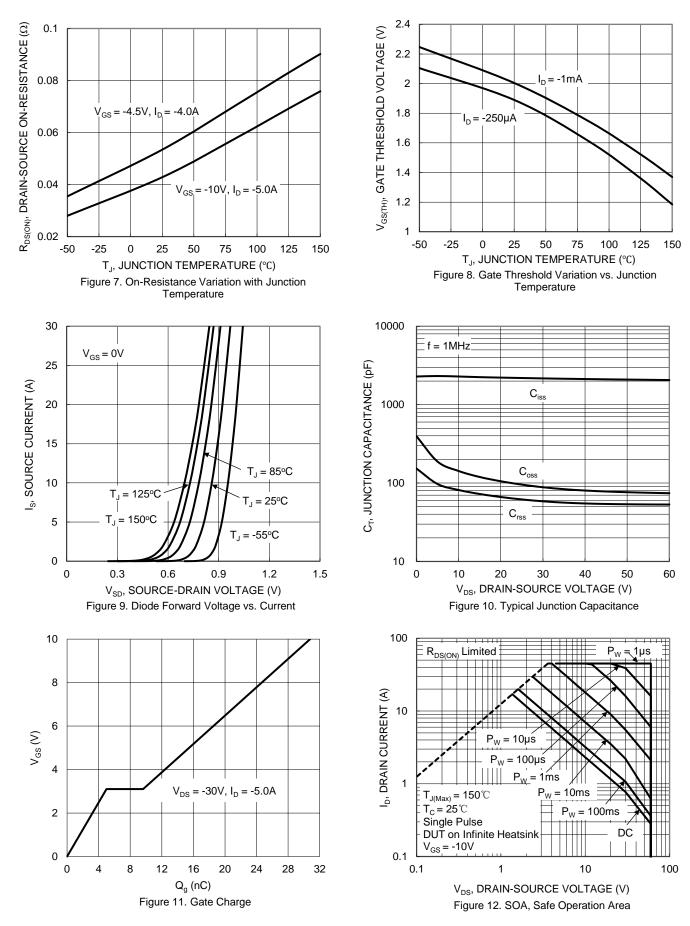
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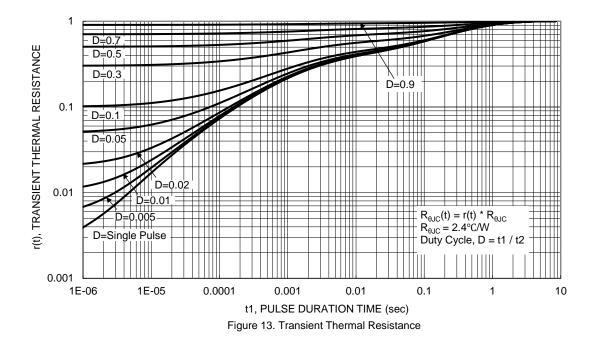


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PowerDI5060-8/SWP

(Type UX)

Max

1.10

0.05

0.50

0.35

0.25REF

5.15 BS

5.10

3.96

4.18

6.00

3.86

4.595

1.27BSC

0.835

0.835

0.400

0.225

4.005

12°

8°

All Dimensions in mm

6.40 BS

0.230 0.330

Тур

1.00

0.41

0.25

0.277

4.90

3.76

3.98

5.80

3.66

4.395

0.735

0.735

0.300

0.125

3.605

11°

7°

Min

0.90

0

0.30

0.20

4.70

3.56

3.78

5.60

3.46

4.195

1.05

0.635

0.635

0.200

0.025

3.205

10°

6°

Dim

А

A1

b

b2

b4

С

D

D1

D2

D2a

Ε

E1

E2

E2a

е

k

L

La

L1

L4

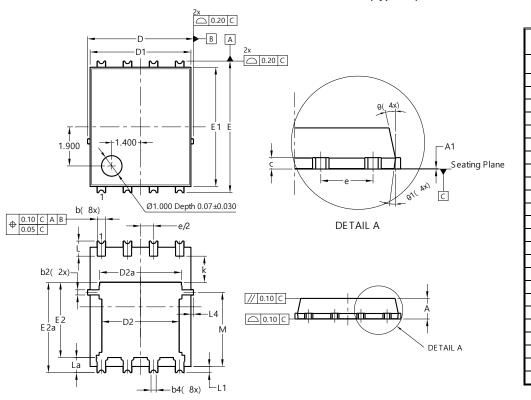
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Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

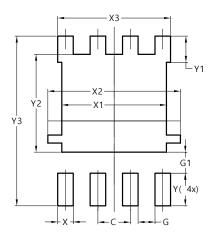


PowerDI5060-8/SWP (Type UX)

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI5060-8/SWP (Type UX)



Dimensions	Value (in mm)		
С	1.270		
G	0.660		
G1	0.820		
Х	0.610		
X1	4.100		
X2	5.190		
X3	4.420		
Y	1.270		
Y1	1.020		
Y2	3.810		
Y3	6.610		



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