



### 60V 175°C P-CHANNEL ENHANCEMENT MODE MOSFET PowerDI3333-8

### **Product Summary**

BV <sub>DSS</sub>	R <sub>DS(ON)</sub> Max	I <sub>D</sub> Max T <sub>C</sub> = +25°C
	60mΩ @ V <sub>GS</sub> = -10V	-20.6A
-60V	80mΩ @ V <sub>G</sub> S = -4.5V	-18.5A

### **Description and Applications**

This MOSFET is designed to minimize the on-state resistance (RDS(ON)) yet maintain superior switching performance, making it ideal for high-efficiency power-management applications.

- Motor controls
- Power-management functions
- DC-DC converters

### **Features and Benefits**

- Rated to +175°C Ideal for High Ambient Temperature Environments
- 100% Unclamped Inductive Switch (UIS) Test in Production
- Low R<sub>DS(ON)</sub> Ensures Minimal On-State Losses
- Wettable Flank for Improved Optical Inspection
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e.: parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please refer to the related automotive grade (Q-suffix) part. A listing can be found at

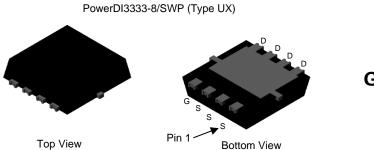
https://www.diodes.com/products/automotive/automotive-products/.

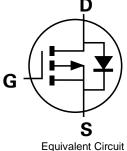
 This part is qualified to JEDEC standards (as references in AEC-Q) for High Reliability.

https://www.diodes.com/quality/product-definitions/

#### **Mechanical Data**

- Package: PowerDI<sup>®</sup>3333-8
- Package Material: Molded Plastic, "Green" Molding Compound.
   UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections Indicator: See Diagram
- Terminals: Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.072 grams (Approximate)





## **Ordering Information** (Note 4)

Orderable Part Number	Dookogo	Packing		
Orderable Part Number	Package	Qty.	Carrier	
DMPH6051SFVW-7	PowerDI3333-8/SWP (Type UX)	2000	Tape & Reel	
DMPH6051SFVW-13	PowerDI3333-8/SWP (Type UX)	3000	Tape & Reel	

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

### **Marking Information**



651 = Product Type Marking Code

YYWW = Date Code Marking

YY = Last Two Digits of Year (ex: 24 = 2024)

WW = Week Code (01 to 53)



### **Maximum Ratings** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Drain-Source Voltage	VDSS	-60	V	
Gate-Source Voltage	Vgss	±20	V	
Continuous Drain Current (Note 5) VGS = -10V $ T_C = +25^{\circ}C $ $ T_C = +100^{\circ}C $		ID	-20.6 -14.6	А
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)	I <sub>DM</sub>	-83	Α	
Maximum Continuous Body Diode Forward Current (Note 5)	Is	-20.6	Α	
Pulsed Source Current (10µs Pulse, Duty Cycle = 1%)	lsм	-83	Α	
Avalanche Current, L = 0.1mH	I <sub>AS</sub>	-27	Α	
Avalanche Energy, L = 0.1mH	E <sub>AS</sub>	36	mJ	

# Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 6)	PD	3.2	W
Thermal Resistance, Junction to Ambient (Note 6)		47	°C/W
Total Power Dissipation (Note 5)	PD	51	W
Thermal Resistance, Junction to Case (Note 5)	Rejc	2.9	°C/W
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +175	°C

# **Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	-60	_	-	V	$V_{GS} = 0V, I_D = -250\mu A$	
Zero Gate Voltage Drain Current	IDSS	_	_	-1	μA	V <sub>DS</sub> = -60V, V <sub>GS</sub> = 0V	
Gate-Source Leakage	Igss	_	_	±100	nA	$V_{GS} = \pm 20V$ , $V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	Vgs(TH)	-1	_	-3	V	$V_{DS} = V_{GS}$ , $I_D = -250\mu A$	
Static Drain-Source On-Resistance	Dagger	_	48	60	mΩ	V <sub>G</sub> S = -10V, I <sub>D</sub> = -7A	
Static Drain-Source On-Resistance	RDS(ON)	_	62	80	11122	Vgs = -4.5V, ID = -7A	
Diode Forward Voltage	V <sub>SD</sub>	_	-0.8	-1.2	V	$V_{GS} = 0V$ , $I_{S} = -1A$	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	Ciss	_	2087		pF	.,	
Output Capacitance	Coss	_	94	_	pF	V <sub>DS</sub> = -30V, V <sub>GS</sub> = 0V f = 1MHz	
Reverse Transfer Capacitance	Crss	_	78	1	pF	1 – 1101112	
Gate Resistance	Rg	_	3.5	1	Ω	$V_{DS} = 0V$ , $V_{GS} = 0V$ , $f = 1MHz$	
Total Gate Charge (V <sub>GS</sub> = -4.5V)	Qg	_	17	_	nC		
Total Gate Charge (V <sub>GS</sub> = -10V)	Qg	_	36		nC	V <sub>DS</sub> = -30V. In = -5A	
Gate-Source Charge	Qgs	_	3.7	1	nC	VDS = -30V, ID = -3A	
Gate-Drain Charge	Q <sub>gd</sub>	_	5.6	1	nC	1	
Turn-On Delay Time	tD(ON)	_	6.5	_	ns		
Turn-On Rise Time	t <sub>R</sub>	_	19	_	ns	$V_{DD}$ = -30V, $V_{GS}$ = -10V $R_g$ = 3 $\Omega$ , $I_D$ = -5A	
Turn-Off Delay Time	tD(OFF)	_	40	_	ns		
Turn-Off Fall Time	tF		24		ns	]	
Body Diode Reverse Recovery Time	trr	_	23	_	ns	L 54 4544 40046	
Body Diode Reverse Recovery Charge	Qrr	_	21		nC	I <sub>F</sub> = -5A, di/dt = 100A/μs	

Notes:

<sup>5.</sup> Thermal resistance from junction to soldering point (on the exposed drain pad).6. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate.

<sup>7.</sup> Short duration pulse test used to minimize self-heating effect.

<sup>8.</sup> Guaranteed by design. Not subject to product testing.



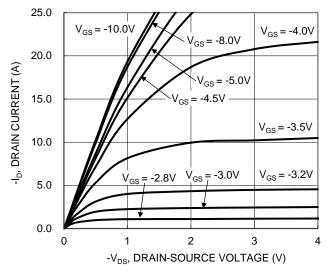


Figure 1. Typical Output Characteristic

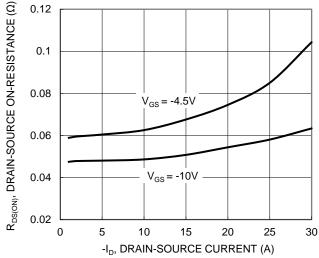


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

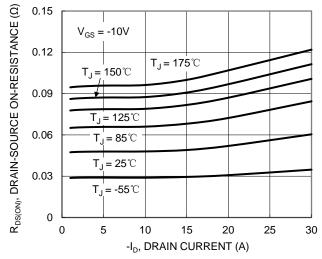


Figure 5. Typical On-Resistance vs. Drain Current and Junction Temperature

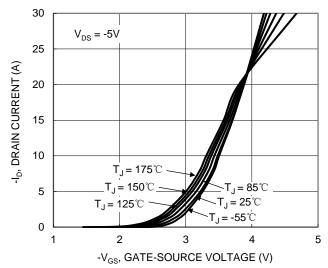


Figure 2. Typical Transfer Characteristic

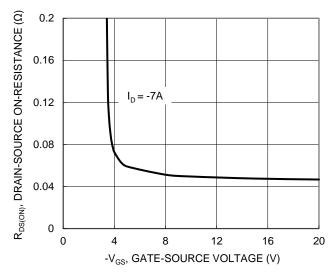


Figure 4. Typical Transfer Characteristic

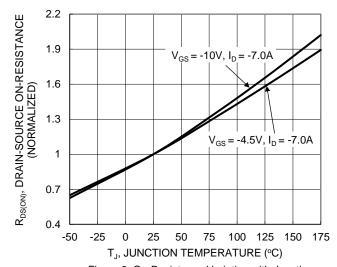


Figure 6. On-Resistance Variation with Junction Temperature



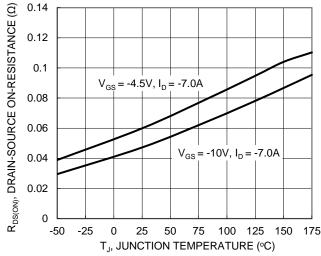


Figure 7. On-Resistance Variation with Junction Temperature

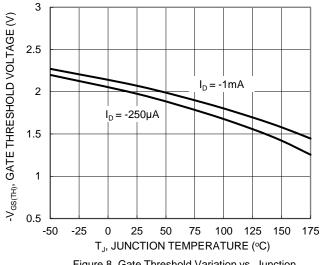


Figure 8. Gate Threshold Variation vs. Junction Temperature

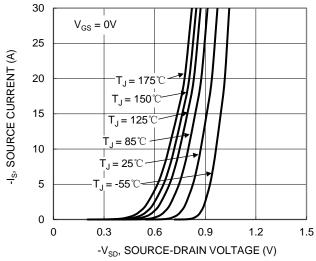
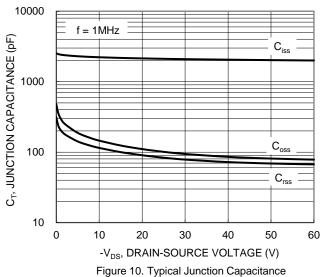


Figure 9. Diode Forward Voltage vs. Current



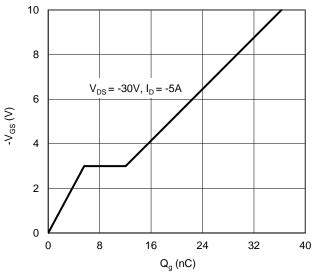


Figure 11. Gate Charge

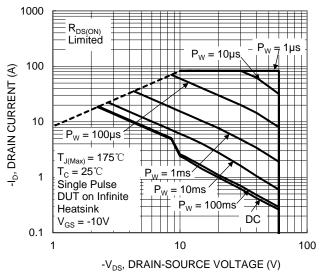


Figure 12. SOA, Safe Operation Area



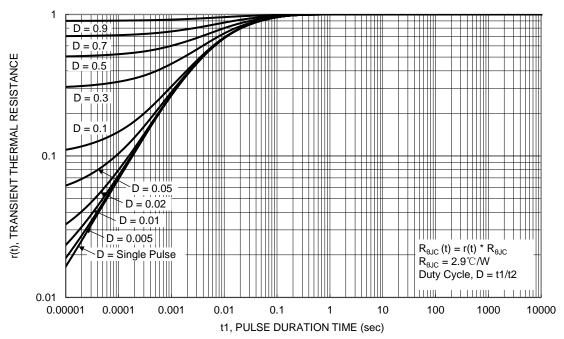


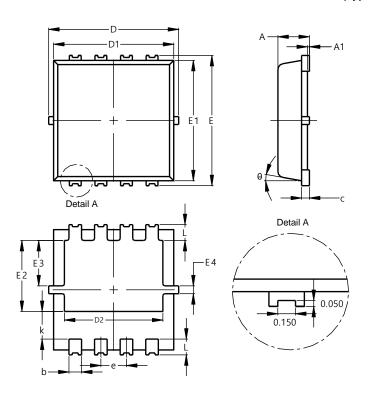
Figure 13. Transient Thermal Resistance



## **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

### PowerDI3333-8/SWP (Type UX)

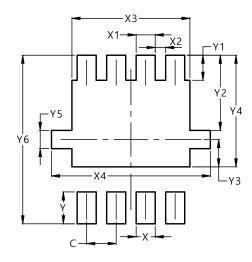


PowerDI3333-8/SWP					
(Type UX)					
Dim	Min	Max	Тур		
Α	0.75	0.85	0.80		
A1	0.00	0.05			
b	0.25	0.40	0.32		
C	0.10	0.25	0.15		
D	3.20	3.40	3.30		
D1	2.95	3.15	3.05		
D2	2.30	2.70	2.50		
Е	3.20	3.40	3.30		
E1	2.95	3.15	3.05		
E2	1.60	2.00	1.80		
E3	0.95	1.35	1.15		
E4	0.10	0.30	0.20		
е	_	_	0.65		
k	0.50	0.90	0.70		
L	0.30	0.50	0.40		
θ	0°	12°	10°		
All Dimensions in mm					

# **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.

#### PowerDI3333-8/SWP (Type UX)



Dimensions	Value (in mm)
C	0.650
X	0.420
X1	0.420
X2	0.230
Х3	2.600
X4	3.500
Υ	0.700
Y1	0.550
Y2	1.650
Y3	0.600
Y4	2.450
Y5	0.400
Y6	3.700



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