

60V +175°C P-CHANNEL ENHANCEMENT MODE MOSFET PowerDI3333-8

#### **Product Summary**

BV <sub>DSS</sub>	Rds(on) Max	I <sub>D</sub> Max Tc = +25°C
60)/	60mΩ @ V <sub>GS</sub> = -10V	-20.6A
-60V	80mΩ @ V <sub>GS</sub> = -4.5V	-18.5A

# **Description and Applications**

This MOSFET is designed to meet the stringent requirements of automotive applications. It is qualified to AEC-Q101, supported by a PPAP, and is ideal for use in:

- Motor controls
- Power-management functions
- DC-DC converters

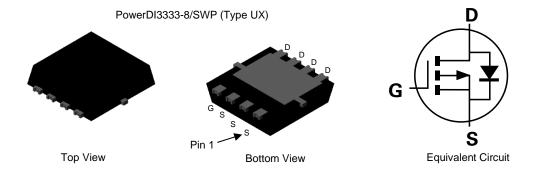
#### **Features and Benefits**

- Rated to +175°C—Ideal for High Ambient Temperature Environments
- 100% Unclamped Inductive Switch (UIS) Test in Production
- Low RDS(ON)—Ensures Minimal On-State Losses
- Wettable Flank for Improved Optical Inspection
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- The DMPH6051SFVWQ is suitable for automotive applications requiring specific change control; this part is AEC-Q101 qualified, PPAP capable, and manufactured in IATF 16949 certified facilities.

https://www.diodes.com/quality/product-definitions/

#### **Mechanical Data**

- Package: PowerDI<sup>®</sup>3333-8
- Package Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections Indicator: See Diagram
- Terminals: Finish—Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (63)
- Weight: 0.072 grams (Approximate)



## Ordering Information (Note 4)

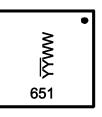
Dort Number	Bookage	Packing		
Part Number	Package	Qty.	Carrier	
DMPH6051SFVWQ-7	PowerDI3333-8/SWP (Type UX)	2000	Tape & Reel	
DMPH6051SFVWQ-13	PowerDI3333-8/SWP (Type UX)	3000	Tape & Reel	

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

## **Marking Information**



 $\begin{array}{l} \displaystyle \underline{651} = \mbox{Product Type Marking Code} \\ \displaystyle \underline{YY}WW = \mbox{Date Code Marking} \\ \displaystyle \underline{YY} = \mbox{Last Two Digits of Year (ex: 24 = 2024)} \\ \displaystyle WW = \mbox{Week Code (01 to 53)} \end{array}$ 

Lead-free.



### Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Drain-Source Voltage	Vdss	-60	V	
Gate-Source Voltage		Vgss	±20	V
Continuous Drain Current (Note 5) VGS = -10V	Tc = +25°C Tc = +100°C	lo	-20.6 -14.6	A
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)	IDM	-83	А	
Maximum Continuous Body Diode Forward Current (Note 5)	ls	-20.6	А	
Pulsed Source Current (10µs Pulse, Duty Cycle = 1%)	lsм	-83	А	
Avalanche Current, L = 0.1mH		I <sub>AS</sub>	-27	A
Avalanche Energy, L = 0.1mH		E <sub>AS</sub>	36	mJ

## **Thermal Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 6)	PD	3.2	W
Thermal Resistance, Junction to Ambient (Note 6)	Reja	47	°C/W
Total Power Dissipation (Note 5)		51	W
Thermal Resistance, Junction to Case (Note 5)		2.9	°C/W
Operating and Storage Temperature Range		-55 to +175	°C

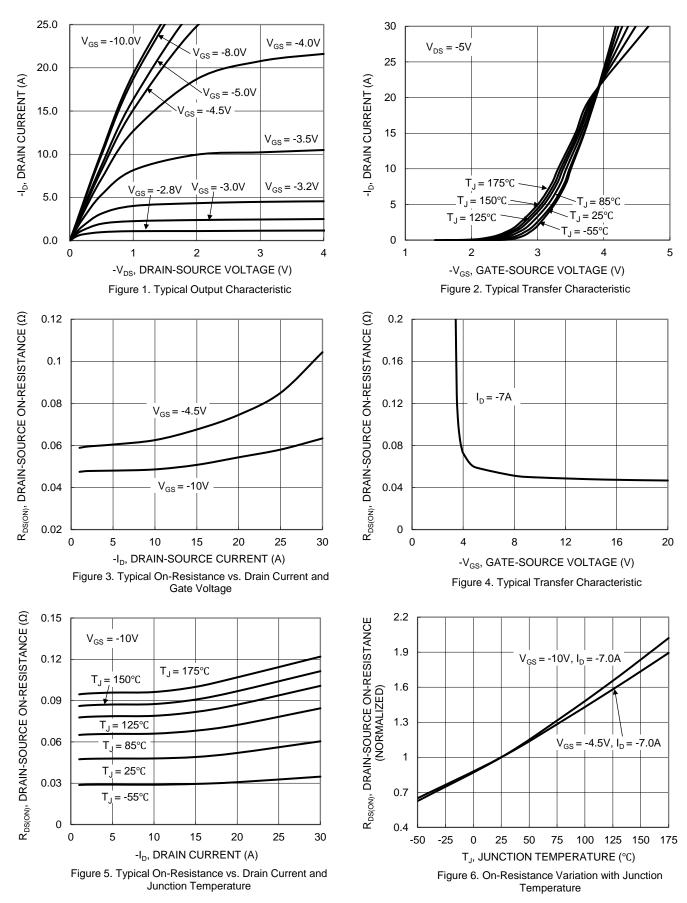
#### Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)		•		•	•	•	
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	-60	_	_	V	V <sub>GS</sub> = 0V, I <sub>D</sub> = -250µA	
Zero Gate Voltage Drain Current	IDSS	_	_	-1	μA	$V_{DS} = -60V, V_{GS} = 0V$	
Gate-Source Leakage	lgss	_	_	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)	<u>.</u>						
Gate Threshold Voltage	Vgs(th)	-1	—	-3	V	$V_{DS} = V_{GS}$ , $I_D = -250 \mu A$	
Static Drain-Source On-Resistance	Deserve	_	48	60	mΩ	V <sub>GS</sub> = -10V, I <sub>D</sub> = -7A	
Static Drain-Source On-Resistance	Rds(on)	_	62	80	11122	$V_{GS} = -4.5V, I_D = -7A$	
Diode Forward Voltage	V <sub>SD</sub>	_	-0.8	-1.2	V	$V_{GS} = 0V, I_{S} = -1A$	
DYNAMIC CHARACTERISTICS (Note 8)	·						
Input Capacitance	Ciss	_	2087	_	pF		
Output Capacitance	Coss	—	94	_	pF	V <sub>DS</sub> = -30V, V <sub>GS</sub> = 0V f = 1MHz	
Reverse Transfer Capacitance	Crss	_	78	_	pF		
Gate Resistance	Rg	—	3.5	—	Ω	$V_{DS} = 0V$ , $V_{GS} = 0V$ , $f = 1MHz$	
Total Gate Charge (V <sub>GS</sub> = -4.5V)	Qg	_	17	_	nC		
Total Gate Charge (V <sub>GS</sub> = -10V)	Qg	_	36	_	nC		
Gate-Source Charge	Qgs	—	3.7	—	nC	V <sub>DS</sub> = -30V, I <sub>D</sub> = -5A	
Gate-Drain Charge	Q <sub>gd</sub>	_	5.6	_	nC	7	
Turn-On Delay Time	t <sub>D(ON)</sub>	_	6.5	_	ns		
Turn-On Rise Time	tR	_	19	_	ns	V <sub>DD</sub> = -30V, V <sub>GS</sub> = -10V	
Turn-Off Delay Time	tD(OFF)	—	40	_	ns	$R_g = 3\Omega$ , $I_D = -5A$	
Turn-Off Fall Time	tF	—	24	_	ns	1	
Body Diode Reverse-Recovery Time	trr	_	23	_	ns		
Body Diode Reverse-Recovery Charge	Qrr	_	21	_	nC	— I <sub>F</sub> = -5A, di/dt = 100A/μs	

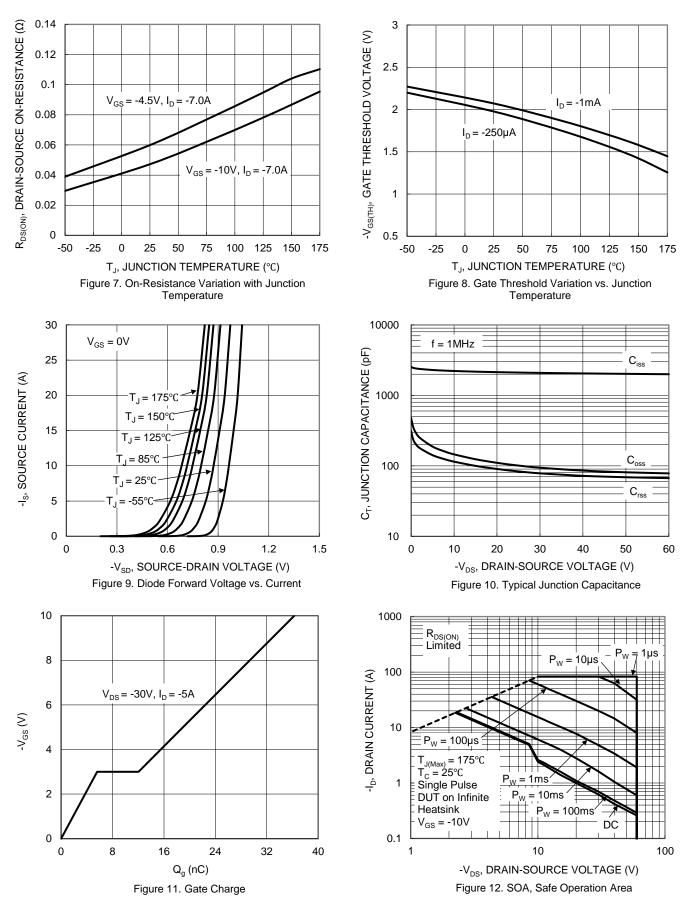
Notes:

Thermal resistance from junction to soldering point (on the exposed drain pad).
Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate.
Short duration pulse test used to minimize self-heating effect.
Guaranteed by design. Not subject to product testing.

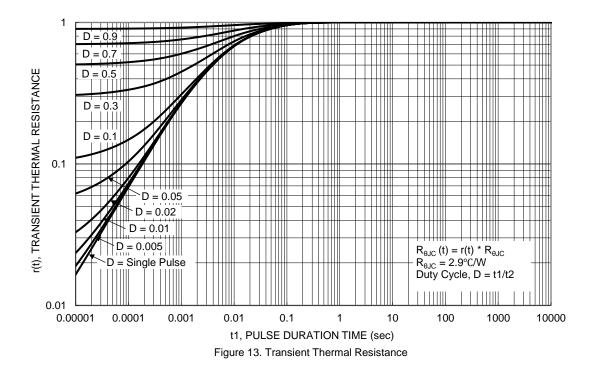








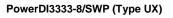


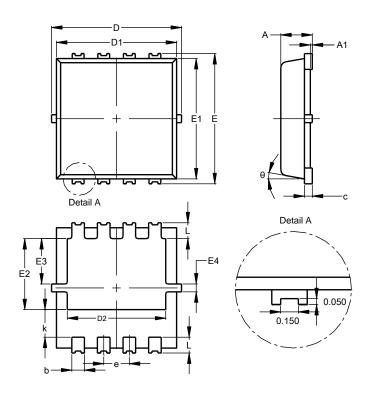




### **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.



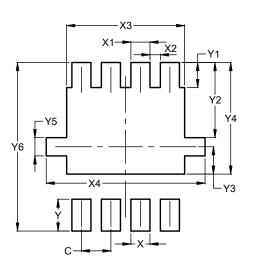


Po	PowerDI3333-8/SWP				
(Type UX)					
Dim	Min	Max	Тур		
Α	0.75	0.85	0.80		
A1	0.00	0.05			
b	0.25	0.40	0.32		
С	0.10	0.25	0.15		
D	3.20	3.40	3.30		
D1	2.95	3.15	3.05		
D2	2.30	2.70	2.50		
Е	3.20	3.40	3.30		
E1	2.95	3.15	3.05		
E2	1.60	2.00	1.80		
E3	0.95	1.35	1.15		
E4	0.10	0.30	0.20		
е	_	-	0.65		
k	0.50	0.90	0.70		
L	0.30	0.50	0.40		
θ	0°	12°	10°		
All Dimensions in mm					

# **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.

#### PowerDI3333-8/SWP (Type UX)



Dimensions	Value (in mm)		
С	0.650		
Х	0.420		
X1	0.420		
X2	0.230		
X3	2.600		
X4	3.500		
Y	0.700		
Y1	0.550		
Y2	1.650		
Y3	0.600		
Y4	2.450		
Y5	0.400		
Y6	3.700		



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