



DUAL 30V N-CHANNEL ENHANCEMENT MODE MOSFET PowerDI3333-8

Product Summary

Device	BV _{DSS}	R _{DS(ON)} Max	I _D Max T _C = +25°C (Note 5)
		11mΩ @ V _G S = 10V	17.5A
Q1 & Q2 30V	17.5mΩ @ V _{GS} = 4.5V	14A	
		25mΩ @ V _{GS} = 3.8V	12A

Description

This new generation MOSFET is designed to minimize the on-state resistance (R_{DS(ON)}) yet maintain superior switching performance, making it ideal for high-efficiency power-management applications.

Applications

- Power-management functions
- Analog switches

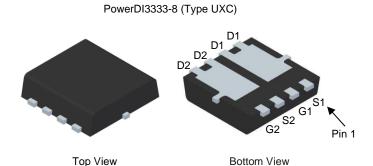
Features

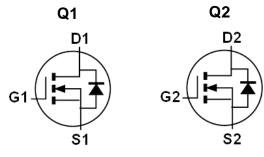
- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please contact us or your local Diodes representative.

https://www.diodes.com/quality/product-definitions/

Mechanical Data

- Package: PowerDI[®]3333-8
- Package Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections Indicator: See Diagram
- Terminals: Finish—Matte Tin Annealed over Copper Leadframe.
 Solderable per MIL-STD-202, Method 208 @3
- Weight: 0.072 grams (Approximate)





Equivalent Circuit

Ordering Information (Note 4)

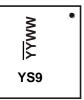
Part Number	Paskage	Packing		
Fait Nullibei	Package	Qty.	Carrier	
DMT3009LDV-7	PowerDI3333-8 (Type UXC)	2000	Tape & Reel	
DMT3009LDV-13	PowerDI3333-8 (Type UXC)	3000	Tape & Reel	

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/
- Package limited.



Marking Information



YS9 = Product Type Marking Code
YYWW = Date Code Marking
YY = Last Two Digits of Year (ex: 24 for 2024)
WW = Week Code (01 to 53)

Maximum Ratings (@TA = +25°C, unless otherwise specified.)

Characteristic			Symbol	Q1 & Q2	Unit
Drain-Source Voltage			VDSS	30	V
Gate-Source Voltage			V _{GSS}	+20/ -16	V
Continuous Drain Current (Note 6) $V_{GS} = 10V$ Steady State $T_{C} = +25^{\circ}C$ (Note 5) $T_{C} = +70^{\circ}C$			ΙD	17.5 15	Α
Maximum Body Diode Forward Current (Note 6)		ls	0.8	Α	
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)			Ірм	70	А
Pulsed Body Diode Forward Current (10µs Pulse, Duty	lsм	70	Α		
Avalanche Current (Note 7) L = 0.1mH			I _{AS}	20	Α
Avalanche Energy (Note 7) L = 0.1mH			Eas	20	mJ

Thermal Characteristics

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 8)	T _A = +25°C	PD	0.9	W
Thermal Resistance, Junction to Ambient (Note 8)	Steady State	R _{0JA}	133	°C/W
Total Power Dissipation (Note 6)	T _A = +25°C	P _D	2.4	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	Reja	51	°C/W
Thermal Resistance, Junction to Case (Note 6)		Rejc	5	-C/VV
Operating and Storage Temperature Range		T _J , T _{STG}	-55 to +150	°C

Notes: 5. Package limited.

6. Device mounted on FR-4 substrate PCB, 2oz copper, with 1-inch square copper plate.
7. UIS in production with L = 0.1mH, starting T_A = +25°C.
8. Device mounted on FR-4 substrate PCB, 2oz copper, with minimum recommended pad layout.



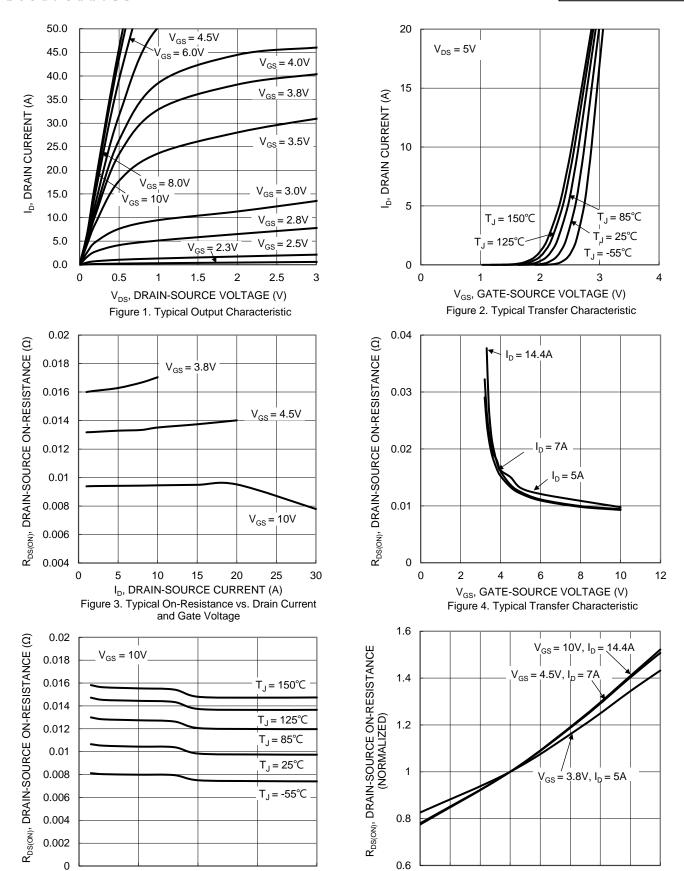
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 9)							
Drain-Source Breakdown Voltage		30	_	_	V	VGS = 0V, ID = 250µA	
Zero Gate Voltage Drain Current	IDSS	_	_	1	μΑ	V _{DS} = 24V, V _{GS} = 0V	
Zero Gate Voltage Drain Current T _J = +150°C (Note 10)	IDSS	-	_	100	μΑ	V _{DS} = 24V, V _{GS} = 0V	
Gate-Source Leakage	I _{GSS}	_	_	±100	nA	V _{GS} = 20V, V _{DS} = 0V V _{GS} = -16V, V _{DS} = 0V	
ON CHARACTERISTICS (Note 9)							
Gate Threshold Voltage	V _{GS(TH)}	1	_	3	V	$V_{DS} = V_{GS}$, $I_D = 250\mu A$	
		_	8	11		V _{GS} = 10V, I _D = 14.4A	
Static Drain-Source On-Resistance	RDS(ON)	_	12	17.5	$m\Omega$	VGS = 4.5V, ID = 7A	
		-	15	25		$V_{GS} = 3.8V, I_{D} = 5A$	
Diode Forward Voltage	V _{SD}	_	0.8	1.2	V	V _{GS} = 0V, I _S = 10A	
DYNAMIC CHARACTERISTICS (Note 10)							
Input Capacitance	Ciss	_	823	_			
Output Capacitance	Coss	_	352	_	pF	V _{DS} = 15V, V _{GS} = 0V, f = 1.0MHz	
Reverse Transfer Capacitance	C _{rss}	_	52	_			
Gate Resistance	Rg		1.2	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1.0MHz$	
Total Gate Charge (Vgs = 10V)	Qg		12	_		V _{DS} = 15V, I _D = 14.4A	
Total Gate Charge (V _{GS} = 4.5V)	Q_g	_	5.8	_	nC		
Gate-Source Charge	Qgs		1.7	_	ПС	VDS = 13V, ID = 14.4A	
Gate-Drain Charge	Qgd		2.4	_			
Turn-On Delay Time	td(on)	_	3.2	_		$V_{GS}=10V,V_{DD}=15V,R_g=1\Omega$ $I_D=10A$	
Turn-On Rise Time	t _R		5.2	_	no		
Turn-Off Delay Time	tD(OFF)		8.9	_	ns		
Turn-Off Fall Time	tF		1.5	_			
Body Diode Reverse Recovery Time	t _{RR}	_	16.4	_	ns	I _F = 10A, di/dt = 100A/µs	
Body Diode Reverse Recovery Charge	Q _{RR}	_	5.9	_	nC	I _F = 10A, di/dt = 100A/µs	

Notes:

^{9.} Short duration pulse test used to minimize self-heating effect.
10. Guaranteed by design. Not subject to product testing.





I_D, DRAIN CURRENT (A) Figure 5. Typical On-Resistance vs. Drain Current and Junction Temperature

10

15

50

25

0

-50

-25

20

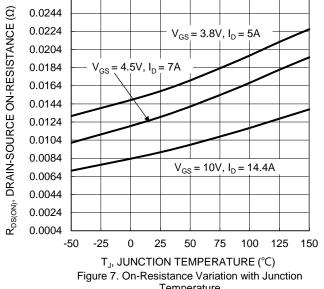
75

100

125

150





Temperature

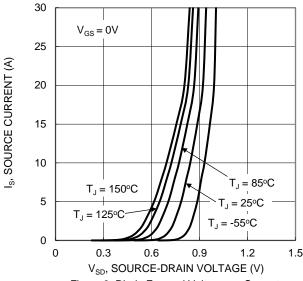


Figure 9. Diode Forward Voltage vs. Current

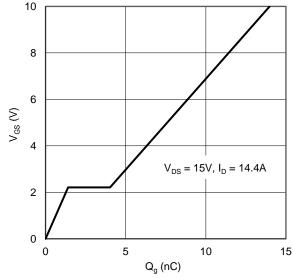


Figure 11. Gate Charge

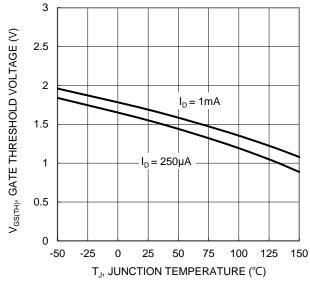
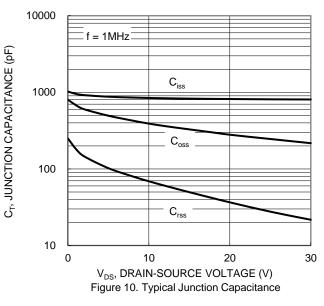
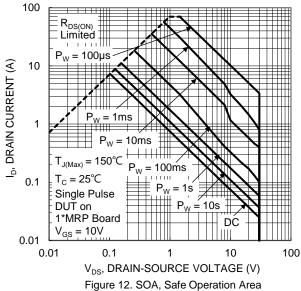


Figure 8. Gate Threshold Variation vs. Junction Temperature







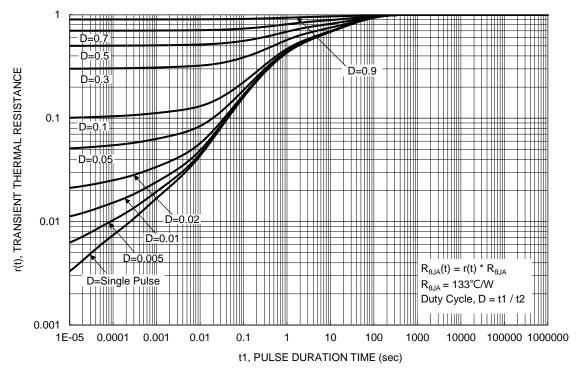


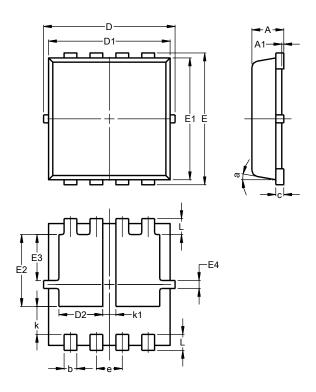
Figure 13. Transient Thermal Resistance



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI3333-8 (Type UXC)

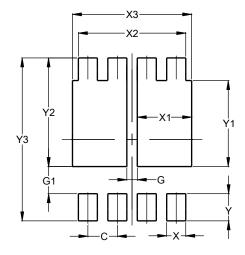


PowerDI3333-8						
(Type UXC)						
Dim	Min	Max	Тур			
Α	0.75	0.85	0.80			
A 1	0.00	0.05				
b	0.25	0.40	0.32			
С	0.10	0.25	0.15			
D	3.20	3.40	3.30			
D1	2.95	3.15	3.05			
D2	0.90	1.30	1.10			
Ε	3.20	3.40	3.30			
E1	2.95	3.15	3.05			
E2	1.60	2.00	1.80			
E3	0.95	1.35	1.15			
E4	0.10	0.30	0.20			
е	-	-	0.65			
L	0.30	0.50	0.40			
k	0.50	0.90	0.70			
k1	0.13	0.53	0.33			
а	0°	12°	10°			
All Dimensions in mm						

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI3333-8 (Type UXC)



Dimensions	Value (in mm)		
С	0.650		
G	0.230		
G1	0.600		
X	0.420		
X1	1.200		
X2	2.370		
Х3	2.630		
Y	0.600		
Y1	1.900		
Y2	2.400		
Y3	3.600		



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