

## Product Summary

BV <sub>DSS</sub>	R <sub>DS(ON)</sub>	I <sub>D</sub> T <sub>C</sub> = +25°C
30V	0.55mΩ @ V <sub>GS</sub> = 10V	150A
	0.95mΩ @ V <sub>GS</sub> = 4.5V	120A

## Description and Applications

This new generation MOSFET is designed to minimize R<sub>DS(ON)</sub> yet maintain superior switching performance. This device is ideal for use in notebook battery power managements and load switches.

- Body control electronics
- DC-DC converters

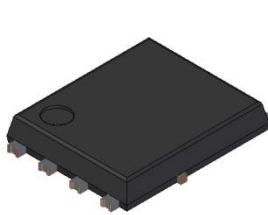
## Features

- 100% Unclamped Inductive Switch (UIS) Test in Production
- Thermally Efficient Package – Cooler Running Applications
- High Conversion Efficiency
- Low R<sub>DS(ON)</sub> – Minimizes On State Losses < 1.1mm Package Profile – Ideal for Thin Applications
- **Lead-Free Finish; RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **The DMT3M70LPSWQ is suitable for automotive applications requiring specific change control; this part is AEC-Q101 qualified, PPAP capable, and manufactured in IATF 16949 certified facilities.**  
<https://www.diodes.com/quality/product-definitions/>

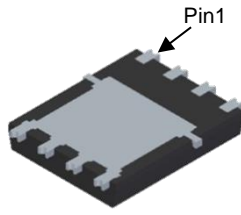
## Mechanical Data

- Package: PowerDI<sup>®</sup>5060-8
- Package Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram Below
- Terminals: Finish — Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (E3)
- Weight: 0.097 grams (Approximate)

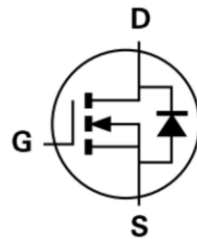
PowerDI5060-8/SWP (Type UX)



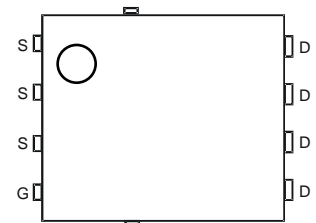
Top View



Bottom View



Internal Schematic



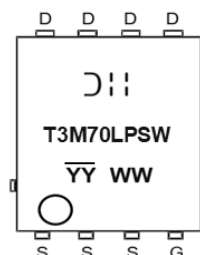
Top View  
Pin Configuration

## Ordering Information (Note 4)

Part Number	Package	Packing	
		Qty.	Carrier
DMT3M70LPSWQ-13	PowerDI5060-8/SWP (Type UX)	2,500	Tape & Reel

- Notes:
1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
  2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
  3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
  4. For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

## Marking Information



- ☺ = Manufacturer's Marking  
 T3M70LPSW = Product Type Marking Code  
 YYWW = Date Code Marking  
 YY = Last Two Digits of Year (ex: 24 = 2024)  
 WW = Week Code (01 to 53)

**Maximum Ratings** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Drain-Source Voltage	V <sub>DSS</sub>	30	V	
Gate-Source Voltage	V <sub>GSS</sub>	±20	V	
Continuous Drain Current, V <sub>GS</sub> = 10V (Note 7)	I <sub>D</sub>	T <sub>C</sub> = +25°C T <sub>C</sub> = +70°C	150 125	A
Continuous Drain Current, V <sub>GS</sub> = 10V (Note 6)		T <sub>A</sub> = +25°C T <sub>A</sub> = +70°C	61 49	A
Pulsed Drain Current (380µs Pulse, Duty Cycle = 1%)	I <sub>DM</sub>	600	A	
Maximum Continuous Body Diode Forward Current (Note 6)	I <sub>S</sub>	4.48	A	
Pulsed Body Diode Forward Current (380µs Pulse, Duty Cycle = 1%)	I <sub>SM</sub>	600	A	
Avalanche Current, L = 0.3mH (Note 8)	I <sub>AS</sub>	48	A	
Avalanche Energy, L = 0.3mH (Note 8)	E <sub>AS</sub>	345	mJ	

**Thermal Characteristics**

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 5)	P <sub>D</sub>	2.13	W
Thermal Resistance, Junction to Ambient (Note 5)	R <sub>θJA</sub>	58.6	°C/W
Total Power Dissipation (Note 6)	P <sub>D</sub>	3.85	W
Thermal Resistance, Junction to Ambient (Note 6)	R <sub>θJA</sub>	32.5	°C/W
Thermal Resistance, Junction to Case (Note 7)	R <sub>θJC</sub>	1.04	°C/W
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

**Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
<b>OFF CHARACTERISTICS (Note 9)</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	30	—	—	V	V <sub>GS</sub> = 0V, I <sub>D</sub> = 1mA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	—	—	1	µA	V <sub>DS</sub> = 30V, V <sub>GS</sub> = 0V
Gate-Source Leakage	I <sub>GSS</sub>	—	—	±100	nA	V <sub>GS</sub> = ±20V, V <sub>DS</sub> = 0V
<b>ON CHARACTERISTICS (Note 9)</b>						
Gate Threshold Voltage	V <sub>GS(TH)</sub>	1	—	3	V	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250µA
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>	—	0.39	0.55	mΩ	V <sub>GS</sub> = 10V, I <sub>D</sub> = 20A
		—	0.67	0.95		V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 20A
Diode Forward Voltage	V <sub>SD</sub>	—	0.7	1	V	V <sub>GS</sub> = 0V, I <sub>S</sub> = 20A
<b>DYNAMIC CHARACTERISTICS (Note 10)</b>						
Input Capacitance	C <sub>iss</sub>	—	11112	—	pF	V <sub>DS</sub> = 15V, V <sub>GS</sub> = 0V f = 1MHz
Output Capacitance	C <sub>oss</sub>	—	8689	—	pF	
Reverse Transfer Capacitance	C <sub>rss</sub>	—	325	—	pF	
Gate Resistance	R <sub>g</sub>	—	0.6	—	Ω	V <sub>DS</sub> = 0V, V <sub>GS</sub> = 0V, f = 1MHz
Total Gate Charge (V <sub>GS</sub> = 10V)	Q <sub>g</sub>	—	152.7	—	nC	V <sub>DS</sub> = 15V, I <sub>D</sub> = 20A
Total Gate Charge (V <sub>GS</sub> = 4.5V)	Q <sub>g</sub>	—	69.4	—	nC	
Gate-Source Charge	Q <sub>gs</sub>	—	28.8	—	nC	
Gate-Drain Charge	Q <sub>gd</sub>	—	10.3	—	nC	
Turn-On Delay Time	t <sub>D(ON)</sub>	—	15	—	ns	V <sub>DD</sub> = 15V, V <sub>GEN</sub> = 10V, R <sub>GEN</sub> = 6Ω, I <sub>D</sub> = 24A
Turn-On Rise Time	t <sub>R</sub>	—	29	—	ns	
Turn-Off Delay Time	t <sub>D(OFF)</sub>	—	89	—	ns	
Turn-Off Fall Time	t <sub>F</sub>	—	42	—	ns	

- Notes:
- Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
  - Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
  - Thermal resistance from junction to soldering point (on the exposed drain pad).
  - I<sub>AS</sub> and E<sub>AS</sub> ratings are based on low frequency and duty cycles to keep T<sub>J</sub> = +25°C.
  - Short duration pulse test used to minimize self-heating effect.
  - Guaranteed by design. Not subject to product testing.

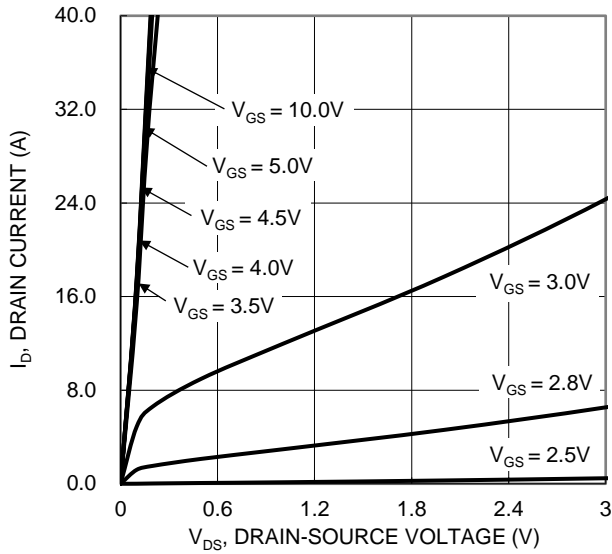


Figure 1. Typical Output Characteristic

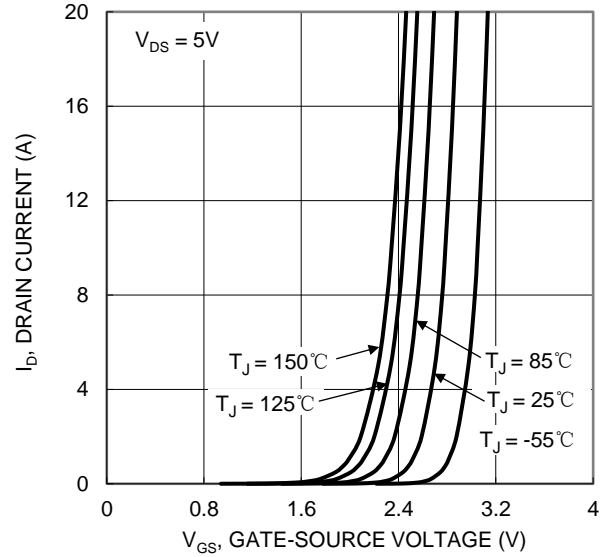


Figure 2. Typical Transfer Characteristic

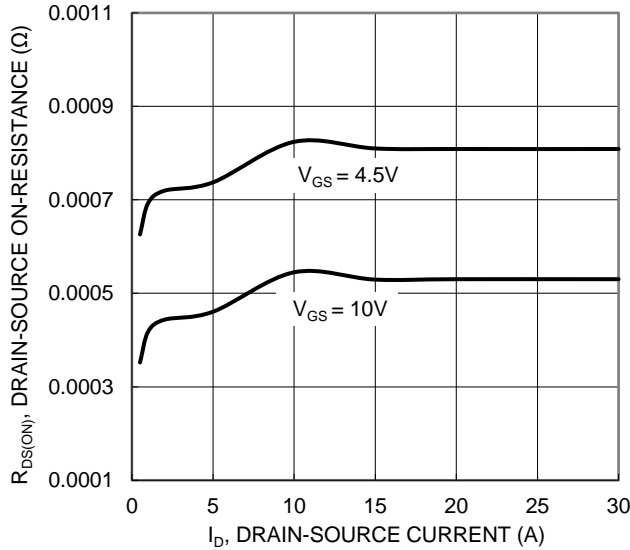


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

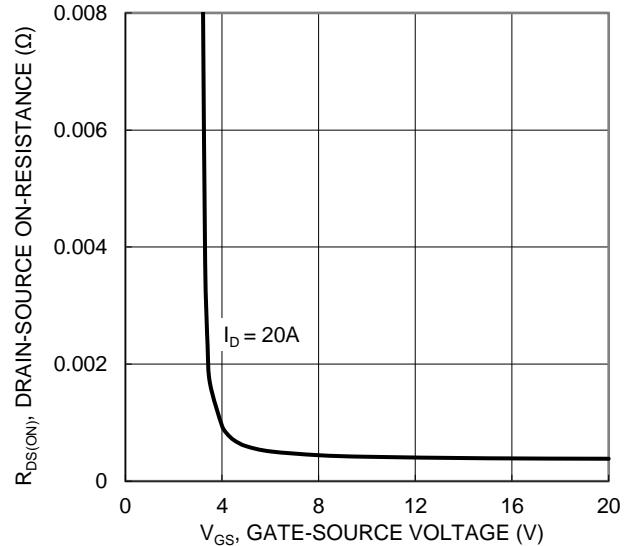


Figure 4. Typical Transfer Characteristic

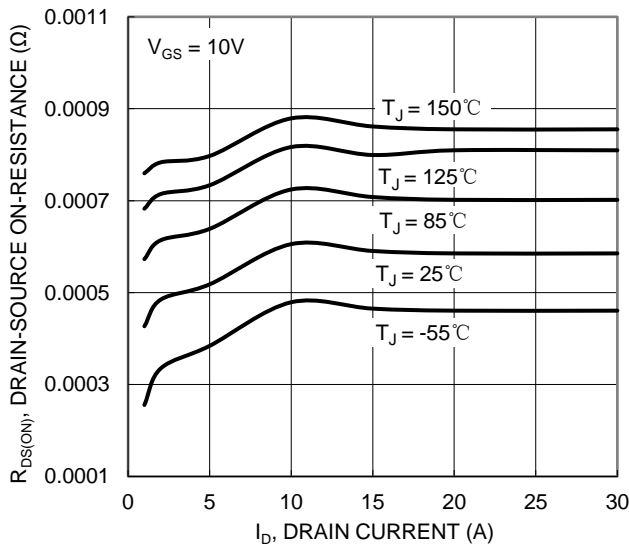


Figure 5. Typical On-Resistance vs. Drain Current and Junction Temperature

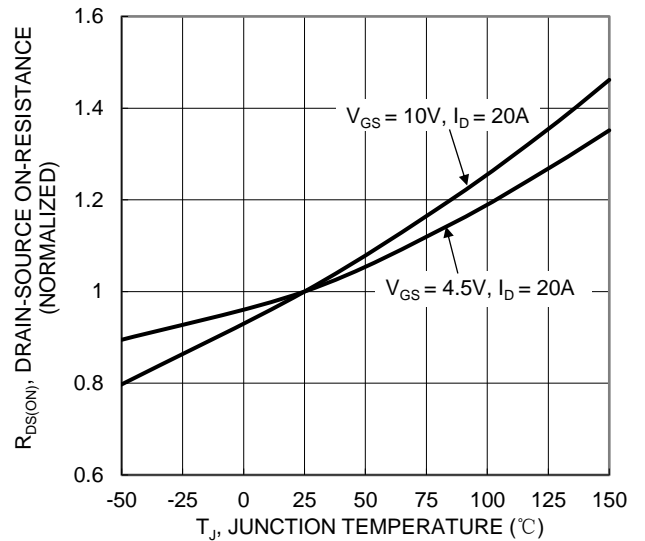


Figure 6. On-Resistance Variation with Junction Temperature

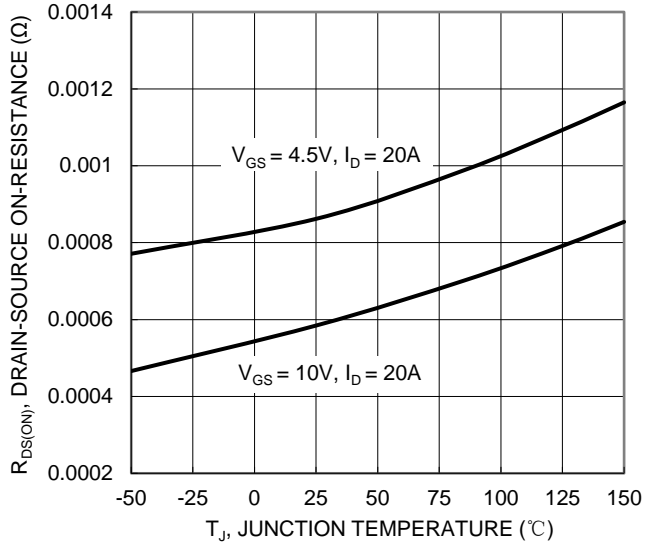


Figure 7. On-Resistance Variation with Junction Temperature

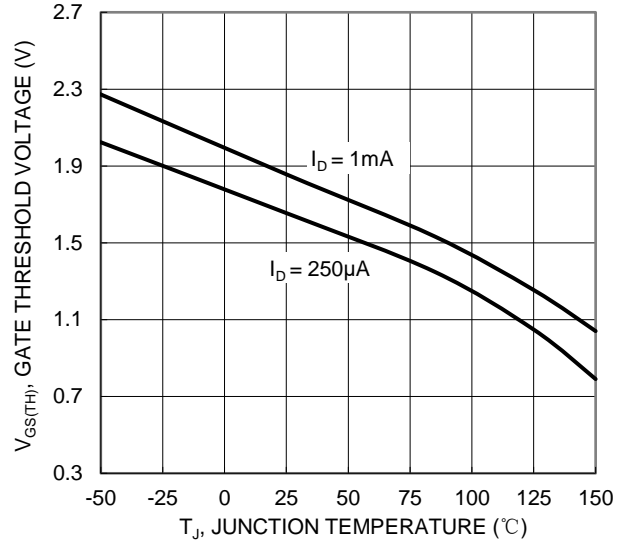


Figure 8. Gate Threshold Variation vs. Junction Temperature

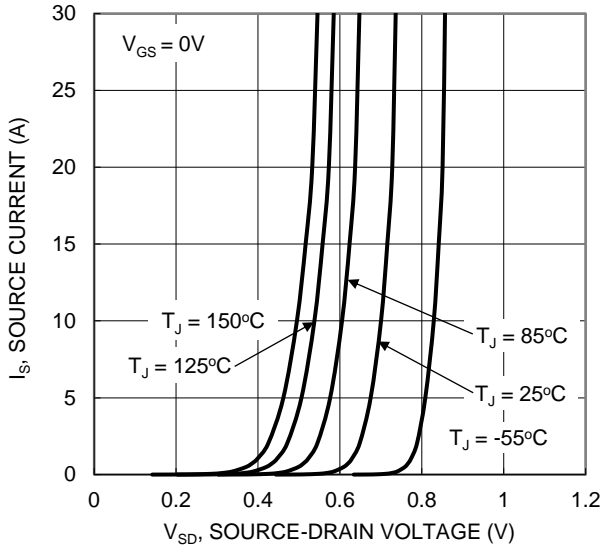


Figure 9. Diode Forward Voltage vs. Current

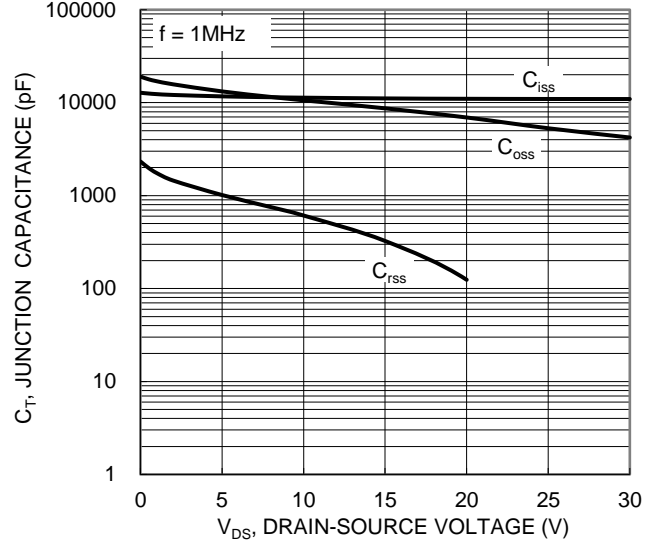


Figure 10. Typical Junction Capacitance

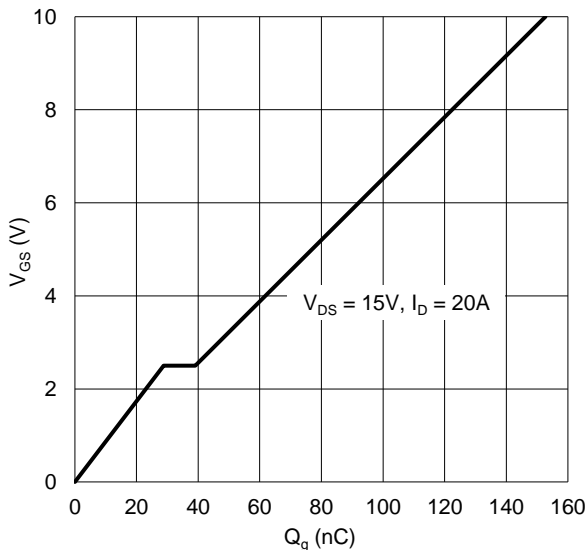


Figure 11. Gate Charge

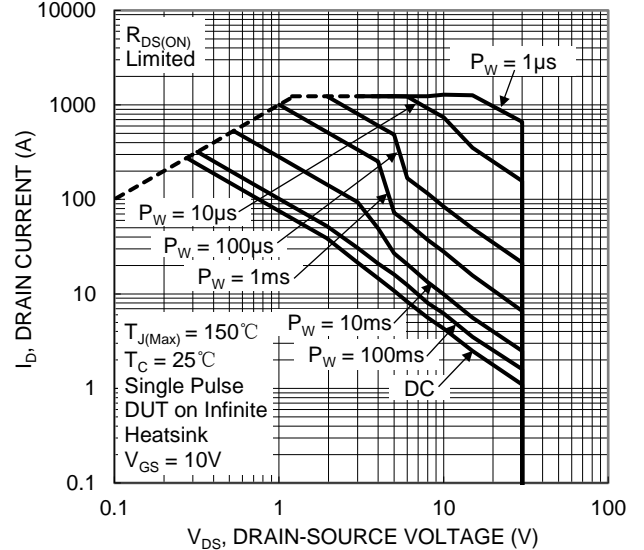


Figure 12. SOA, Safe Operation Area

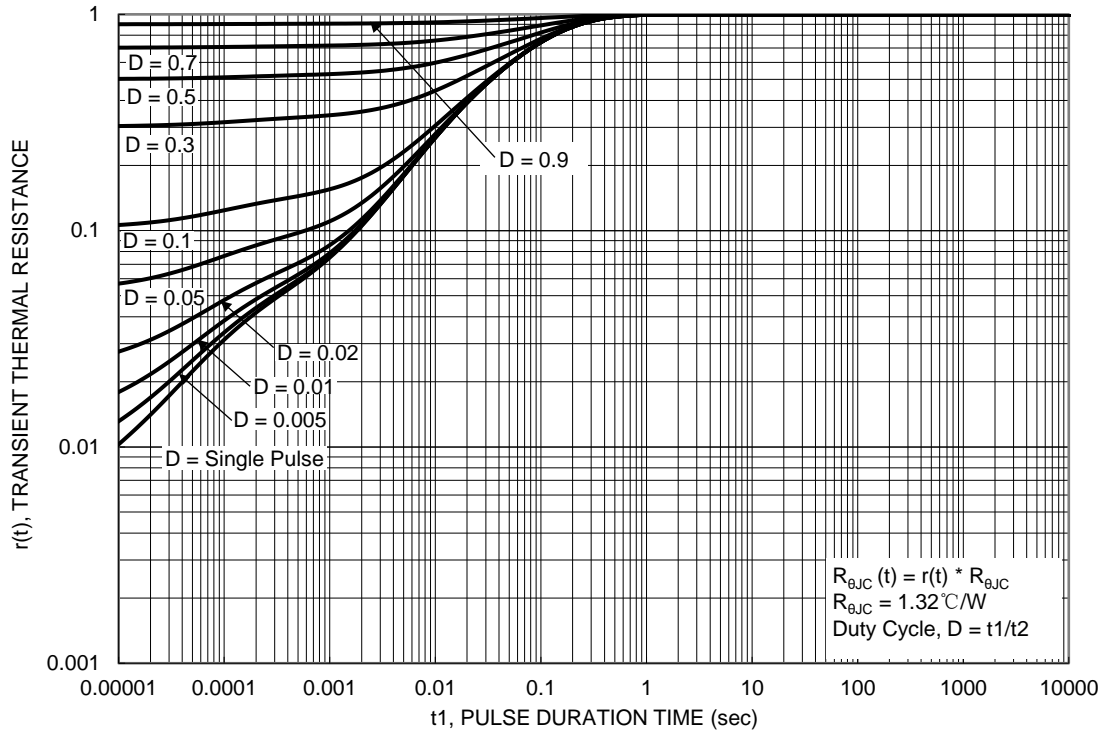


Figure 13. Transient Thermal Resistance



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