

Product Summary

BV _{DSS}	R _{DS(ON)} Max	I _D Max T _C = +25°C (Note 5)
40V	7.6mΩ @ V _{GS} = 10V	100A

Features

- Rated to +175°C – Ideal for High Ambient Temperature Environments
- Thermally Efficient Package – Cooler Running Applications
- High Conversion Efficiency
- Low R_{DS(ON)} – Minimizes On-State Losses
- Low Input Capacitance
- Fast Switching Speed
- < 1.1mm Package Profile – Ideal for Thin Applications
- **Lead-Free Finish; RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. “Green” Device (Note 3)**
- **The DMTH4007SPSWQ is suitable for automotive applications requiring specific change control; this part is AEC-Q101 qualified, PPAP capable, and manufactured in IATF 16949 certified facilities.**

<https://www.diodes.com/quality/product-definitions/>

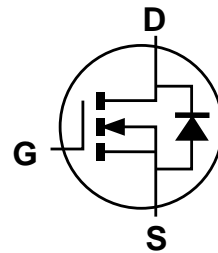
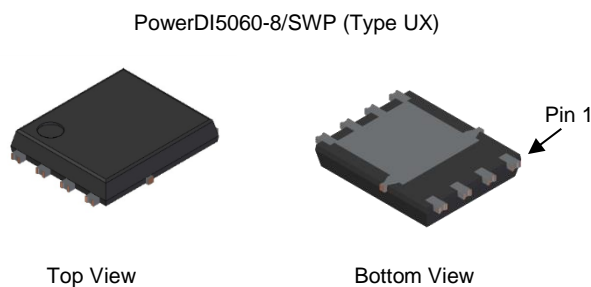
Description and Applications

This MOSFET is designed to meet the stringent requirements of automotive applications. It is qualified to AEC-Q101, supported by a PPAP and is ideal for use in:

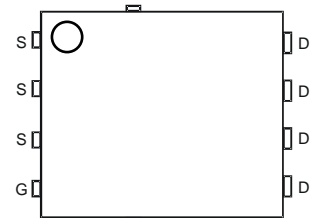
- Power management
- DC-DC converters
- Motor controls

Mechanical Data

- Package: PowerDI[®]5060-8
- Package Material: Molded Plastic, “Green” Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish – Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (E3)
- Weight: 0.097 grams (Approximate)



Internal Schematic



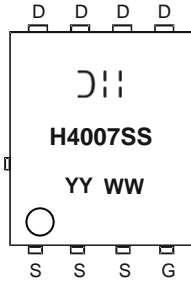
Top View
Pin Configuration

Ordering Information (Note 4)

Part Number	Package	Packing	
		Qty.	Carrier
DMTH4007SPSWQ-13	PowerDI5060-8/SWP (Type UX)	2,500	Tape & Reel

- Notes:
1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
 2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 4. For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.
 5. Package limited.

Marking Information



DII = Manufacturer's Code Marking
H4007SS = Product Type Marking Code
YYWW = Date Code Marking
YY = Last Two Digits of Year (ex: 23 = 2023)
WW = Week Code (01 to 53)

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Drain-Source Voltage	V _{DSS}	40	V
Gate-Source Voltage	V _{GSS}	±20	V
Continuous Drain Current (Note 6)	I _D	T _A = +25°C	15.7
		T _A = +70°C	13.1
Continuous Drain Current (Note 7)	I _D	T _C = +25°C (Note 5)	100
		T _C = +100°C	77
Maximum Continuous Body Diode Forward Current (Note 7)	I _S	100	A
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)	I _{DM}	120	A
Avalanche Current, L = 0.3mH	I _{AS}	20	A
Avalanche Energy, L = 0.3mH	E _{AS}	60	mJ

Thermal Characteristics

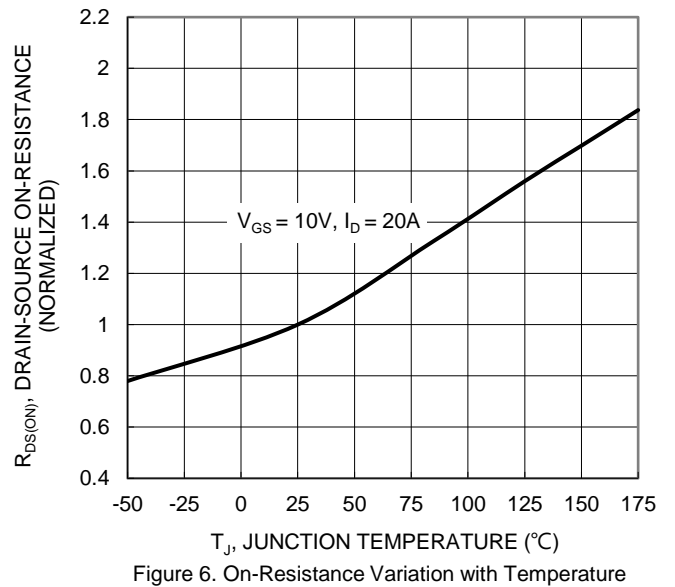
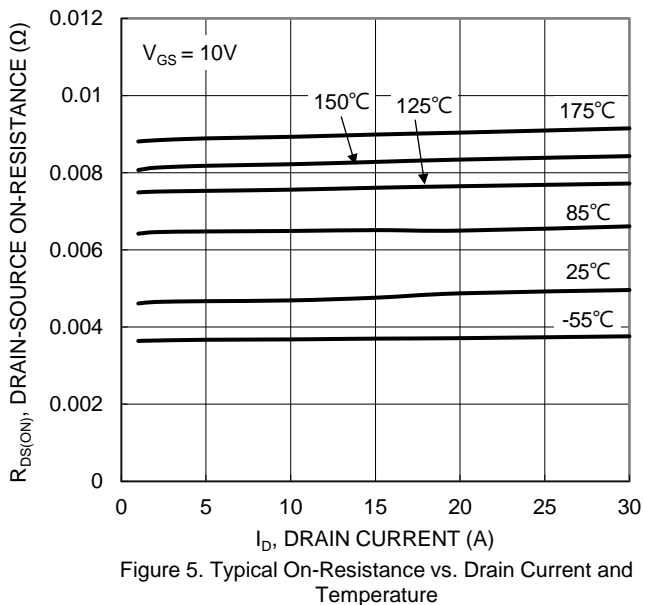
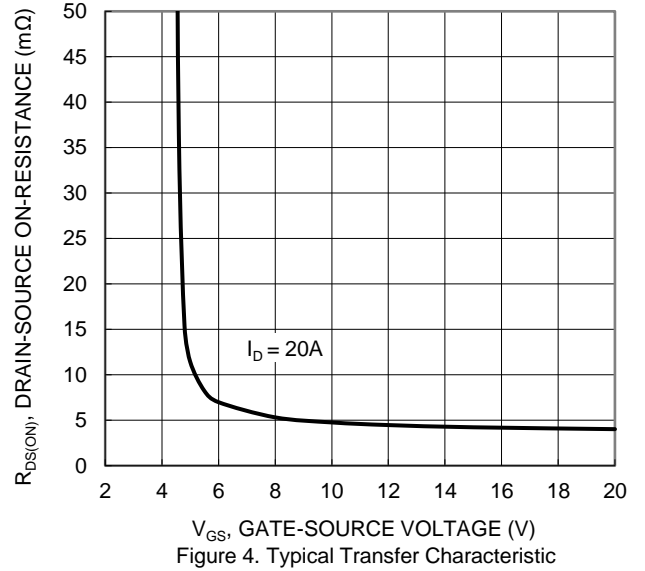
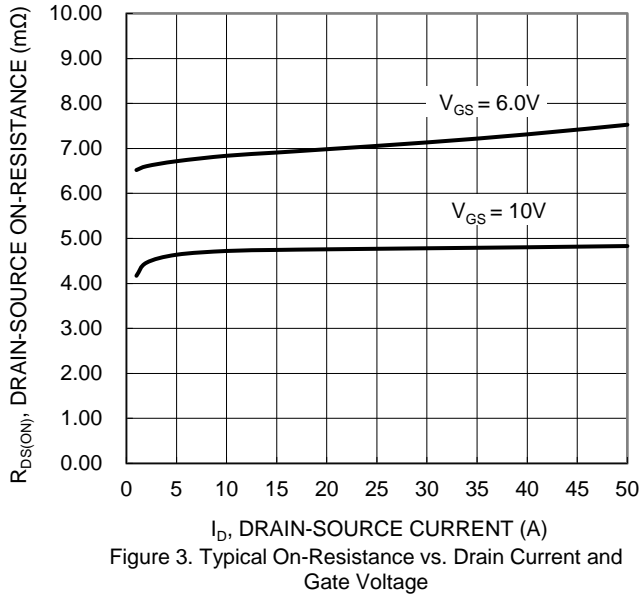
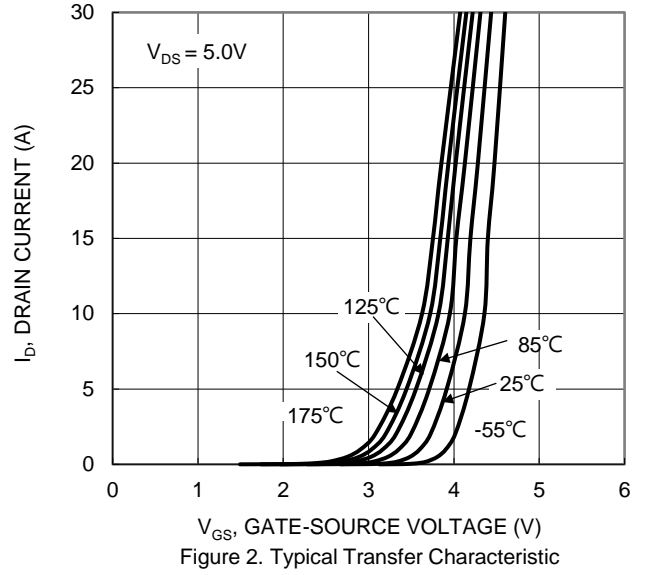
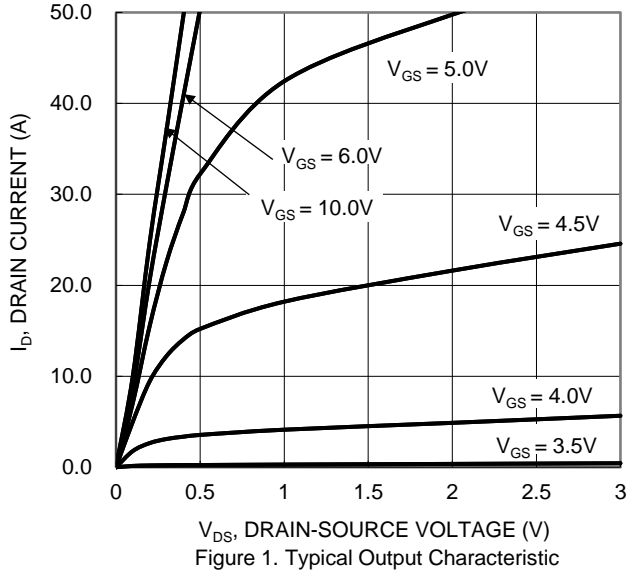
Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 6)	P _D	T _A = +25°C	2.8
Thermal Resistance, Junction to Ambient (Note 6)		R _{θJA}	53
Total Power Dissipation (Note 7)	P _D	T _C = +25°C	136
Thermal Resistance, Junction to Case (Note 7)		R _{θJC}	1.1
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +175	°C

- Notes:
5. Package limited.
 6. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate.
 7. Thermal resistance from junction to soldering point (on the exposed drain pad).

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 8)							
Drain-Source Breakdown Voltage	BV _{DSS}	40	—	—	V	V _{GS} = 0V, I _D = 1mA	
Zero Gate Voltage Drain Current	—	I _{DSS}	—	—	1	μA	V _{DS} = 32V, V _{GS} = 0V
	(Note 9)	I _{DSS}	—	—	100	μA	V _{DS} = 32V, V _{GS} = 0V, T _J = +125°C
Gate-Source Leakage	I _{GSS}	—	—	±100	nA	V _{GS} = ±20V, V _{DS} = 0V	
ON CHARACTERISTICS (Note 8)							
Gate Threshold Voltage	V _{GS(TH)}	2	—	4	V	V _{DS} = V _{GS} , I _D = 250μA	
Static Drain-Source On-Resistance	R _{DS(ON)}	—	4.9	7.6	mΩ	V _{GS} = 10V, I _D = 20A	
Diode Forward Voltage	V _{SD}	—	—	1.2	V	V _{GS} = 0V, I _S = 20A	
DYNAMIC CHARACTERISTICS (Note 9)							
Input Capacitance	C _{iss}	—	2,082	—	pF	V _{DS} = 25V, V _{GS} = 0V f = 1MHz	
Output Capacitance	C _{oss}	—	790	—			
Reverse Transfer Capacitance	C _{rss}	—	113	—			
Gate Resistance	R _g	0.1	0.46	1.4	Ω	V _{DS} = 0V, V _{GS} = 0V, f = 1MHz	
Total Gate Charge	Q _g	—	41.9	—	nC	V _{DS} = 30V, I _D = 20A, V _{GS} = 10V	
Gate-Source Charge	Q _{gs}	—	10	—			
Gate-Drain Charge	Q _{gd}	—	11.5	—			
Turn-On Delay Time	t _{D(ON)}	—	7	—	ns	V _{DD} = 30V, V _{GS} = 10V I _D = 20A, R _g = 3Ω	
Turn-On Rise Time	t _r	—	11.5	—			
Turn-Off Delay Time	t _{D(OFF)}	—	15.6	—			
Turn-Off Fall Time	t _f	—	8.8	—			
Body Diode Reverse Recovery Time	t _{RR}	—	29.9	—	ns	I _F = 20A, di/dt = 100A/μs	
Body Diode Reverse Recovery Charge	Q _{RR}	—	23	—	nC		

Notes: 8. Short duration pulse test used to minimize self-heating effect.
9. Guaranteed by design. Not subject to product testing.



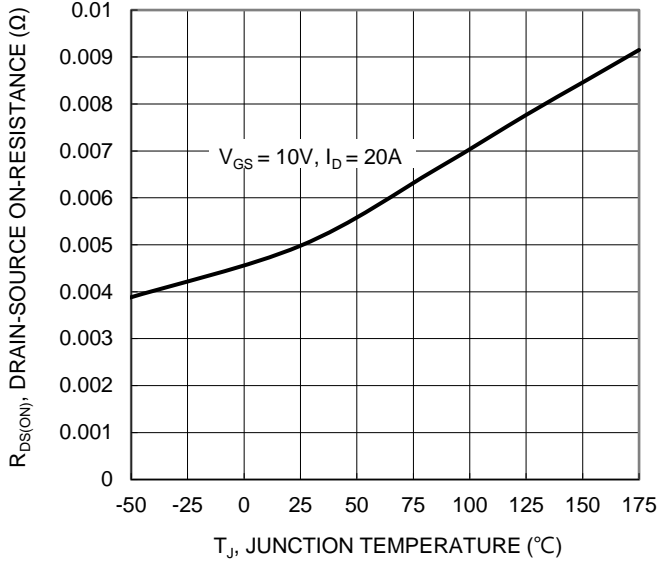


Figure 7. On-Resistance Variation with Temperature

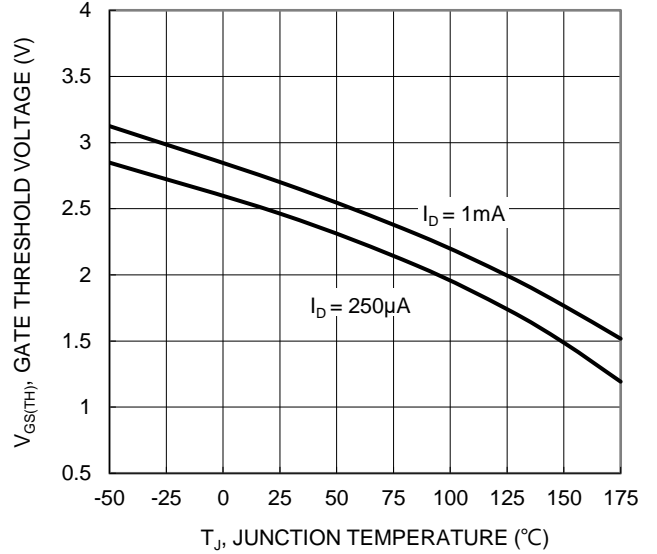


Figure 8. Gate Threshold Variation vs. Temperature

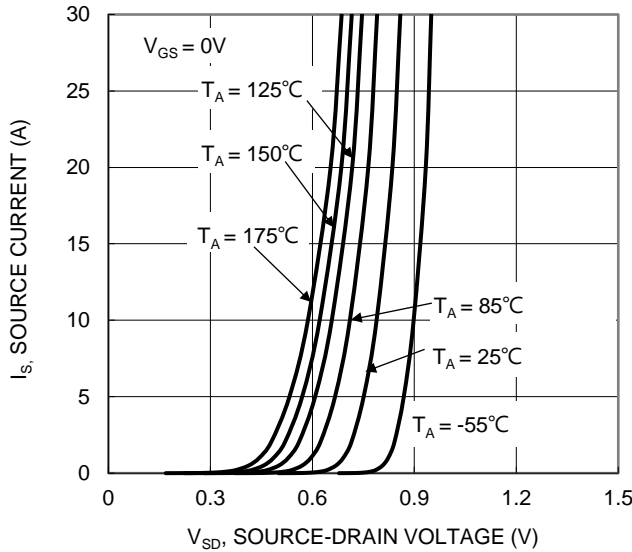


Figure 9. Diode Forward Voltage vs. Current

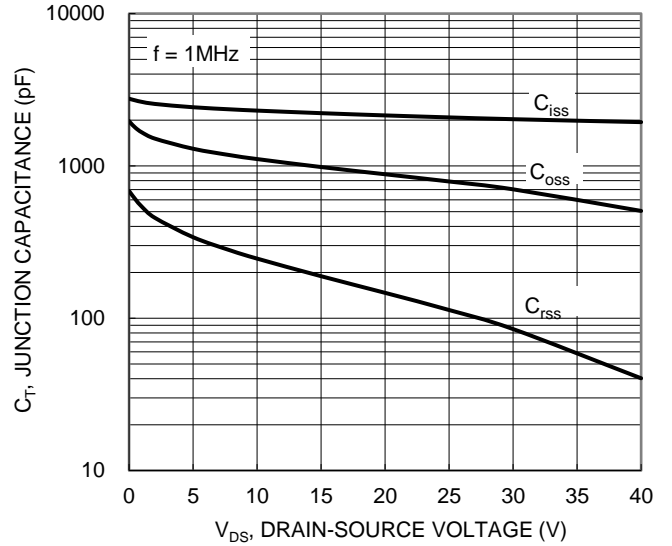


Figure 10. Typical Junction Capacitance

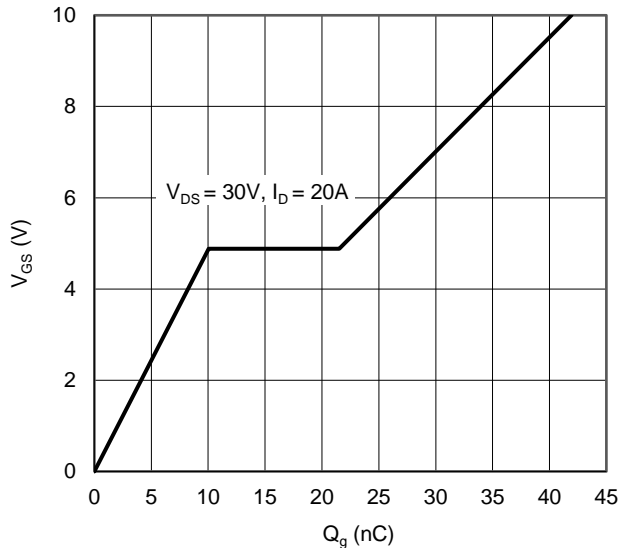


Figure 11. Gate Charge

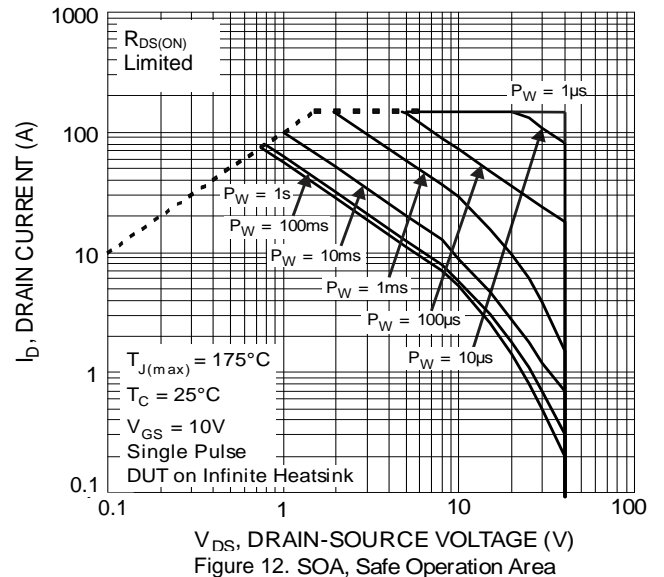


Figure 12. SOA, Safe Operation Area

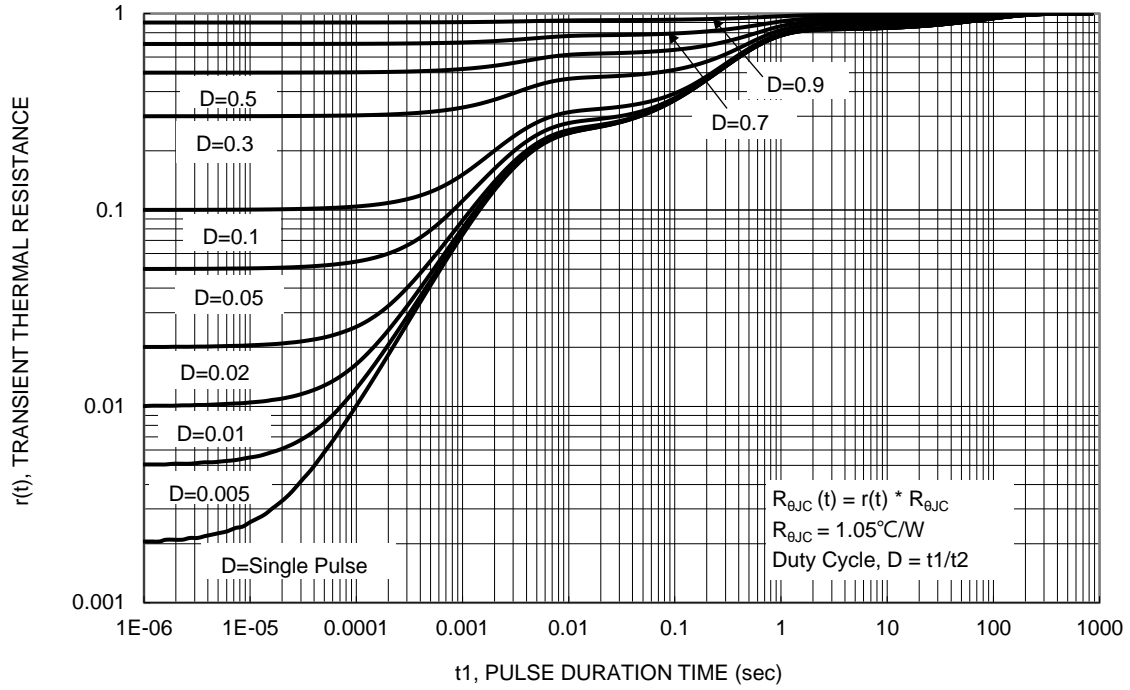
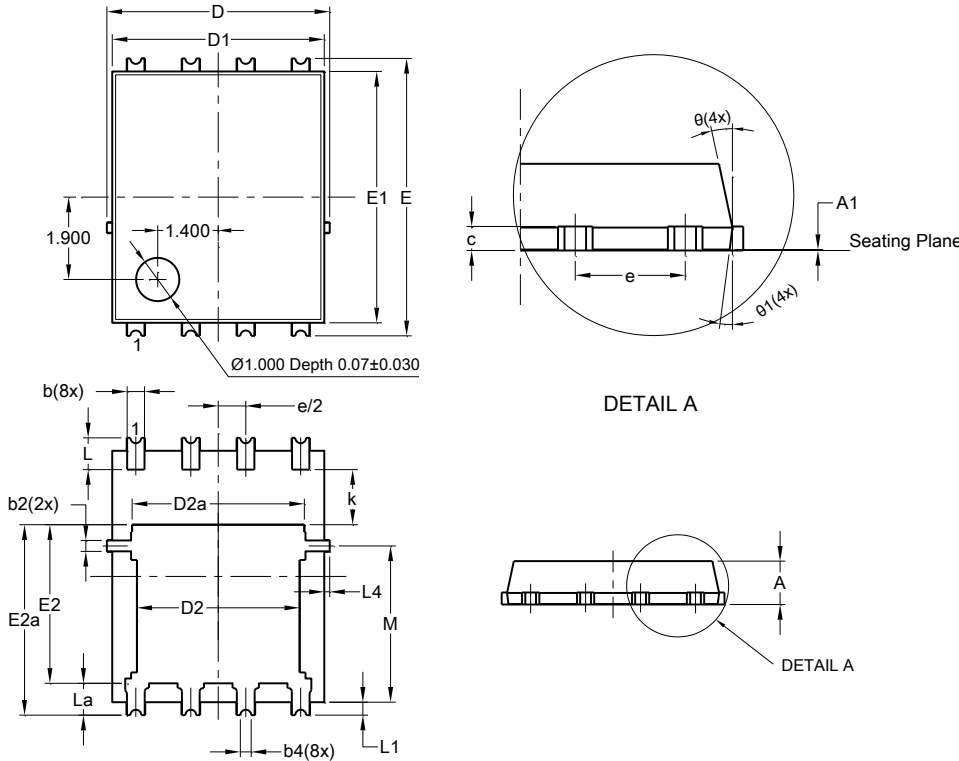


Figure 13. Transient Thermal Resistance

Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

PowerDI5060-8/SWP (Type UX)

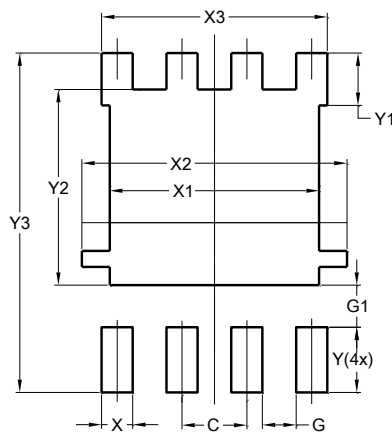


PowerDI5060-8/SWP (Type UX)			
Dim	Min	Max	Typ
A	0.90	1.10	1.00
A1	0	0.05	--
b	0.30	0.50	0.41
b2	0.20	0.35	0.25
b4	0.25REF		
c	0.230	0.330	0.277
D	5.15 BSC		
D1	4.70	5.10	4.90
D2	3.56	3.96	3.76
D2a	3.78	4.18	3.98
E	6.40 BSC		
E1	5.60	6.00	5.80
E2	3.46	3.86	3.66
E2a	4.195	4.595	4.395
e	1.27BSC		
k	1.05	--	--
L	0.635	0.835	0.735
La	0.635	0.835	0.735
L1	0.200	0.400	0.300
L1a	0.050REF		
L4	0.025	0.225	0.125
M	3.205	4.005	3.605
θ	10°	12°	11°
θ1	6°	8°	7°
All Dimensions in mm			

Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

PowerDI5060-8/SWP (Type UX)



Dimensions	Value (in mm)
C	1.270
G	0.660
G1	0.820
X	0.610
X1	4.100
X2	5.190
X3	4.420
Y	1.270
Y1	1.020
Y2	3.810
Y3	6.610

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