



40V 175°C N-CHANNEL ENHANCEMENT MODE MOSFET PowerDI3333-8

Product Summary

BV _{DSS}	R _{DS(ON)} Max	I _D Max T _C = +25°C	
40V	$7.4 \text{m}\Omega$ @ $V_{GS} = 10 \text{V}$	67.2A	

Features and Benefits

- Rated to +175°C Ideal for High Ambient Temperature Environments
- Excellent Q_{GD} x R_{DS(ON)} Product (FOM)
- Low R_{DS(ON)} Ensures On-State Losses are Minimized
- 100% Unclamped Inductive Switching (UIS) Test in Production Ensures More Reliable and Robust End Application
- Wettable Flank for Improved Optical Inspection
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please contact us or your local Diodes representative.

https://www.diodes.com/quality/product-definitions/

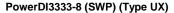
Description and Applications

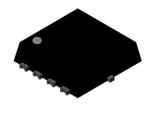
This new generation MOSFET is designed to minimize the on-state resistance (RDS(ON)) yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- Motor controls
- Power management functions
- DC-DC converters

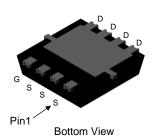
Mechanical Data

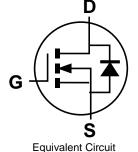
- Package: PowerDI[®]3333-8
- Package Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Finish Matte Tin Annealed over Copper Leadframe;
 Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.072 grams (Approximate)











Ordering Information (Note 4)

Part Number	Package	Packing		
Fait Number	Fackage	Qty.	Carrier	
DMTH46M7SFVW-7	PowerDI3333-8 (SWP) (Type UX)	2,000	Tape & Reel	
DMTH46M7SFVW-13	PowerDI3333-8 (SWP) (Type UX)	3.000	Tape & Reel	

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



 $\frac{\text{M7S} = \text{Product Type Marking Code}}{\frac{\text{YY}}{\text{WW}} = \text{Date Code Marking}}$ $\frac{\text{YY}}{\text{YY}} = \text{Last Two Digits of Year (ex: 22 = 2022)}$ WW = Week Code (01 to 53)



Maximum Ratings (@ $T_A = +25$ °C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Drain-Source Voltage		VDSS	40	V
Gate-Source Voltage		V_{GSS}	±20	V
Continuous Drain Current (Note 6) Voc. 10V	Tc = +25°C	ī	67.2	A
Continuous Drain Current (Note 6), V _{GS} = 10V	T _C = +100°C	ID	47.5	
Continuous Drain Current (Note E) Voc. 10V	T _A = +25°C	ī	16.3	A
Continuous Drain Current (Note 5), VGS = 10V	T _A = +100°C	lD	11.5	
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)	lом	260	Α	
Maximum Continuous Body Diode Forward Current (Note 6)	Is	65	А	
Pulsed Body Diode Forward Current (10µs Pulse, Duty Cycle = 1	lsм	260	Α	
Avalanche Current, L = 0.1mH		las	26.6	А
Avalanche Energy, L = 0.1mH		Eas	35.3	mJ

Thermal Characteristics

Characteristic	Symbol	Value	Unit	
Total Power Dissipation (Note 5)	T _A = +25°C	PD	3.2	W
Thermal Resistance, Junction to Ambient (Note 5)	RθJA	46.8	°C/W	
Total Power Dissipation (Note 6) $T_C = +25^{\circ}C$		PD	54.5	W
Thermal Resistance, Junction to Case (Note 6)		R _θ JC	2.75	°C/W
Operating and Storage Temperature Range		TJ, TSTG	-55 to +175	°C

Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BV _{DSS}	40	_	_	V	$V_{GS} = 0V, I_{D} = 250\mu A$	
Zero Gate Voltage Drain Current	IDSS	_	_	1	μA	V _{DS} = 32V, V _{GS} = 0V	
Gate-Source Leakage	Igss	_	_	±100	nA	$V_{GS} = \pm 20V$, $V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)						•	
Gate Threshold Voltage	Vgs(TH)	2	_	4	V	$V_{DS} = V_{GS}$, $I_D = 250\mu A$	
Static Drain-Source On-Resistance	RDS(ON)		5.7	7.4	mΩ	V _G S = 10V, I _D = 20A	
Diode Forward Voltage	VsD	_	0.9	1.2	V	V _G S = 0V, I _S = 20A	
DYNAMIC CHARACTERISTICS (Note 8)						•	
Input Capacitance	Ciss	_	1315	_		V _{DS} = 20V, V _{GS} = 0V, f = 1MHz	
Output Capacitance	Coss	_	517	_	pF		
Reverse Transfer Capacitance	Crss	_	30.9	_			
Gate Resistance	Rg	_	1.13	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$	
Total Gate Charge	Qg	_	14.8	_		V _{DS} = 20V, I _D = 20A, V _{GS} = 10V	
Gate-Source Charge	Qgs	_	1.9	_	nC		
Gate-Drain Charge	Q _{gd}	_	5.2	_			
Turn-On Delay Time	t _{D(ON)}	_	8.67	_		$V_{DD} = 20V, V_{GS} = 10V,$ $R_g = 3\Omega, I_D = 20A$	
Turn-On Rise Time	t _R	_	16.1	_			
Turn-Off Delay Time	t _D (OFF)	_	15.9	_	ns		
Turn-Off Fall Time	t _F	_	9.07	_			
Body Diode Reverse Recovery Time	t _{RR}	_	105	_	ns	1 200 4:/44 2000///-	
Body Diode Reverse Recovery Charge	Q _{RR}	_	229	_	$_{\rm nC}$ I _F = 20A, di/dt = 300A/µs		

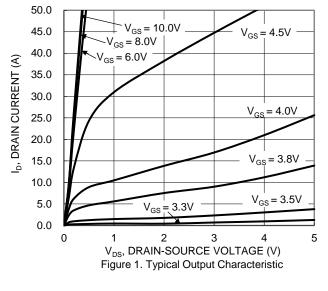
Notes: 5. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate.

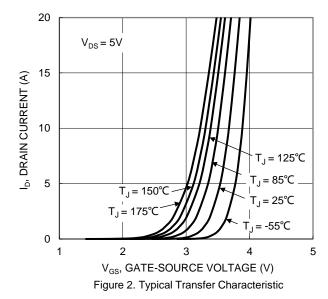
6. Thermal resistance from junction to soldering point (on the exposed drain pad).

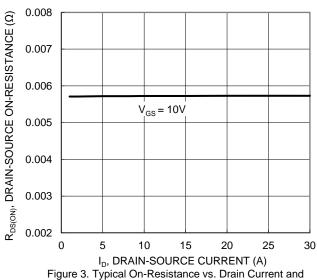
^{7.} Short duration pulse test used to minimize self-heating effect.

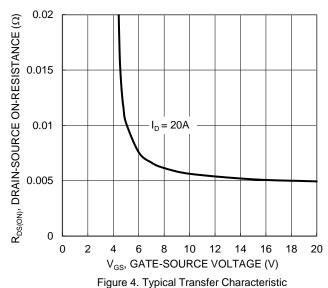
^{8.} Guaranteed by design. Not subject to production testing.



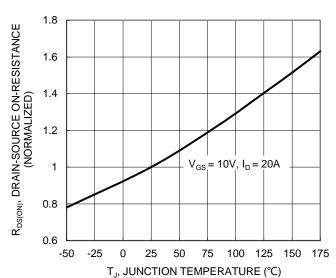












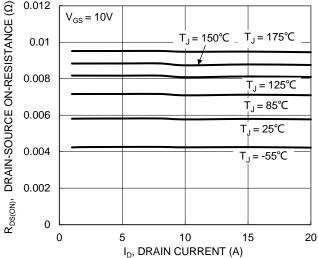


Figure 6. On-Resistance Variation with Junction Temperature

Figure 5. Typical On-Resistance vs. Drain Current and Junction Temperature



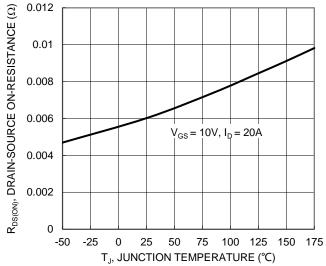


Figure 7. On-Resistance Variation with Junction Temperature

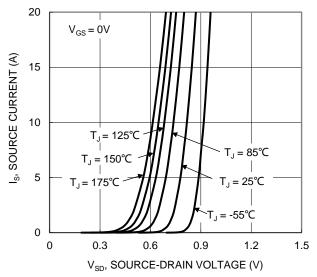


Figure 9. Diode Forward Voltage vs. Current

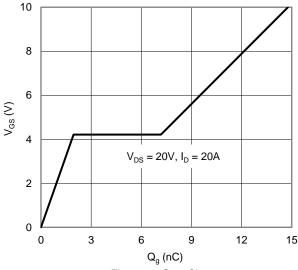


Figure 11. Gate Charge

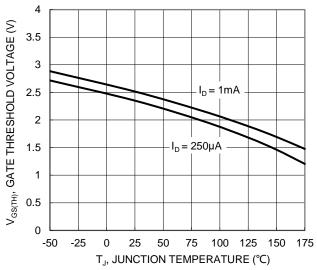


Figure 8. Gate Threshold Variation vs. Junction Temperature

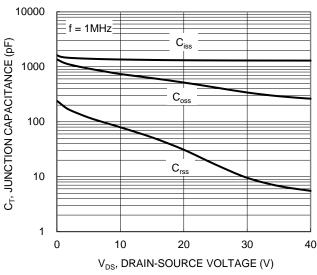
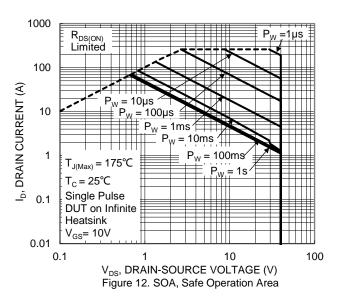


Figure 10. Typical Junction Capacitance





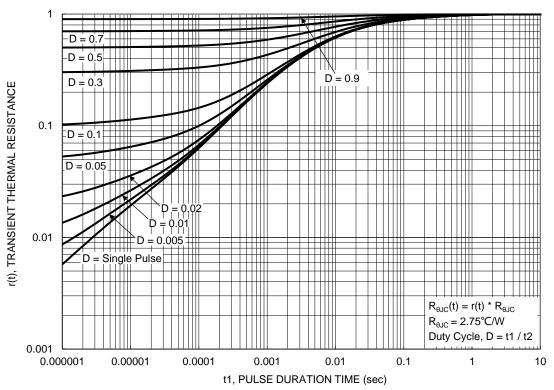


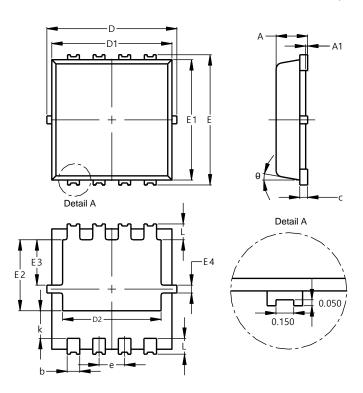
Figure 13. Transient Thermal Resistance



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI3333-8 (SWP) (Type UX)

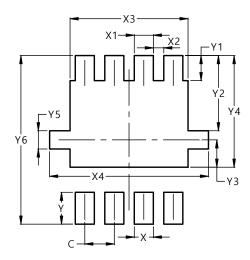


PowerDI3333-8 (SWP)						
(Type UX)						
Dim	Min	Max	Тур			
Α	0.75	0.85	0.80			
A1	0.00	0.05				
b	0.25	0.40	0.32			
C	0.10	0.25	0.15			
D	3.20	3.40	3.30			
D1	2.95	3.15	3.05			
D2	2.30	2.70	2.50			
Е	3.20	3.40	3.30			
E1	2.95	3.15	3.05			
E2	1.60	2.00	1.80			
E3	0.95	1.35	1.15			
E4	0.10	0.30	0.20			
е	_	-	0.65			
k	0.50	0.90	0.70			
L	0.30	0.50	0.40			
θ	0°	12°	10°			
All Dimensions in mm						

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI3333-8 (SWP) (Type UX)



Dimensions	Value (in mm)			
C	0.650			
X	0.420			
X1	0.420			
X2	0.230			
Х3	2.600			
X4	3.500			
Υ	0.700			
Y1	0.550			
Y2	1.650			
Y3	0.600			
Y4	2.450			
Y5	0.400			
Y6	3.700			



IMPORTANT NOTICE

- 1. DIODES INCORPORATED (Diodes) AND ITS SUBSIDIARIES MAKE NO WARRANTY OF ANY KIND, EXPRESS OR IMPLIED, WITH REGARDS TO ANY INFORMATION CONTAINED IN THIS DOCUMENT, INCLUDING, BUT NOT LIMITED TO, THE IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE OR NON-INFRINGEMENT OF THIRD PARTY INTELLECTUAL PROPERTY RIGHTS (AND THEIR EQUIVALENTS UNDER THE LAWS OF ANY JURISDICTION).
- 2. The Information contained herein is for informational purpose only and is provided only to illustrate the operation of Diodes' products described herein and application examples. Diodes does not assume any liability arising out of the application or use of this document or any product described herein. This document is intended for skilled and technically trained engineering customers and users who design with Diodes' products. Diodes' products may be used to facilitate safety-related applications; however, in all instances customers and users are responsible for (a) selecting the appropriate Diodes products for their applications, (b) evaluating the suitability of Diodes' products for their intended applications, (c) ensuring their applications, which incorporate Diodes' products, comply the applicable legal and regulatory requirements as well as safety and functional-safety related standards, and (d) ensuring they design with appropriate safeguards (including testing, validation, quality control techniques, redundancy, malfunction prevention, and appropriate treatment for aging degradation) to minimize the risks associated with their applications.
- 3. Diodes assumes no liability for any application-related information, support, assistance or feedback that may be provided by Diodes from time to time. Any customer or user of this document or products described herein will assume all risks and liabilities associated with such use, and will hold Diodes and all companies whose products are represented herein or on Diodes' websites, harmless against all damages and liabilities.
- 4. Products described herein may be covered by one or more United States, international or foreign patents and pending patent applications. Product names and markings noted herein may also be covered by one or more United States, international or foreign trademarks and trademark applications. Diodes does not convey any license under any of its intellectual property rights or the rights of any third parties (including third parties whose products and services may be described in this document or on Diodes' website) under this document.
- 5. Diodes' products are provided subject to Diodes' Standard Terms and Conditions of Sale (https://www.diodes.com/about/company/terms-and-conditions/terms-and-conditions-of-sales/) or other applicable terms. This document does not alter or expand the applicable warranties provided by Diodes. Diodes does not warrant or accept any liability whatsoever in respect of any products purchased through unauthorized sales channel.
- 6. Diodes' products and technology may not be used for or incorporated into any products or systems whose manufacture, use or sale is prohibited under any applicable laws and regulations. Should customers or users use Diodes' products in contravention of any applicable laws or regulations, or for any unintended or unauthorized application, customers and users will (a) be solely responsible for any damages, losses or penalties arising in connection therewith or as a result thereof, and (b) indemnify and hold Diodes and its representatives and agents harmless against any and all claims, damages, expenses, and attorney fees arising out of, directly or indirectly, any claim relating to any noncompliance with the applicable laws and regulations, as well as any unintended or unauthorized application.
- 7. While efforts have been made to ensure the information contained in this document is accurate, complete and current, it may contain technical inaccuracies, omissions and typographical errors. Diodes does not warrant that information contained in this document is error-free and Diodes is under no obligation to update or otherwise correct this information. Notwithstanding the foregoing, Diodes reserves the right to make modifications, enhancements, improvements, corrections or other changes without further notice to this document and any product described herein. This document is written in English but may be translated into multiple languages for reference. Only the English version of this document is the final and determinative format released by Diodes.
- 8. Any unauthorized copying, modification, distribution, transmission, display or other use of this document (or any portion hereof) is prohibited. Diodes assumes no responsibility for any losses incurred by the customers or users or any third parties arising from any such unauthorized use.
- 9. This Notice may be periodically updated with the most recent version available at https://www.diodes.com/about/company/terms-and-conditions/important-notice

DIODES is a trademark of Diodes Incorporated in the United States and other countries. The Diodes logo is a registered trademark of Diodes Incorporated in the United States and other countries. © 2022 Diodes Incorporated. All Rights Reserved.

www.diodes.com