



DMWSH120H23SM3

1200V N-CHANNEL SILICON CARBIDE POWER MOSFET

Product Summary

BV _{DSS}	RDS(ON) Max	I _D T _C = +25°C	
1200V	23mΩ @V _{GS} = 18V	100A	

Features and Benefits

- Low On-Resistance
- High BV_{DSS} Rating for Power Application
- Low Input Capacitance
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please contact us or your local Diodes representative.

 https://www.diodes.com/quality/product-definitions/

Description and Applications

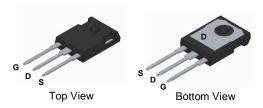
This SiC MOSFET is designed to minimize the on-state resistance yet maintain superior switching performance, making it ideal for high-efficiency power-management applications.

- SMPS (switching mode power supplies)
- UPS (uninterruptable power supplies)
- Energy storages
- EV charging systems
- Solar inverters

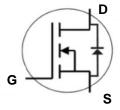
Mechanical Data

- Package: TO247
- Package Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Terminal Connections: See Diagram
- Terminals: Finish Matte Tin Annealed over Copper Leadframe.
 Solderable per MIL-STD-202, Method 208 @3
- Weight: 6.6 grams (Approximate)

TO247 Standard



Pin Configuration



Internal Schematic

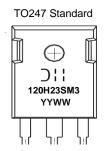
Ordering Information (Note 4)

Don't Normalian	Deekene	Packing		
Part Number	Package	Qty.	Carrier	
DMWSH120H23SM3	TO247 Standard	30 Pieces	Tube	

Notes:

- 1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



☐ I = Manufacturer's Marking
120H23SM3 = Product Type Marking Code
YYWW or YYWW = Date Code Marking
YY or YY = Last Two Digits of Year (ex: 24 = 2024)
WW or WW = Week Code (01 to 53)



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic			Value	Unit
Drain-Source Voltage		V_{DSS}	1200	V
Gate-Source Voltage		Vgs	-10/+22	V
Gate-Source Voltage (Recommended Operating Values)		V_{GS}	-4/+18	V
Gate-Source Transient Voltage, tP < 1µs, t < 10 Hours over Lifetime		V_{GS}	-11/+25	V
Continuous Drain Current (Notes 5, 6)	T _C = +25°C T _C = +100°C	I _D	100 71	А
Continuous Diode Forward Current (Note 5)			76	Α
Pulsed Source Current (10µs Pulse, Duty Cycle = 1%) (Note 5)		lsм	280	Α
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%) (Note 5)		I _{DM}	280	Α

Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit	
Total Power Dissipation (Note 5)	$T_C = +25^{\circ}C$	0	349	W	
Total Power Dissipation (Note 5)	$T_C = +100^{\circ}C$	PD	174		
Thermal Resistance, Junction to Ambient (Note 7)		Reja	26.6	°C/W	
Thermal Resistance, Junction to Case (Note 5)		Rejc	0.43	*C/VV	
Operating and Storage Temperature Range		T_J, T_{STG}	-55 to +175	°C	

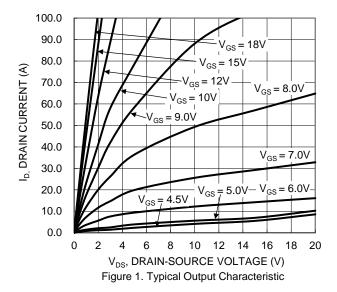
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 8)							
Drain-Source Breakdown Voltage	BVDSS	1200	_	_	V	V _G S = 0, I _D = 100μA	
Zero Gate Voltage Drain Current	I _{DSS}	_	_	50	μΑ	V _{DS} = 1200V, V _{GS} = 0	
Gate-Source Leakage	Igss	_	_	±250	nA	Vgs = +18/-4V, Vps = 0	
ON CHARACTERISTICS (Note 8)							
Gate Threshold Voltage	Vgs(TH)	1.8	2.5	3.6	V	$V_{DS} = V_{GS}$, $I_D = 17.7 \text{mA}$	
Static Drain-Source On-Resistance	D	_	17	23	mΩ	Vgs = 18V, ID = 50A	
Static Dialii-Source Oil-Resistance	R _{DS(ON)}	_	20	_	mΩ	V _G S = 15V, I _D = 50A	
Diode Forward Voltage	V_{SD}	_	3.8	_	V	V _{GS} = -4V, I _S = 25A	
Transconductance	gfs	_	13	_	S	V _{DS} = 20V, I _D = 50A	
DYNAMIC CHARACTERISTICS (Note 9)							
Input Capacitance	Ciss	-	3962	_	pF	V _{GS} = 0, V _{DS} = 1000V V _{AC} = 25mV, f = 1MHz	
Output Capacitance	Coss	_	184	_			
Reverse Transfer Capacitance	Crss	_	9.1	_			
Coss Stored Energy	Eoss	_	110	_	μJ		
Turn-On Switching Energy (Body Diode Forward)	Eon	_	676	_	1	$V_{GS} = -4V/+18V, V_{DS} = 800V$	
Turn-Off Switching Energy (Body Diode Forward)	E _{OFF}	-	486	_	μJ	$R_g = 5\Omega$, $I_D = 50A$, $L = 157\mu F$	
Gate Resistance	R_g	_	1.2	_	Ω	$V_{AC} = 100 \text{mV}, f = 1 \text{MHz}$	
Total Gate Charge	Q_g	_	217	_		V _{GS} = -4V/+18V, V _{DS} = 800V I _D = 50A	
Gate-Source Charge	Qgs	_	60.4	_	nC		
Gate-Drain Charge	Q_{gd}	_	53.4	_		ID = 30A	
Turn-On Delay Time	td(ON)	_	21.0	_		$V_{GS} = -4V/+18V, \ V_{DS} = 800V$ $R_g = 5\Omega, \ I_D = 50A$ Inductive Load	
Turn-On Rise Time	t _R	_	39.5	_			
Turn-Off Delay Time	t _{D(OFF)}	_	51.4	_	ns		
Turn-Off Fall Time	tF		14.3	_			
Body Diode Reverse-Recovery Time	t _{RR}	_	23.1	_	ns	\/ 4\/ \/ 000\/	
Body Diode Reverse-Recovery Charge	Q_{RR}		424	_	nC	V _G S = -4V, V _D S = 800V - I _D = 50A, di/dt = 2600A/µs	
Body Diode Reverse-Recovery Current	IRRM	_	29.7	_	Α	10 = 30A, α/αι = 2000A/μS	

Notes:

- 5. Device mounted on an infinite heatsink.
- 6. Drain current limited by maximum junction temperature.
- 7. Device mounted on FR-4 substrate PC board, 2oz. copper, with minimum recommended pad layout.
- 8. Short duration pulse test used to minimize self-heating effect.
- 9. Guaranteed by design. Not subject to production testing.





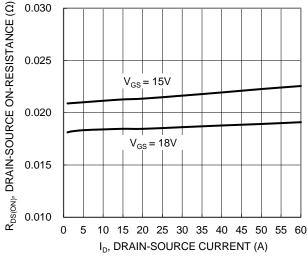


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

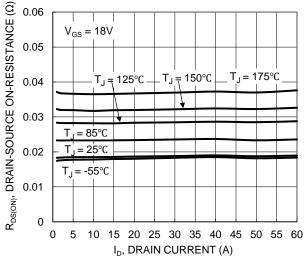


Figure 5. Typical On-Resistance vs. Drain Current and Temperature

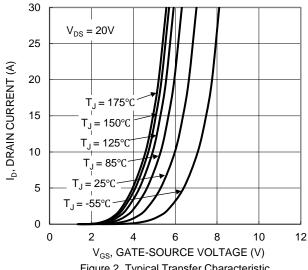


Figure 2. Typical Transfer Characteristic

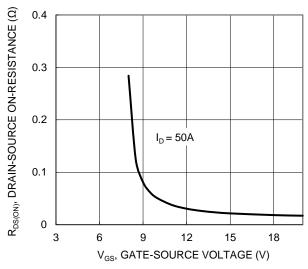


Figure 4. Typical Transfer Characteristic

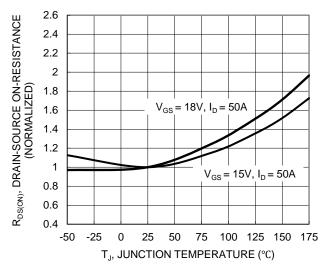


Figure 6. On-Resistance Variation with Junction Temperature



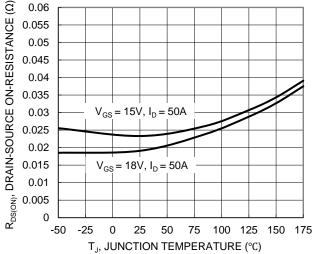


Figure 7. On-Resistance Variation with Junction Temperature

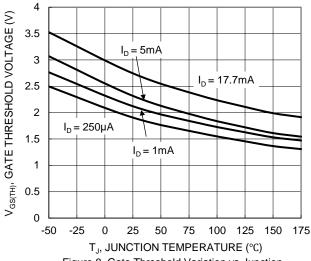


Figure 8. Gate Threshold Variation vs Junction Temperature

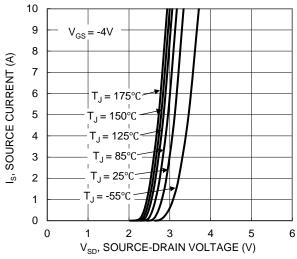


Figure 9. Diode Forward Voltage vs. Current

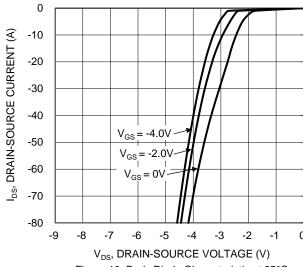


Figure 10. Body Diode Characteristic at 25°C

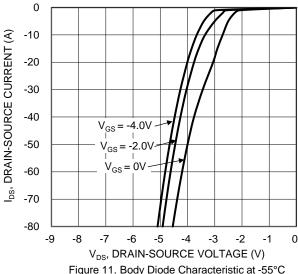


Figure 11. Body Diode Characteristic at -55°C

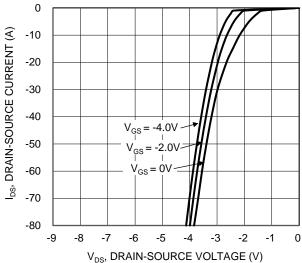


Figure 12. Body Diode Characteristic at 175°C





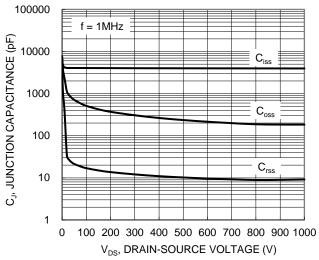


Figure 13. Typical Junction Capacitance

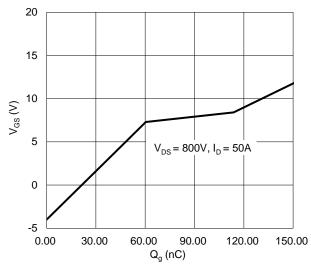


Figure 14. Gate Charge

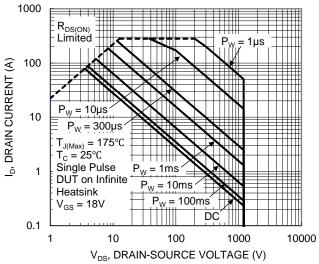


Figure 15. SOA, Safe Operation Area

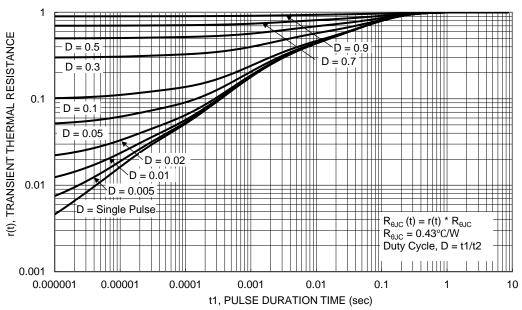


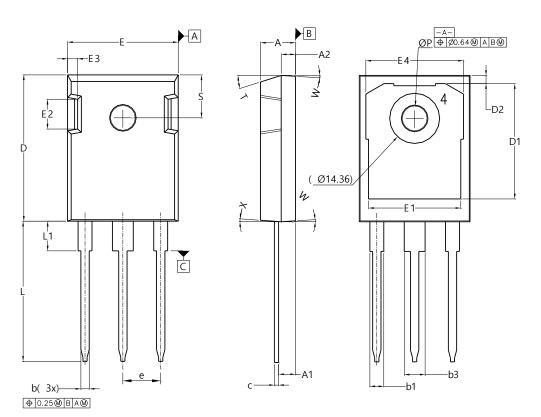
Figure 16. Transient Thermal Resistance



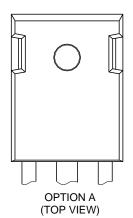
Package Outline Dimensions

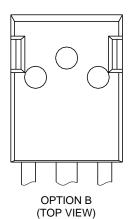
Please see http://www.diodes.com/package-outlines.html for the latest version.

TO247 Standard



TO247 Standard					
Dim	Min	Max	Тур		
Α	4.83	5.21			
A1	2.10	2.54			
A2	1.88	2.16			
b	1.07	1.33			
b1	1.90	2.41			
b3	2.87	3.38			
С	0.51	0.76	0.60		
D	20.80	21.75			
D1	15.88	17.65			
D2	0.95	1.77			
E	15.75	16.25			
E1	12.38	14.52			
E2	3.68	5.10			
E3	1.00	2.18			
E4	13.10	14.52			
е	5.44 BSC				
L	19.60	20.32			
L1	3.78	4.40			
PØ	2.90	3.65			
S	6.04	6.80			
Т	17.5-20° REF				
W	3.5-4.5° REF				
Х	4-5° REF				
All Dimensions in mm					







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