



1200V N-CHANNEL SILICON CARBIDE POWER MOSFET

Product Summary

BV _{DSS}	Rds(on) Max	I _D Tc = +25°C	
1200V	97.5mΩ @V _{GS} = 15V	41A	

Description and Applications

This SiC MOSFET is designed to minimize the on-state resistance yet maintain superior switching performance, making it ideal for high-efficiency power-management applications.

- EV high-power DC-DC converters
- EV charging systems
- AC-DC traction inverters
- Automotive motor drivers

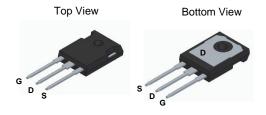
Features and Benefits

- Low On-Resistance
- High BV_{DSS} Rating for Power Application
- Low Input Capacitance
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- An automotive-compliant part is available under separate datasheet (DMWSH120H90SM3Q)

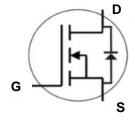
Mechanical Data

- Package: TO247
- Package Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Terminal Connections: See Diagram
- Terminals: Finish Matte Tin Annealed over Copper Leadframe.
 Solderable per MIL-STD-202, Method 208@3
- Weight: 5.6 grams (Approximate)

TO247 Standard







Internal Schematic

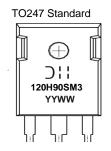
Ordering Information (Note 4)

Part Number	Paakaga	Packing		
Part Number	Package	Qty.	Carrier	
DMWSH120H90SM3	TO247 Standard	30 Pieces	Tube	

Notes:

- 1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



☐ I = Manufacturer's Marking

120H90SM3 = Product Type Marking Code

YYWW or YYWW = Date Code Marking

YY or YY = Last Two Digits of Year (ex: 24 = 2024)

WW or WW = Week Code (01 to 53)



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Drain-Source Voltage	V _{DSS}	1200	V	
Gate-Source Voltage (Dynamic)	Vgss	+19/-8	V	
Gate-Source Voltage (Static)	Vgss	+15/-4	V	
Continuous Drain Current (Notes 5, 9)	T _C = +25°C T _C = +100°C	I _D	41 29	А
Continuous Diode Forward Current (Note 5)	Is	42	А	
Pulsed Source Current (Pulse Width tp Limited by TJ Max) (Note 5)	Ism	85	А	
Pulsed Drain Current (Pulse Width t _P Limited by T _{J Max}) (Note 5)	I _{DM}	85	Α	

Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit		
Total Power Dissipation (Note 5)	T _C = +25°C	Pp	246	W	
Total Fower Dissipation (Note 3)	$T_C = +100^{\circ}C$	Pυ	123	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	
Thermal Resistance, Junction to Ambient (Note 6)	Reja	30	°C/W		
Thermal Resistance, Junction to Case (Note 5)	Rejc	0.61	C/VV		
Operating and Storage Temperature Range	TJ, TSTG	-55 to +175	°C		

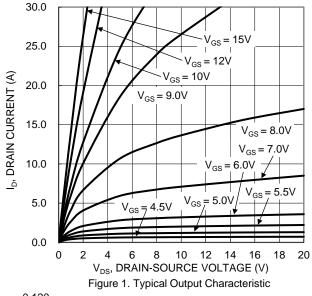
Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 8)							
Drain-Source Breakdown Voltage	BV _{DSS}	1200	_		V	$V_{GS} = 0V, I_{D} = 100\mu A$	
Zero Gate Voltage Drain Current	I _{DSS}	_	_	100	μA	V _{DS} = 1200V, V _{GS} = 0V	
Gate-Source Leakage	Igss		_	±200	nA	$V_{GS} = +15/-4V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 8)							
Gate Threshold Voltage	Vgs(TH)	1.7	2.5	3.5	V	$V_{DS} = V_{GS}$, $I_D = 5mA$	
Static Drain-Source On-Resistance	R _{DS(ON)}	-	75	97.5	mΩ	$V_{GS} = 15V, I_D = 20A$	
Diode Forward Voltage	VsD		4.3	_	V	VGS = -4V, IS = 10A	
Transconductance	gfs	_	4.5	_	S	VDS = 20V, ID = 20A	
DYNAMIC CHARACTERISTICS (Note 7)							
Input Capacitance	C _{iss}		1090				
Output Capacitance	Coss	1	59	_	pF	$V_{GS} = 0V$, $V_{DS} = 1000V$, $V_{AC} = 25mV$, $f = 1MHz$	
Reverse Transfer Capacitance	Crss	_	4.72	_			
Coss Stored Energy	Eoss	_	33.9	_	μJ		
Turn-On Switching Energy (Body Diode Forward)	Eon	_	175	_	1	$V_{GS} = -4V/+15V$, $V_{DS} = 800V$,	
Turn-Off Switching Energy (Body Diode Forward)	Eoff	_	69	_	μJ	$R_g = 5\Omega$, $I_D = 20A$, $L = 156\mu H$	
Gate Resistance	Rg	_	2.5	_	Ω	V _{AC} = 100mV, f = 1MHz	
Total Gate Charge	Qg	_	50.9	_		$V_{GS} = -4V/+15V, V_{DS} = 800V,$ $I_{D} = 20A$	
Gate-Source Charge	Qgs	_	16.2	_	nC		
Gate-Drain Charge	Qgd	_	18.8	_		10 - 20A	
Turn-On Delay Time	td(ON)	_	10.1	_		$V_{GS} = -4V/+15V$, $V_{DD} = 800V$, $R_g = 5\Omega$, $I_D = 20A$, Inductive Load	
Turn-On Rise Time	t _R	_	20.0	_			
Turn-Off Delay Time	tD(OFF)	_	18.0	_	ns		
Turn-Off Fall Time	tF		7.1			madouve Load	
Body Diode Reverse-Recovery Time	t _{RR}	_	11.8	_	ns	V 4V V 200V	
Body Diode Reverse-Recovery Charge	Q _{RR}		162	_	nC	$V_{GS} = -4V$, $V_{DS} = 800V$,	
Body Diode Reverse-Recovery Current	IRRM	_	22.8	_	Α	IF = 20A, di/dt = 3600A/µs	

Notes:

- 5. Device mounted on an infinite heatsink.
- 6. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
- Guaranteed by design. Not subject to production testing.
 Short duration pulse test used to minimize self-heating effect.
- 9. Drain current limited by maximum junction temperature.





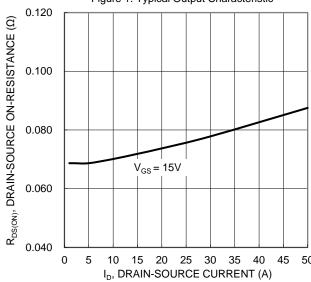


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

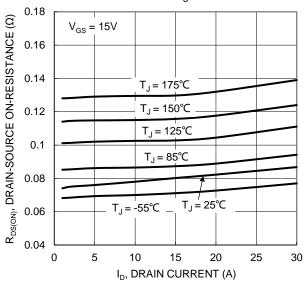


Figure 5. Typical On-Resistance vs. Drain Current and Junction Temperature

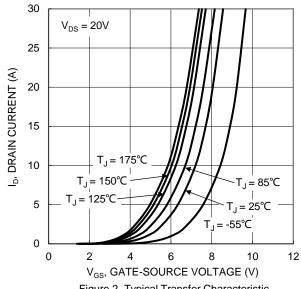


Figure 2. Typical Transfer Characteristic

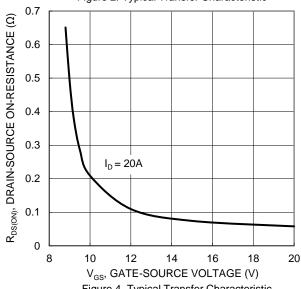


Figure 4. Typical Transfer Characteristic

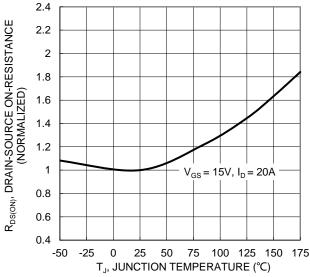


Figure 6. On-Resistance Variation with Junction Temperature





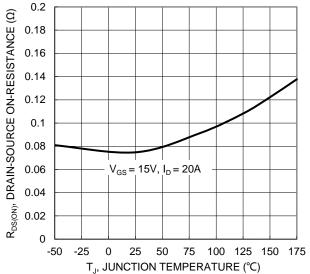


Figure 7. On-Resistance Variation with Junction Temperature

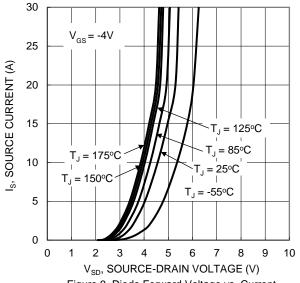
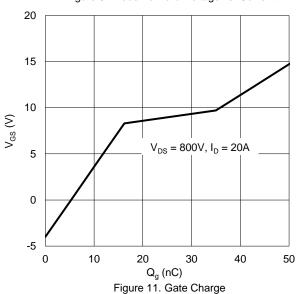
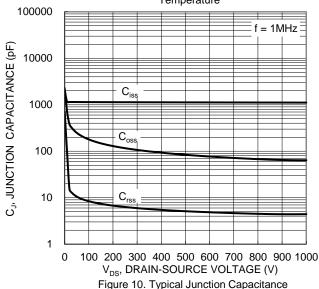


Figure 9. Diode Forward Voltage vs. Current



4 V_{GS(TH)}, GATE THRESHOLD VOLTAGE (V) 3.5 3 2.5 $I_D = 1mA$ 2 1.5 $I_{D} = 250 \mu A$ 1 0.5 0 -50 -25 25 50 75 100 125 150 175 T_J , JUNCTION TEMPERATURE (°C)

Figure 8. Gate Threshold Variation vs. Junction Temperature



1000 R_{DS(ON)} Limited 100 ID, DRAIN CURRENT (A) 10 T_C = 25°C Single Pulse DUT on Infinite Heatsink $V_{GS} = 15V$ 100 1000 10000 V_{DS}, DRAIN-SOURCE VOLTAGE (V) Figure 12. SOA, Safe Operation Area



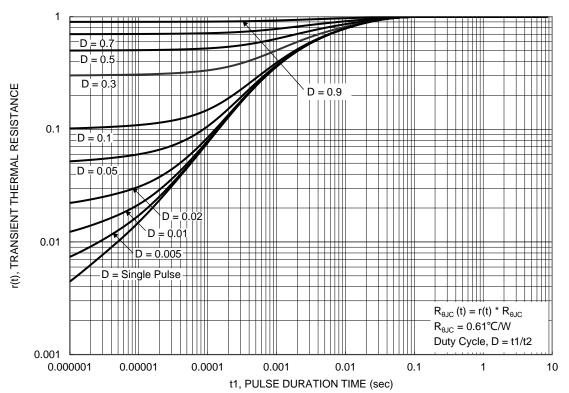


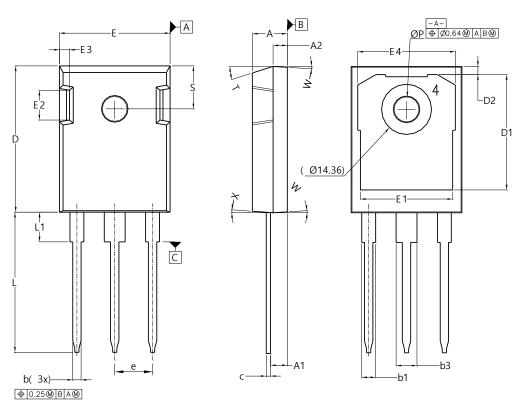
Figure 13. Transient Thermal Resistance



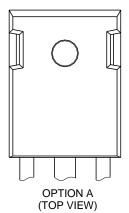
Package Outline Dimensions

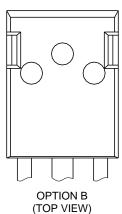
Please see http://www.diodes.com/package-outlines.html for the latest version.

TO247 Standard



TO247 Standard						
Dim	Min	Max	Тур			
Α	4.83	5.21				
A1	2.10	2.54				
A2	1.88	2.16				
b	1.07	1.33				
b1	1.90	2.41				
b3	2.87	3.38				
С	0.51	0.76	0.60			
D	20.80	21.75				
D1	15.88	17.65				
D2	0.95	1.77				
Е	15.75	16.25				
E1	12.38	14.52				
E2	3.68	5.10				
E3	1.00	2.18				
E4	13.10	14.52				
е	5	.44 BSC	;			
L	19.60	20.32				
L1	3.78	4.40				
PØ	2.90	3.65				
S	6.04	6.80				
Т	17.5-20° REF					
W	3.5-4.5° REF					
Х	4-5° REF					
All Dimensions in mm						







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