

12V 9A GATE DRIVER IN SOT26

Description

The ZXGD3001E6Q is a high-speed non-inverting single MOSFET gate driver capable of driving up to 9A into a MOSFET or IGBT gate capacitive load from supply voltages up to 12V with typical propagation delay times down to 3ns and rise/fall times down to 11ns. This device ensures rapid switching of the power MOSFET or IGBT to minimize power losses and distortion in high-current fast-switching applications.

The ZXGD3001E6Q is inherently rugged to latchup and shootthrough. Its wide supply voltage range allows full enhancement to minimize on-losses of the power MOSFET or IGBT.

Its low-input voltage requirement and high current gain allows high current driving from low voltage controller ICs.

The optimized pinout SOT26 package with separate source and sink pins eases board layout, enabling reduced parasitic inductance and independent control of rise and fall slew rates.

Applications

Power MOSFET and IGBT gate driving in

- Synchronous switch-mode power supplies
- Secondary side synchronous rectification
- Plasma display panel power modules
- 1, 2, and 3-phase motor control circuits
- Audio switching amplifier power output stages

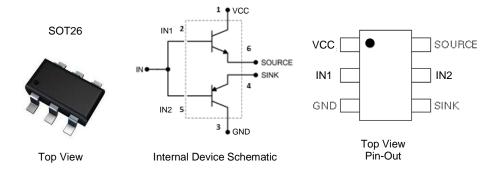
Features

- 12V Operating Voltage Range
- 9A Peak Output Current
- Fast-Switching Emitter-Follower Configuration
 - 3ns Propagation Delay Time
 - 11ns Rise/Fall Time, 1000pF Load
- Low-Input Current Requirement
 - 4.2A (Source)/2.2A (Sink) Output Current from 10mA Input
- SOT26 Package
- Separate Source and Sink Outputs for Independent Control of Rise and Fall Time
- Optimized Pinout to Ease Board Layout and Minimize Trace Inductance
- No Latchup
- No Shoot-Through
- Near-Zero Quiescent and Output Leakage Current
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- The ZXGD3001E6Q is suitable for automotive applications requiring specific change control; this part is AEC-Q101 qualified, PPAP capable, and manufactured in IATF16949 certified facilities.

https://www.diodes.com/quality/product-definitions/

Mechanical Data

- Package: SOT26
- Package Material: Molded Plastic. "Green" Molding Compound; UL Flammability Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Plated Leads, Solderable per MIL-STD-202, Method 208 @3
- Weight: 0.018 grams (Approximate)



Pin Name	Pin Function
VCC	Driver Supply
IN1/IN2	Driver inputs are normally connected together by circuit tracks
GND	Ground
SOURCE	Source Current Output
SINK	Sink Current Output

Ordering Information (Notes 4)

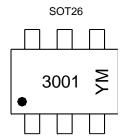
Part Number	Package	Marking	Reel Size (inches)	Reel Size (inches) Tape Width (mm)		Packing		
Part Number	Fackage	Warking	Reel Size (Iliches)	rape widin (ililii)	Qty.	Carrier		
ZXGD3001E6QTA	SOT26	3001	7	8 embossed	3000	Reel		

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com/design/support/pakaging/diodes-packaging/.



Marking Information

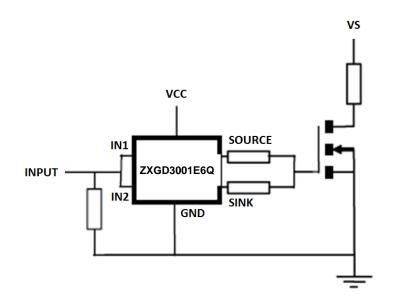


 $\begin{array}{l} 3001 = Product\ Type\ Marking\ Code \\ YM = Date\ Code\ Marking \\ Y\ or\ \overline{Y} = Year\ (ex:\ L=2024) \\ M\ or\ \overline{M} = Month\ (ex:\ 9=September) \end{array}$

Date Code Key

Year	2024	2025	2026	2027	2028	2029	2030	2031	2032	2033	2034	2035
Code	L	М	N	Р	R	S	Т	U	V	W	Х	Υ
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec

Typical Application Circuit





Absolute Maximum Ratings (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Supply Voltage	Vcc	12	V
Input Voltage	V _{IN}	12	V
Peak Sink Current	I(sink)PK	9	V
Source Current @ I _{IN1} + I _{IN2} = 10mA	I _(source)	4.2	Α
Sink Current @ I _{IN1} + I _{IN2} = 10mA	I _(sink)	2.2	Α
Input Current (c)	I _{IN1} , I _{IN2}	1	Α

Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Power Dissipation (Notes 5 & 6) Linear Derating Factor	PD	1.1 8.8	W mW/°C
Thermal Resistance, Junction to Ambient (Notes 5 & 6)	Reja	113	°C/W
Thermal Resistance, Junction to Lead (Note 7)	Rejl	105	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

ESD Ratings (Note 8)

Characteristic	Symbol	Value	Unit	JEDEC Class
Electrostatic Discharge – Human Body Model	ESD HBM	1500	V	1C
Electrostatic Discharge – Charged Device Model	ESD CDM	1000	V	IV

Notes:

- 5. For a device mounted on 25mm × 25mm 1oz copper that is on a single-sided 1.6mm FR4 PCB; device is measured under still air conditions whilst operating in a steady-state. The heatsink is split in half with the pin 1 (VCC) and pin 3 (GND) connected separately to each half.

 6. For device with two active dies running at equal power.
- 8. To device with this dark disc standing at equal point.

 7. Thermal resistance from junction to solder-point at the end of each lead on pin 1 (VCC) and pin 3 (GND).

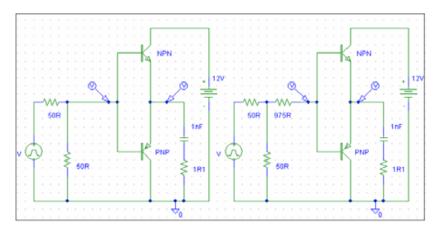
 8. Refer to JEDEC specification JESD22-A114, JESD22-A115, and JESD22-C101.

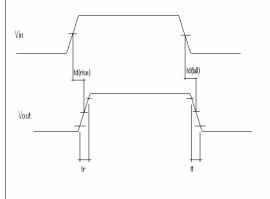


Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Parameter	Symbol	Min	Тур	Max	Unit	Conditions
Output Voltage, High	Vон		Vcc 0.4		V	Isource = 1µA
Output Voltage, Low	Vol	_	0.4	_	V	Isink = 1µA
Source Output Leakage Current	I _{L(source)}	_	_	1	μA	$V_{CC} = 12V$ $V_{IN1} = V_{IN2} = 0V$
Sink Output Leakage Current	I _L (sink)	_	_	1	μA	V _{CC} = 12V V _{IN1} = V _{IN2} = V _{CC}
Quiescent Current	IQ	_	_	50	nA	Vcc = 9.6V V _{IN1} = V _{IN2} = 0V
Source Output Current	I(source)	1	1.7	_	Α	I _{IN1} + I _{IN2} = 2.5mA
Sink Output Current	I _(sink)	0.7	1.1	_	А	$I_{IN1} + I_{IN2} = 2.5 \text{mA}$
Source Output Current	I(source)	2.7	4.2	_	А	I _{IN1} + I _{IN2} = 10mA
Sink Output Current	I(sink)	1.5	2.2	_	Α	I _{IN1} + I _{IN2} = 10mA
Source Output Current	I _{(source)PK}	_	9	_	Α	$I_{IN1} + I_{IN2} = 1A$
Sink Output Current	I(sink)PK	_	9	_	Α	I _{IN1} + I _{IN2} = 1A
Gate Driver Switching Times	$t_{d(rise)} \ t_{r} \ t_{d(fall)} \ t_{f}$	_ _ _	1.3 7.3 3 11		ns	$C_L = 1 nF, R_L = 1\Omega, V_{CC} = 8V$ $V_{IN} = 6V, R_S = 25\Omega$
Gate Driver Switching Times	td(rise) t _r td(fall) tf	_ _ _ _	9 141.5 14 151		ns	$C_L = 1nF, R_L = 1\Omega, V_{CC} = 8V$ $V_{IN} = 6V, R_S = 1k\Omega$

Switching Test Circuit and Timing Diagram







Typical Switching Characteristics (@TA = +25°C, unless otherwise specified.)

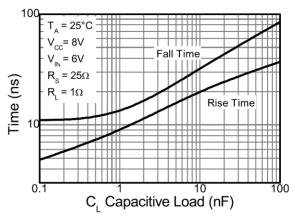


Figure 1. Rise and Fall Time

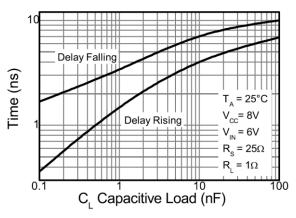


Figure 2. Propagation Delay

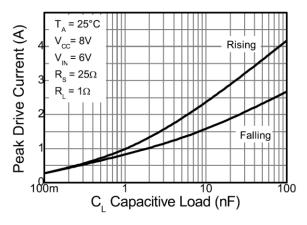


Figure 3. Peak Drive Current

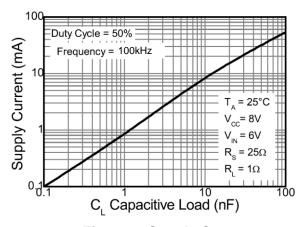


Figure 4. Supply Current

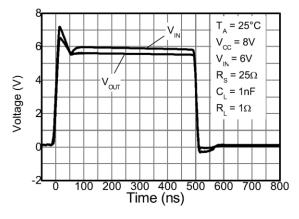


Figure 5. Switching Speed

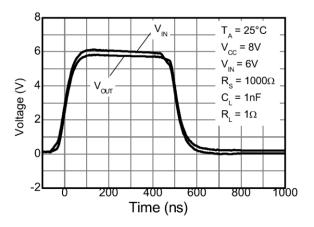


Figure 6. Switching Speed



Typical Switching Characteristics (@TA = +25°C, unless otherwise specified.) (continued)

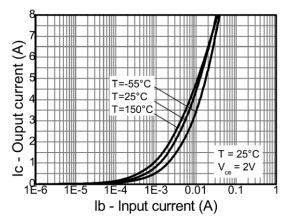


Figure 7. Source Current vs. Input Current

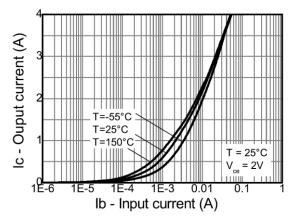


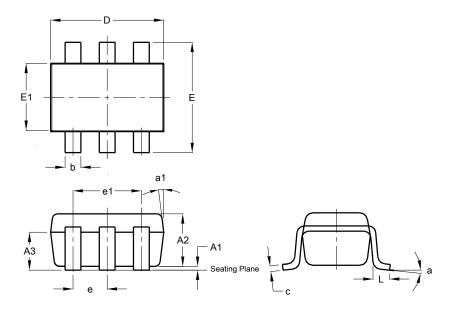
Figure 8. Sink Current vs. Input Current



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT26

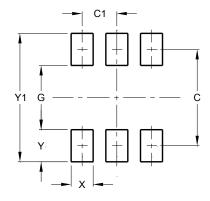


	SOT26						
Dim	Min	Max	Тур				
A1	0.013	0.10	0.05				
A2	1.00	1.30	1.10				
А3	0.70	0.80	0.75				
b	0.35	0.50	0.38				
С	0.10	0.20	0.15				
D	2.90	3.10	3.00				
е	-	-	0.95				
e1	-	-	1.90				
Е	2.70	3.00	2.80				
E1	1.50	1.70	1.60				
L	0.35	0.55	0.40				
а	-	-	8°				
a1	-	-	7°				
All	All Dimensions in mm						

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT26



Dimensions	Value (in mm)
С	2.40
C1	0.95
G	1.60
Х	0.55
Υ	0.80
Y1	3.20



IMPORTANT NOTICE

- 1. DIODES INCORPORATED (Diodes) AND ITS SUBSIDIARIES MAKE NO WARRANTY OF ANY KIND, EXPRESS OR IMPLIED, WITH REGARDS TO ANY INFORMATION CONTAINED IN THIS DOCUMENT, INCLUDING, BUT NOT LIMITED TO, THE IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE OR NON-INFRINGEMENT OF THIRD PARTY INTELLECTUAL PROPERTY RIGHTS (AND THEIR EQUIVALENTS UNDER THE LAWS OF ANY JURISDICTION).
- 2. The Information contained herein is for informational purpose only and is provided only to illustrate the operation of Diodes' products described herein and application examples. Diodes does not assume any liability arising out of the application or use of this document or any product described herein. This document is intended for skilled and technically trained engineering customers and users who design with Diodes' products. Diodes' products may be used to facilitate safety-related applications; however, in all instances customers and users are responsible for (a) selecting the appropriate Diodes products for their applications, (b) evaluating the suitability of Diodes' products for their intended applications, (c) ensuring their applications, which incorporate Diodes' products, comply the applicable legal and regulatory requirements as well as safety and functional-safety related standards, and (d) ensuring they design with appropriate safeguards (including testing, validation, quality control techniques, redundancy, malfunction prevention, and appropriate treatment for aging degradation) to minimize the risks associated with their applications.
- 3. Diodes assumes no liability for any application-related information, support, assistance or feedback that may be provided by Diodes from time to time. Any customer or user of this document or products described herein will assume all risks and liabilities associated with such use, and will hold Diodes and all companies whose products are represented herein or on Diodes' websites, harmless against all damages and liabilities.
- 4. Products described herein may be covered by one or more United States, international or foreign patents and pending patent applications. Product names and markings noted herein may also be covered by one or more United States, international or foreign trademarks and trademark applications. Diodes does not convey any license under any of its intellectual property rights or the rights of any third parties (including third parties whose products and services may be described in this document or on Diodes' website) under this document.
- 5. Diodes' products are provided subject to Diodes' Standard Terms and Conditions of Sale (https://www.diodes.com/about/company/terms-and-conditions/terms-and-conditions-of-sales/) or other applicable terms. This document does not alter or expand the applicable warranties provided by Diodes. Diodes does not warrant or accept any liability whatsoever in respect of any products purchased through unauthorized sales channel.
- 6. Diodes' products and technology may not be used for or incorporated into any products or systems whose manufacture, use or sale is prohibited under any applicable laws and regulations. Should customers or users use Diodes' products in contravention of any applicable laws or regulations, or for any unintended or unauthorized application, customers and users will (a) be solely responsible for any damages, losses or penalties arising in connection therewith or as a result thereof, and (b) indemnify and hold Diodes and its representatives and agents harmless against any and all claims, damages, expenses, and attorney fees arising out of, directly or indirectly, any claim relating to any noncompliance with the applicable laws and regulations, as well as any unintended or unauthorized application.
- 7. While efforts have been made to ensure the information contained in this document is accurate, complete and current, it may contain technical inaccuracies, omissions and typographical errors. Diodes does not warrant that information contained in this document is error-free and Diodes is under no obligation to update or otherwise correct this information. Notwithstanding the foregoing, Diodes reserves the right to make modifications, enhancements, improvements, corrections or other changes without further notice to this document and any product described herein. This document is written in English but may be translated into multiple languages for reference. Only the English version of this document is the final and determinative format released by Diodes.
- 8. Any unauthorized copying, modification, distribution, transmission, display or other use of this document (or any portion hereof) is prohibited. Diodes assumes no responsibility for any losses incurred by the customers or users or any third parties arising from any such unauthorized use.
- 9. This Notice may be periodically updated with the most recent version available at https://www.diodes.com/about/company/terms-and-conditions/important-notice

The Diodes logo is a registered trademark of Diodes Incorporated in the United States and other countries. All other trademarks are the property of their respective owners.

© 2024 Diodes Incorporated. All Rights Reserved.

www.diodes.com