



40V P-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{DSS}	R _{DS(on)} Max	I _D T _A = +25°C	
-40V	60mΩ @ V _{GS} = -10V	-6.4A	
-40 V	100mΩ @ V _{GS} = -4.5V	-5.6A	

Description and Applications

This MOSFET is designed to meet the stringent requirements of automotive applications. It is qualified to AEC-Q101, supported by a PPAP, and is ideal for use in:

- DC-DC converters
- Disconnect switches
- Audio output stages
- Motor controls

Features and Benefits

- Low On-Resistance
- Fast Switching Speed
- Low Threshold
- Low Gate Drive
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- The ZXMP4A16GQ is suitable for automotive applications requiring specific change control; this part is AEC-Q101 qualified, PPAP capable, and manufactured in IATF 16949 certified facilities.

https://www.diodes.com/quality/product-definitions/

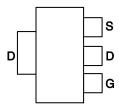
Mechanical Data

- Package: SOT223
- Package Material: Molded Plastic, UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Annealed over Copper Leadframe.
 Solderable per MIL-STD-202, Method 208 (2)
- Weight: 0.112 grams (Approximate)

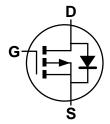
SOT223 (Type DN)



Top View



Pin Out - Top View



Equivalent Circuit

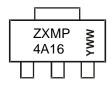
Ordering Information (Note 4)

Part Number	Paakaga	Packing		
Part Number	Package	Qty.	Carrier	
ZXMP4A16GQTA	SOT223 (Type DN)	1,000	Tape & Reel	
ZXMP4A16GQTC	SOT223 (Type DN)	4,000	Tape & Reel	

Notes:

- 1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



ZXMP4A16 = Product Type Marking Code YWW = Date Code Marking Y = Year (ex: 4 = 2024) WW = Week (01 to 53)



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			VDSS	-40	V
Gate-Source Voltage			Vgs	±20	V
		(Note 6)		-6.4	
Continuous Drain Current	Vgs = 10V	$T_A = +70^{\circ}C \text{ (Note 6)}$	ΙD	-4.6	Α
		(Note 5)		-1.7	
Pulsed Drain Current	V _G s= 10V	(Note 7)	I _{DM}	-21	Α
Continuous Source Current (Body Diode) (Note 6)		Is	-5.2	Α	
Pulsed Source Current (Body Diode) (Note 7)		Ism	-21	A	

Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit	
Power Dissipation	(Note 5)	0	2.0 16	W mW/°C	
Linear Derating Factor	(Note 6)	PD	3.9 31		
Thermal Resistance, Junction to Ambient	(Note 5)	Do	62.5	°C/W	
Thermal Resistance, Junction to Ambient	(Note 6)	Reja	32.2	C/VV	
Operating and Storage Temperature Range		T _J , T _{STG}	-55 to +150	°C	

Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Co	ondition
OFF CHARACTERISTICS	OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BVDSS	-40	_	_	V	I _D = -250μA, V _G S= 0V	
Zero Gate Voltage Drain Current	IDSS	_	_	-1	μΑ	V _{DS} = -40V, V _{GS} =	0V
Gate-Source Leakage	Igss	_	_	100	nA	V _G S= ±20V, V _D S=	0V
ON CHARACTERISTICS				•			
Gate Threshold Voltage	VGS(th)	-1.0		_	V	I _D = -250μA, V _D S=	Vgs
Static Drain-Source On-Resistance (Note 8)	0			60	mΩ	V _G S= -10V, I _D = -3	.8A
Static Drain-Source On-Resistance (Note 8)	R _{DS(ON)}	_	_	100	11177	Vgs= -4.5V, ID= -2	2.9A
Forward Transconductance (Notes 8 & 10)	g fs	_	8.85	_	S	V _{DS} = -15V, I _D = -3	.8A
Diode Forward Voltage (Note 8)	V _{SD}	_	-0.85	-1.2	V	T _J = +25°C , I _S = -	3.4A, V _{GS} = 0V
Reverse Recovery Time (Note 10)	trr	_	27.2	_	ns	T _J = +25°C , I _F = -3A, di/dt= 100A/µs	
Reverse Recovery Charge (Note 10)	Qrr	_	25.4	_	nC		
DYNAMIC CHARACTERISTICS (Note 10)							
Input Capacitance	Ciss	_	1007	_	pF		
Output Capacitance	Coss	_	130	_	pF	V _{DS} = -20V, V _{GS} = f = 1MHz	0V
Reverse Transfer Capacitance	Crss	_	85	_	pF	1 = 11011 12	
Total Gate Charge (Note 9)	Qg	_	13.6	_	nC	V _G s= -5V	
Total Gate Charge (Note 9)	Qg	_	26.1	_	nC	V _{DS} = -20V I _D = -3.8A	
Gate-Source Charge (Note 9)	Qgs	_	2.8	_	nC		
Gate-Drain Charge (Note 9)	Q _{gd}	_	4.8	_	nC		
Turn-On Delay Time (Note 9)	t _{D(on)}	_	3.0	_	ns	V_{DD} = -20V, V_{GS} = -10V, I_{D} = -1A, R_{G} \cong 6.0Ω	
Turn-On Rise Time (Note 9)	tr	_	3.5	_	ns		
Turn-Off Delay Time (Note 9)	tD(off)	_	13.4	_	ns		
Turn-Off Fall Time (Note 9)	tf	_	7.2	_	ns		

Notes: 5. For a device surface mounted on 25mm x 25mm FR4 PCB with high coverage of single sided 1oz copper, in still air conditions.

^{6.} For a device surface mounted on FR4 PCB measured at $t \le 10$ seconds.

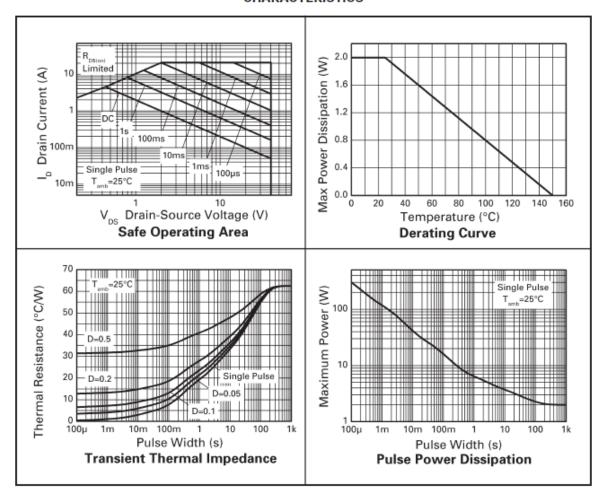
^{7.} For a device surface mounted on 25mm x 25mm FR4 PCB, D= 0.05 pulse width limited by maximum junction temperature.

^{8.} Measured under pulsed conditions. Width \leq 300µs. Duty cycle \leq 2%.

Switching characteristics are independent of operating junction temperature.
 For design aid only, not subject to production testing.

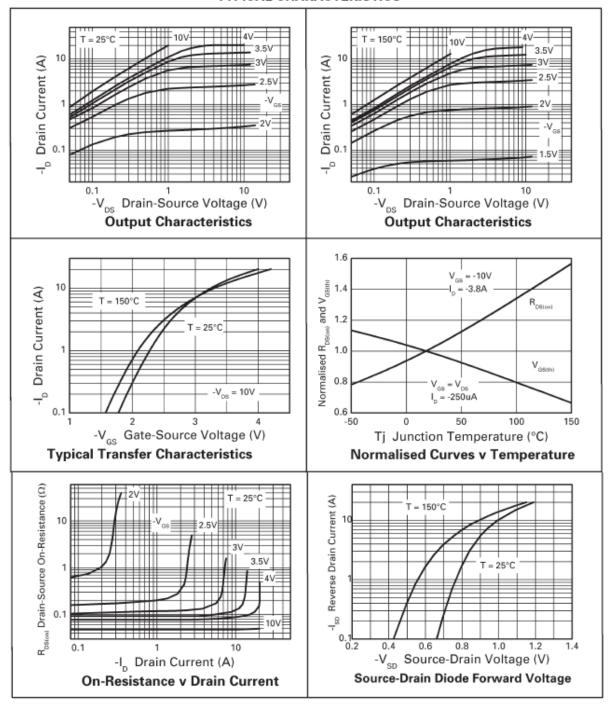


CHARACTERISTICS

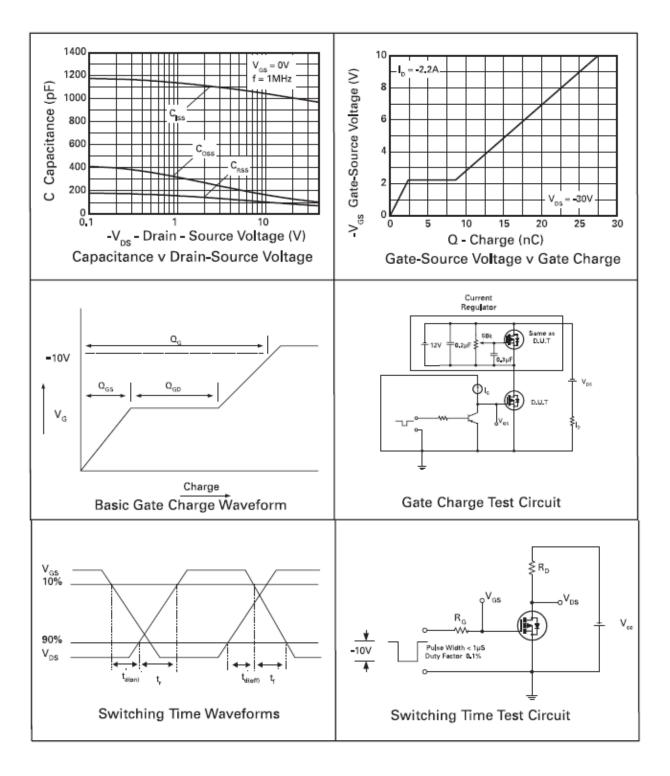




TYPICAL CHARACTERISTICS





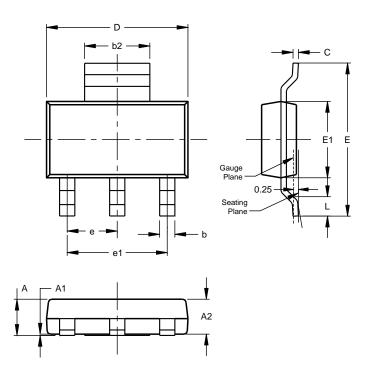




Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT223 (Type DN)

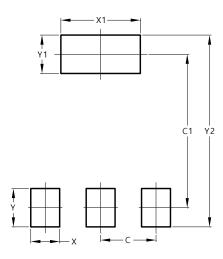


SC	SOT223 (Type DN)					
Dim	Min	Max	Тур			
Α		1.70	-			
A1	0.01	0.15				
A2	1.50	1.68	1.60			
b	0.60	0.80	0.70			
b2	2.90	3.10				
С	0.20	0.32				
D	6.30	6.70				
Е	6.70	7.30				
E1	3.30	3.70				
е			2.30			
e1			4.60			
L	0.85					
All Dimensions in mm						

Suggested Pad Layout

 $\label{prop:package-outlines.html} Please see \ http://www.diodes.com/package-outlines.html \ for \ the \ latest \ version.$

SOT223 (Type DN)



Dimensions	Value (in mm)
C	2.30
C1	6.40
Х	1.20
X1	3.30
Y	1.60
Y1	1.60
Y2	8 00



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