

# NOT RECOMMENDED FOR NEW DESIGN USE DMP3007SPSQ



DMP3015LSSQ

#### 30V P-CHANNEL ENHANCEMENT MODE MOSFET

## **Product Summary**

BVDSS	R <sub>DS(ON)</sub> Max	I <sub>D</sub> Max T <sub>A</sub> = +25°C
-30V	11mΩ @ V <sub>GS</sub> = -10V	-13A
-307	17mΩ @ V <sub>GS</sub> = -4.5V	-9.9A

### **Description**

This MOSFET is designed to minimize the on-state resistance (RDS(ON)) yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

- Backlighting
- Power management functions
- DC-DC converters

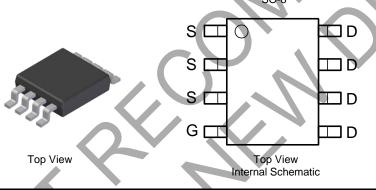
# **Features and Benefits**

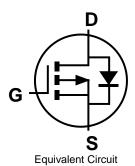
- Low On-Resistance
- Low Gate Threshold Voltage
- Low Input Capacitance
- · Fast Switching Speed
- Low Input/Output Leakage
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- The DIODES™ DMP3015LSSQ is suitable for automotive applications requiring specific change control; this part is AEC-Q101 qualified, PPAP capable, and manufactured in IATF16949 certified facilities.

https://www.diodes.com/quality/product-definitions/

#### **Mechanical Data**

- Package: SO-8
- Package Material: Molded Plastic, "Green" Molding Compound.
   UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals Connections: See Diagram
- Terminals: Finish—Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (€3)
- Weight: 0.074g (Approximate)





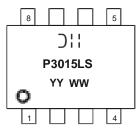
## Ordering Information (Note 4)

Part Number	Dankage	Pac	king
Part Number	Package	Qty.	Carrier
DMP3015LSSQ-13	SO-8	2500	Tape & Reel

Notes

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

# **Marking Information**



⊃∷ = Manufacturer's Marking
P3015LS = Product Type Marking Code
YYWW = Date Code Marking
YY = Year (ex: 22 = 2022)
WW = Week (01 to 53)



# **Maximum Ratings** (@ $T_A = +25^{\circ}C$ , unless otherwise specified.)

Characteristic			Symbol	Value	Units
Drain-Source Voltage			VDSS	-30	V
Gate-Source Voltage			Vgss	±20	V
Drain Current (Note 5)	Steady State	T <sub>A</sub> = +25°C T <sub>A</sub> = +70°C	lD	-13 -9.75	А
Pulsed Drain Current (Note 6)			I <sub>DM</sub>	-45	A

### **Thermal Characteristics**

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 5)	PD	2.5	W
Thermal Resistance, Junction to Ambient	Reja	50	°C/W
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

Notes: 5. Device mounted on 2oz. copper pads on FR-4 PCB with  $R_{\theta JA}$  = +50°C/W.

6. Pulse width ≤ 10µs, duty cycle ≤ 1%.

# Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

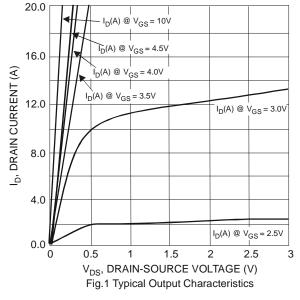
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
DFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BVDSS	-30			V	$V_{GS} = 0V, I_{D} = -250\mu A$
Zero Gate Voltage Drain Current	IDSS	7	_	-1	μΑ	$V_{DS} = -30V, V_{GS} = 0V$
Gate-Source Leakage	IGSS			±100	nA	$V_{GS} = \pm 20V$ , $V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	VGS(th)	-1	_	-2	V	$V_{DS} = V_{GS}$ , $I_D = -250\mu A$
Static Drain-Source On-Resistance	RDS(ON)	1	9	11	mΩ	Vgs = -10V, ID = -13A
Static Brain Godice on Resistance	INDS(ON)		14	17	11122	$V_{GS} = -4.5V, I_{D} = -10A$
Forward Transconductance	<b>g</b> fs	-	15	_	S	$V_{DS} = -15V, I_{D} = -8A$
Diode Forward Voltage (Note 7)	VsD	-0.5	_	-1.1	V	V <sub>G</sub> S = 0V, I <sub>S</sub> = -2.1A
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	Ciss	_	2,748	_	pF	
Output Capacitance	Coss	_	357	_	pF	V <sub>DS</sub> = -20V, V <sub>GS</sub> = 0V f = 1.0MHz
Reverse Transfer Capacitance	Crss	_	356	_	pF	1 – 1.000112
Gate Resistance	Rg	_	2.0	_	Ω	$V_{DS} = 0V$ , $V_{GS} = 0V$ f = 1.0MHz
SWITCHING CHARACTERISTICS (Note 8)						
Total Gate Charge	Qq	_	30.0	_		$V_{DS} = -10V$ , $V_{GS} = -4.5V$ , $I_{D} = -13A$
Total Cate Charge	Qу	_	60.4	_	nC	$V_{DS} = -10V$ , $V_{GS} = -10V$ , $I_{D} = -13A$
Gate-Source Charge	$Q_{gs}$	_	7.2	_	110	$V_{DS} = -10V$ , $V_{GS} = -10V$ , $I_{D} = -13A$
Gate-Drain Charge	$Q_{gd}$	_	16.4			V <sub>DS</sub> = -10V, V <sub>GS</sub> = -10V, I <sub>D</sub> = -13A
Turn-On Delay Time	t <sub>D(ON)</sub>	_	11.2	_		
Rise Time	t <sub>R</sub>	_	12.4	_		V <sub>DS</sub> = -15V, V <sub>GS</sub> = -10V
Turn-Off Delay Time	tD(OFF)	_	104.9	_	ns	$I_D = -1A$ , $R_G = 6.0\Omega$
Fall Time	t <sub>F</sub>		61.7	_		

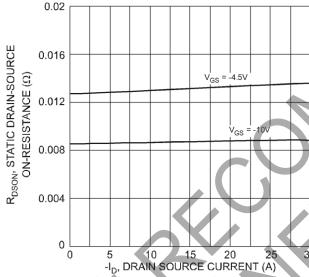
Notes: 7. Short duration pulse test used to minimize self-heating effect.

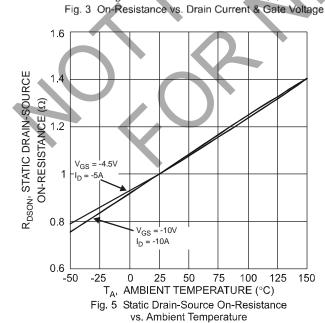
8. Guaranteed by design. Not subject to product testing.

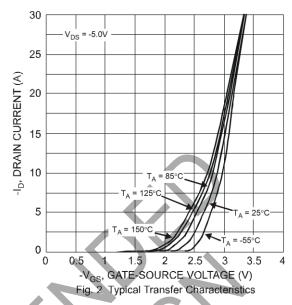












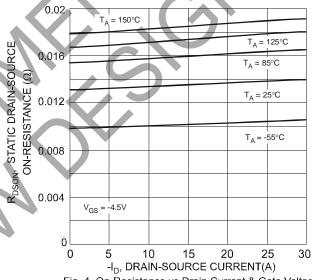
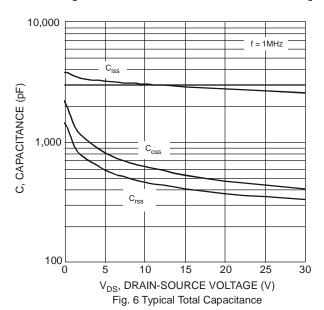


Fig. 4 On-Resistance vs Drain Current & Gate Voltage





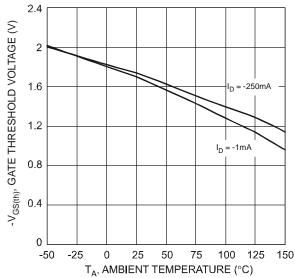
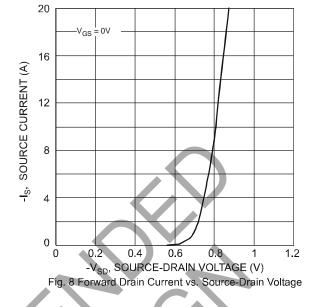
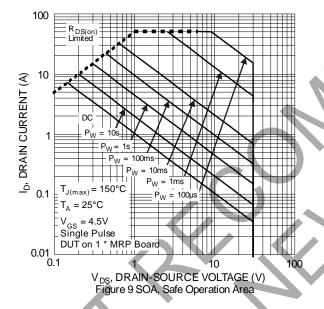
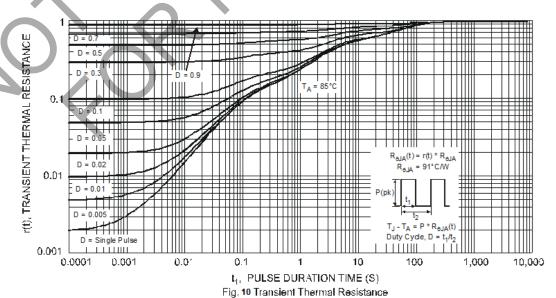


Fig. 7 Gate Threshold Variation vs. Ambient Temperature



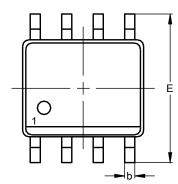


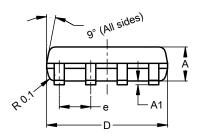


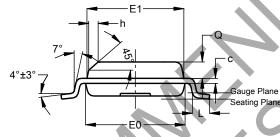


# **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.







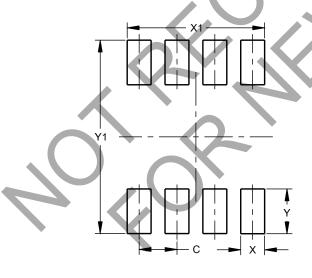
SO-8

**SO-8** 

SO-8				
Dim	Min	Max	Тур	
Α	1.40	1.50	1.45	
A1	0.10	0.20	0.15	
b	0.30	0.50	0.40	
C	0.15	0.25	0.20	
D	4.85	4.95	4.90	
E	5.90	6.10	6.00	
E1	3.80	3.90	3.85	
E0_	3.85	3.95	3.90	
е		_	1.27	
þ	_		0.35	
Ĺ	0.62	0.82	0.72	
ø	0.60	0.70	0.65	
All Dimensions in mm				

# **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.



Dimensions	Value (in mm)
С	1.27
Х	0.802
X1	4.612
Y	1.505
Y1	6.50



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