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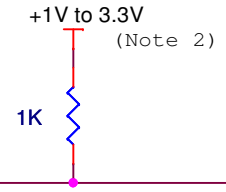
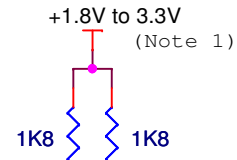
PI3HDX511F

Dual-mode DP Source



0.1u_0402

0.1u_0402

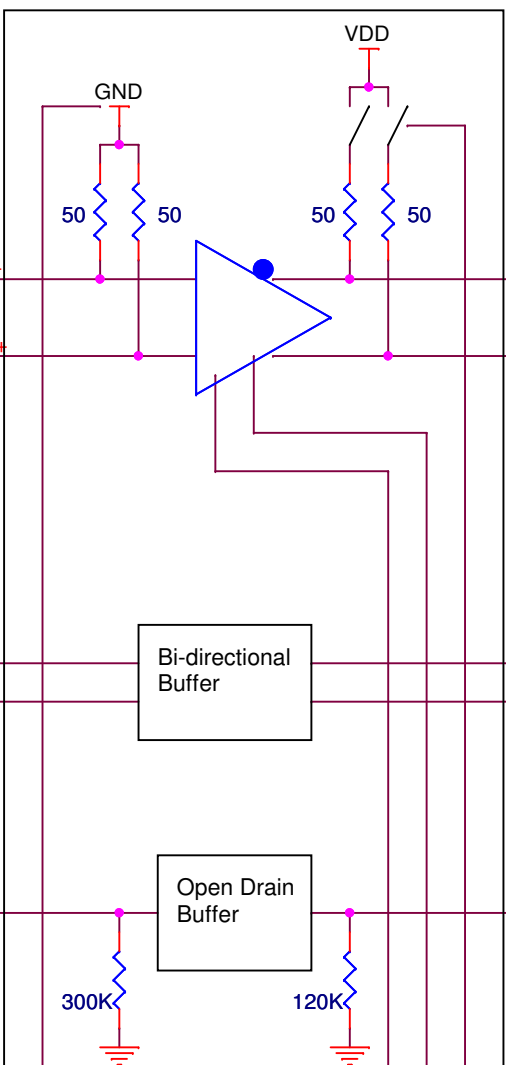


Note 1: It is assumed that the DDC VIL of Intel Chipset can accept the DDC VOL of PI3VDP1431A, which is $\leq 0.6V$.

Note 2: Please refer to Intel core processor documents.

Note 3: Per HDMI specification version 1.4, it is recommended that sources capable of higher speeds incorporate an effective amount of source termination, especially if using type C connectors.

VBIAS
 OC_S0
 EQ_S0, EQ_STEP
 ROUT_S0
 (Note 3)



IN DX-

IN DX+

SCL_SRC

SDA_SRC

HPD_SRC

OUT DX-

OUT DX+

SCL_SINK

SDA_SINK

HPD_SINK

Docking Connector

Title		
PI3HDX511F Source Application Diagram with 1.8V DDC Buffer		
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