



## New Product Announcement

LOGIC  
74AUP2Gxx

# Advanced Ultra-Low Power Logic 74AUP2Gxx

### Advanced Ultra Low Power CMOS Dual Gate Logic

- 0.8 V to 3.6 V
- 4mA drive capability at 3.0 V
- 7ns typical propagation time at 3V
- 7 popular dual gate functions

### Features:

#### Low Power

- Low Static Power Consumption  
 $I_{CC}$  Less than 0.9  $\mu$ A
- Low Dynamic Power Consumption  
 $C_{PD} = 6$  pF (Typical at 3.3 V)

#### Schmitt Trigger on all inputs.

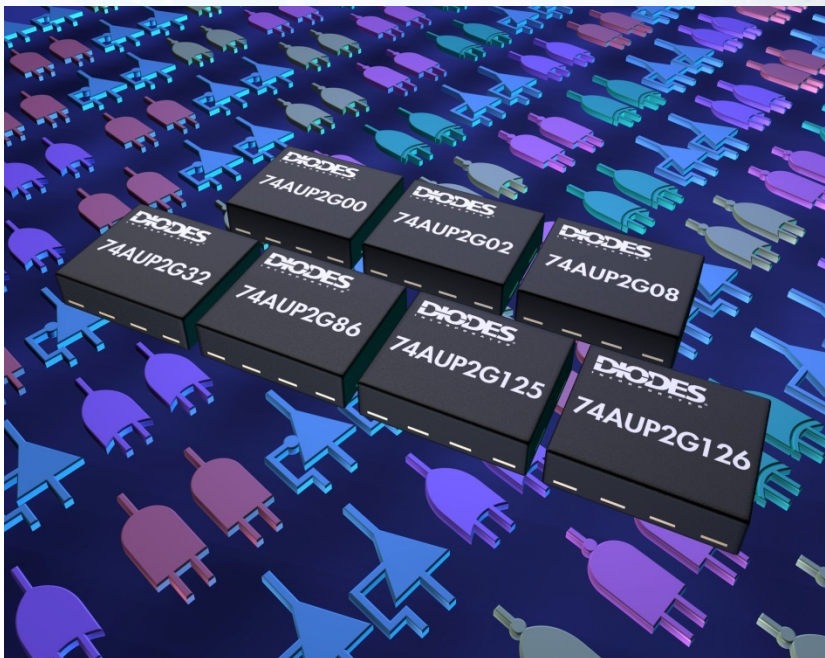
- Rejects noisy signals
- Tolerant of Slow rise/fall times
- Hysteresis typically 250 mV

### loff Circuitry

- Prevents output backflow when Output >  $V_{CC}$
- Power down isolation:  
When device is unpowered adjacent circuits are not loaded

### Package

- DFN1210



### The Diodes Advantage

#### ▪ Low Voltage and Low Power

Parts are well suited for battery driven, handheld applications such as cell phones, tablets, E-readers, games, cameras, music players, wearables, netbooks, and notebooks.

#### ▪ Noise rejection circuitry

All of the devices in this release include a small amount of input hysteresis making less susceptible to problems from slow rising or falling signals.

#### ▪ Small package

The DFN11210 at 1.2 mm X 1.00 mm X 0.35 mm is one of the smallest eight pin packages in the world.



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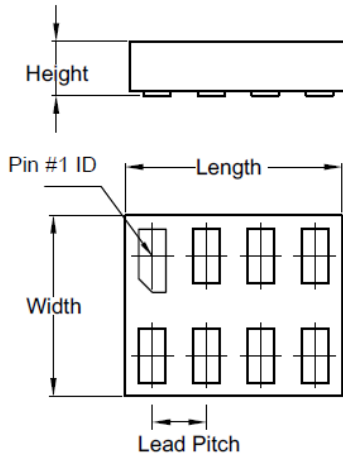
## LOGIC 74AUP2Gxx

### AUP2Gxx Cross Reference

Diodes Device	Package	Description	NXP
74AUP2G00RA3-7	X2-DFN1210-8	Dual NAND Gate	74AUP2G00GN,132
74AUP2G02RA3-7	X2-DFN1210-8	Dual NOR Gate	74AUP2G02GN,132
74AUP2G08RA3-7	X2-DFN1210-8	Dual AND Gate	74AUP2G08GN,132
74AUP2G125RA3-7	X2-DFN1210-8	Dual 3-State Buffer	74AUP2G125GN,132
74AUP2G126RA3-7	X2-DFN1210-8	Dual 3-State Buffer	74AUP2G126GN,132
74AUP2G32RA3-7	X2-DFN1210-8	Dual OR Gate	74AUP2G32GN,132
74AUP2G86RA3-7	X2-DFN1210-8	Dual EXCLUSIVE OR Gate	74AUP2G86GN,132

### Package Information

Package	Width (nominal)	Length (nominal)	Height (maximum)	Lead Pitch (nominal)
X2-DFN1210-8	1.0 mm	1.2 mm	0.35 mm	0.3 mm



The X2-DFN1210-8 is one of the world's smallest 8 pin packages used for logic.

### Product Information

- This release is for 74AUP2G products that require 8 pin packages.
- Other 74AUP2Gxx products in 6 pin packages include functions 04,06,07,14,17,34,3404
- The 74AUP2Gxx complements the existing 74AUP1Gxx family which has 14 functions in a variety of packages
- 74AUP2Gxx datasheets are located at [www.diodes.com/catalog/dual\\_gate\\_185/](http://www.diodes.com/catalog/dual_gate_185/)
- 74AUP1Gxx datasheets are located [www.diodes.com/catalog/single\\_gate\\_148/](http://www.diodes.com/catalog/single_gate_148/)