





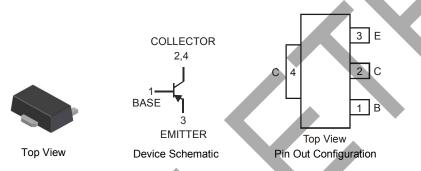
#### PNP SURFACE MOUNT TRANSISTOR

## **Features**

- **Epitaxial Planar Die Construction**
- Ideally Suited for Automated Assembly Processes
- Ideal for Medium Power Switching or Amplification Applications
- Lead Free By Design/RoHS Compliant (Note 1)
- "Green" Device (Note 2)

## **Mechanical Data**

- Case: SOT89-3L
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin annealed over Copper leadframe (Lead Free Plating). Solderable per MIL-STD-202, Method 208
- Marking Information: See Page 4
- Ordering Information: See Page 4
- Weight: 0.055 grams (approximate)



## Maximum Ratings @TA = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V <sub>CBO</sub>	-25	V
Collector-Emitter Voltage	V <sub>CEO</sub>	-25	V
Emitter-Base Voltage	V <sub>EBO</sub>	-5	V
Peak Pulse Current	Ісм	-2	Α
Continuous Collector Current	I <sub>C</sub>	-1	Α

## **Thermal Characteristics**

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 3) @ T <sub>A</sub> = 25°C	$P_{D}$	1	W
Thermal Resistance, Junction to Ambient Air (Note 3) @ T <sub>A</sub> = 25°C	$R_{ heta JA}$	125	°C/W
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

Notes:

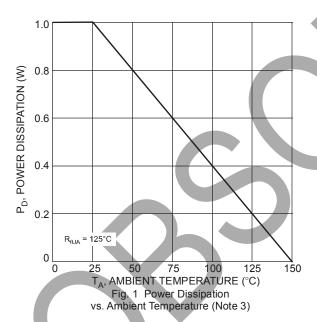
- 1. No purposefully added lead.
  2. Diodes Inc.'s "Green" policy can be found on our website at http://www.diodes.com/products/lead\_free/index.php.
  3. Device mounted on FR-4 PCB; pad layout as shown on Diodes Inc. suggested pad layout document AP02001, which can be found on our website at http://www.diodes.com/datasheets/ap02001.pdf.

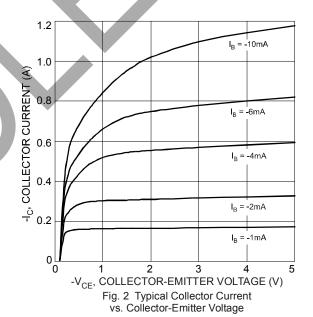


## Electrical Characteristics @TA = 25°C unless otherwise specified

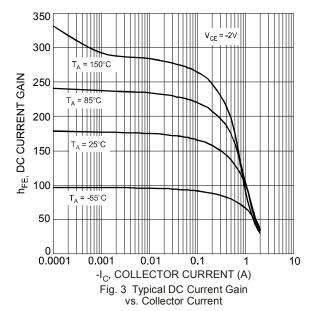
Characteristic	Symbol	Min	Тур	Max	Unit	Conditions
OFF CHARACTERISTICS (Note 4)	OFF CHARACTERISTICS (Note 4)					
Collector-Base Breakdown Voltage	V <sub>(BR)CBO</sub>	-25	_	_	V	$I_C = -10\mu A, I_E = 0$
Collector-Emitter Breakdown Voltage	$V_{(BR)CEO}$	-25	_	_	٧	$I_C = -1 \text{mA}, I_B = 0$
Emitter-Base Breakdown Voltage	V <sub>(BR)EBO</sub>	-5	_	_	V	$I_E = -10\mu A, I_C = 0$
Collector Cut-Off Current	I <sub>CBO</sub>	_	_	-0.1	μΑ	$V_{CB} = -20V, I_{E} = 0$
Emitter Cut-Off Current	I <sub>EBO</sub>	_	_	-0.1	μΑ	$V_{EB} = -4V, I_{C} = 0$
ON CHARACTERISTICS (Note 4)						
Collector-Emitter Saturation Voltage	V <sub>CE(SAT)</sub>	_	-0.15	-0.7	<b>&gt;</b>	$I_C = -500 \text{mA}, I_B = -50 \text{mA}$
Base-Emitter Saturation Voltage	$V_{BE(SAT)}$	_	-0.85	-1.2	V	$I_C = -500 \text{mA}, I_B = -50 \text{mA}$
DC Current Gain	h <sub>FE</sub>	140	_	280		$V_{CE} = -2V, I_{C} = -50mA$
DC Current Gain		40	_	_	ĺ	$V_{CE} = -2V, I_{C} = -1A$
SMALL SIGNAL CHARACTERISTICS						
Transition Frequency	f⊤	_	200		MHz	$V_{CE} = -10V, I_{C} = -50mA$ f = 100MHz
Output Capacitance	C <sub>ob</sub>	_	12		pF	$V_{CB} = -10V, I_{E} = 0,$ f = 1MHz

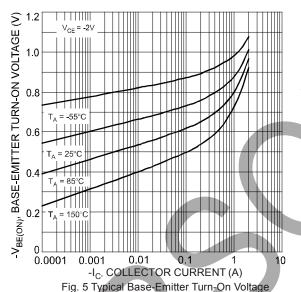
Notes: 4. Measured under pulsed conditions. Pulse width =  $300\mu s$ . Duty cycle  $\leq 2\%$ .

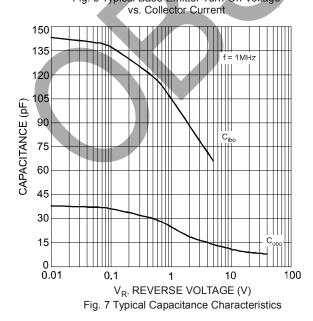


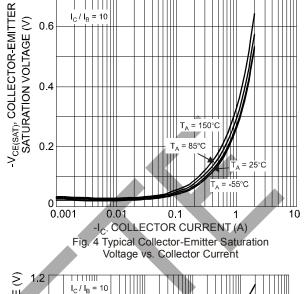












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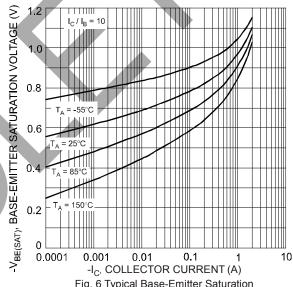


Fig. 6 Typical Base-Emitter Saturation Voltage vs. Collector Current

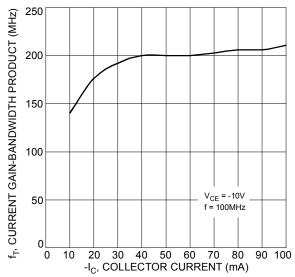


Fig. 8 Typical Gain-Bandwidth Product vs. Collector Current

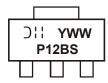


## Ordering Information (Note 5)

Part Number	Part Number Case Packaging	
2DB1119S-13	SOT89-3L	2500/Tape & Reel

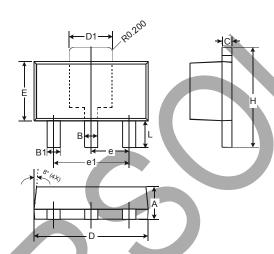
Notes: 5. For packaging details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

# **Marking Information**



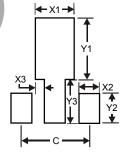
P12BS = Product Type Marking Code YWW = Date Code Marking Y = Last digit of year (ex: 7 = 2007) WW = Week code (01 – 53)

# **Package Outline Dimensions**



SOT89-3L		
Dim	Min	Max
Α	1.40	1.60
В	0.44	0.62
B1	0.35	0.54
C	0.35	0.43
D	4.40	4.60
D1	1.52	1.83
Е	2.29	2.60
е	1.50 Typ	
e1	3.00 Typ	
Н	3.94	4.25
٦	0.89	1.20
All Dimensions in mm		

# **Suggested Pad Layout**



Dimensions	Value (in mm)
X1	1.7
X2	0.9
Х3	0.4
Y1	2.7
Y2	1.3
Y3	1.9
С	3.0



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