

Description

AP1122 is a low dropout positive fixed-mode regulator with 1A output current capability. The product is specifically designed to provide well-regulated supply for low voltage IC applications such as high-speed bus termination and low current 1.2V logic supply. AP1122 is also well suited for other applications such as VGA cards. AP1122 is guaranteed to have lower than 1.3V dropout at full load current making it ideal to provide well-regulated outputs of 1.2 output voltage with 2.5V input voltage supply.

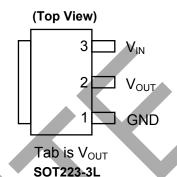
Applications

- PC peripheral
- Communication

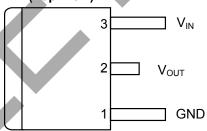
Features

- 1.3V maximum dropout at full load current
- Fixed 1.2V± 2% output voltage
- Fast transient response
- Output current limiting
- Built-in thermal shutdown
- Good noise rejection
- Packages: SOT223-3L, TO252-3L, SOT89-3L, TO263-3L and TO220-3L
- SOT89-3L, SOT223-3L and TO252-3L: Available in "Green" Molding Compound (No Br, Sb)
- Lead Free Finish/ RoHS Compliant (Note 1)

Pin Assignments

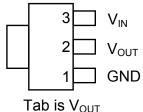


(Top View)

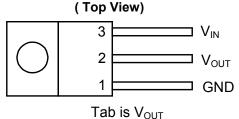


Tab is V_{OUT} TO252-3L/ TO263-3L

(Top View)



SOT89-3L

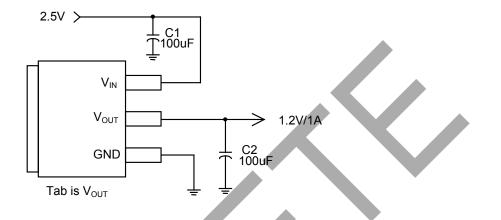


TO220-3L

1. EU Directive 2002/95/EC (RoHS). All applicable RoHS exemptions applied. Please visit our website at http://www.diodes.com/products/lead_free.html.



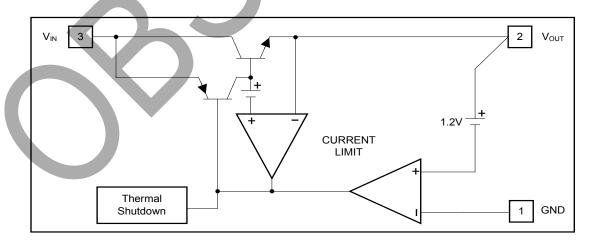
Typical Application Circuit



Pin Descriptions

Pin Name	I/O	Pin#	Description		
GND	I	1	Ground Pin		
V _{OUT}	0	2	The output of the regulator. A minimum of 10uF capacitor $(0.15\Omega \le ESR \le 20\Omega)$ must be connected from this pin to ground to insure stability.		
V _{IN}	I	3	The input pin of regulator. Typically a large storage capacitor is connected from this pin to ground to insure that the input voltage does not sag below the minimum dropout voltage during the load transient response.		

Functional Block Diagram





Absolute Maximum Ratings

Symbol	Parameter	Rating	Unit
V_{IN}	DC Supply Voltage	-0.3 to 12	V
T _{ST}	Storage Temperature	-65 to +150	°C
T_{MJ}	Maximum Junction Temperature	150	°C

Recommended Operating Conditions

Symbol	Parameter	Min	Max	Unit
I _{OUT}	Output Current		1.0	Α
T _{OP}	Operating Junction Temperature Range	0	125	°C

Electrical Characteristics (Under Operating Conditions)

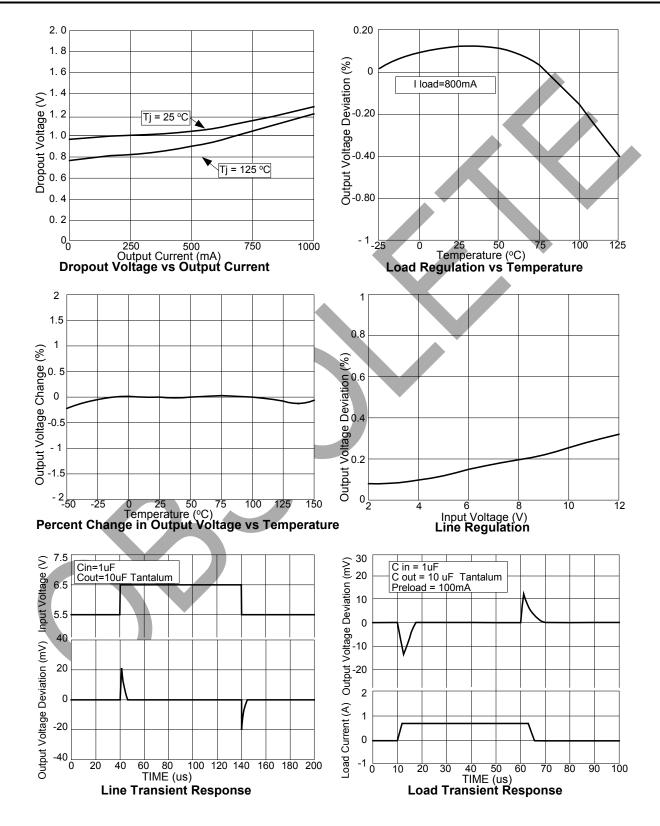
Parameter	Test Conditions	Min	Тур.	Max	Unit
Output Voltage	$2.5V \le V_{IN} \le 12V$, $I_0 = 10$ mA, $T_A = 25$ °C	1.176	1.2	1.224	V
Line Regulation	$2.5V \le V_{IN} \le 12V$, $I_0 = 10$ mA, $T_A = 25$ °C			0.2	%
Load Regulation	V _{IN} =2.5V~12V, 10mA <lo<1a, t<sub="">A=25°C (Note 2, 3)</lo<1a,>			1	%
Dropout Voltage (V _{IN} -V _{OUT})	$I_{OUT} = 1A$, $\Delta V_{OUT} = 1\%V_{OUT}$			1.3	V
Current Limit	$(V_{IN}-V_{OUT}) = 5V$	1. 1			Α
Minimum Load Current (Note 4)	0°C≦TJ≦125°C		5	10	mA
Thermal Regulation	T _A =25°C, 30ms pulse		0.008	0.04	%/W
Ripple Rejection	F=120Hz,C _{OUT} =25uF Tantalum, I _{OUT} =1A, V _{IN} =V _{OUT} +3V		60	70	dB
Temperature Stability	I _O =10mA		0.5		%
θ _{JA} Thermal Resistance Junction-to-Ambient	SOT89-3L: Control Circuitry/Power Transistor (Note 5) SOT223-3L: Control Circuitry/Power Transistor (Note 6) TO252-3L: Control Circuitry/Power Transistor (Note 5) TO220-3L: Control Circuitry/Power Transistor (Note 5) TO263-3L: Control Circuitry/Power Transistor (Note 5)		182 107 73 78 60		°C/W
SOT89-3L: Control Circuitry/Power Transistor (Note 5) SOT223-3L: Control Circuitry/Power Transistor (Note 6) TO252-3L: Control Circuitry/Power Transistor (Note 5) TO220-3L: Control Circuitry/Power Transistor (Note 5) TO263-3L: Control Circuitry/Power Transistor (Note 5)			42 16 12 3.5 3.5		°C/W

Notes:

- See thermal regulation specifications for changes in output voltage due to heating effects. Line and load regulation are
 measured at a constant junction temperature by low duty cycle pulse testing. Load regulation is measured at the output lead
 = 1/18" from the package.
- 3. Line and load regulation are guaranteed up to the maximum power dissipation of 15W. Power dissipation is determined by the difference between input and output differential and the output current. Guaranteed maximum power dissipation will not be available over the full input/output range.
- 4. Quiescent current is defined as the minimum output current required in maintaining regulation. At 12V input/output differential the device is guaranteed to regulate if the output current is greater than 10mA.
- 5. Test conditions for SOT89-3L, TO252-3L, TO220-3L, and TO263-3L: Devices mounted on FR-4 substrate, single sided PC board, 2oz copper, with minimum recommended pad layout, no air flow.
- 6. Test condition for SOT223-3L: Device mounted on FR-4 substrate, single sided PC board, 2oz copper, with 5mmX5mm thermal pad layout, no air flow.



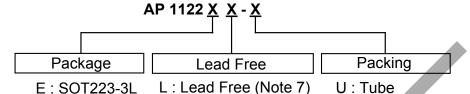
Typical Performance Characteristics





13: Tape & Reel

Ordering Information



G: Green

D: TO252-3L Y: SOT89-3L

K: TO263-3L T: TO220-3L

		Package Packaging		Tube		13" Tape and Reel		
	Device	Package Code	(Note 8)	Quantity	Part Number Suffix	Quantity	Part Number Suffix	
Pb Lead-free	AP1122EL-13	Е	SOT223-3L	NA	NA	2500/Tape & Reel	-13	
Pby Lead-free Green	AP1122EG-13	Е	SOT223-3L	NA _	NA	2500/Tape & Reel	-13	
Pb	AP1122DL-13	D	TO252-3L	NA	NA	2500/Tape & Reel	-13	
Pb Lead-free Green	AP1122DG-13	D	TO252-3L	NA	NA	2500/Tape & Reel	-13	
Pb	AP1122YL-13	Υ	SOT89-3L	NA	NA	2500/Tape & Reel	-13	
Pb. Lead-free Green	AP1122YG-13	Υ	SOT89-3L	NA	NA	2500/Tape & Reel	-13	
Pb Lead-free	AP1122KL-13	K	TO263-3L	NA	NA	800/Tape & Reel	-13	
Pb	AP1122TL-U	Т	TO220-3L	50	-U	NA	NA	

Notes:

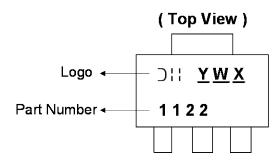
^{7.} TO263-3L and TO220-3L are available in "Lead Free" products only.
8. Pad layout as shown on Diodes Inc. suggested pad layout document AP02001, which can be found on our website at http://www.diodes.com/datasheets/ap02001.pdf





Marking Information

(1) SOT223-3L



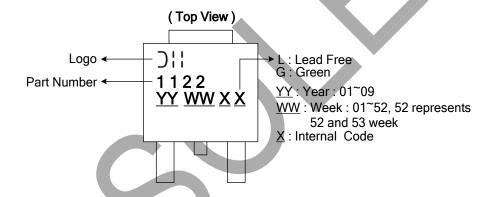
Y: Year: 0~9

<u>W</u>: Week: A~Z: 1~26 week; a~z: 27~52 week;

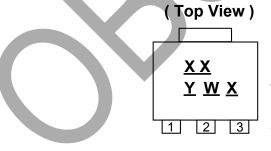
z repersents 52 and 53 week

X: Internal code a~z : Lead Free A~Z : Green

(2) TO252-3L



(3) SOT89-3L



XX: Identification code

Y: Year: 0~9

 $\overline{\underline{W}}$: Week : A~Z : 1~26 week;

a~z : 27~52 week; z represents 52 and 53 week

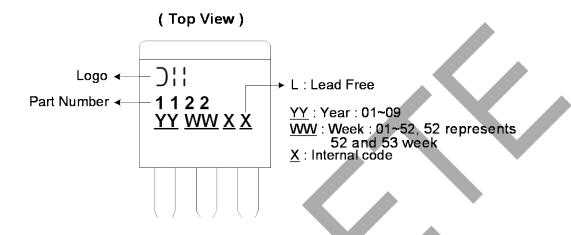
X: Internal code a~z: Lead Free A~Z: Green

Part Number	Package	Identification Code
AP1122	SOT89-3L	JB

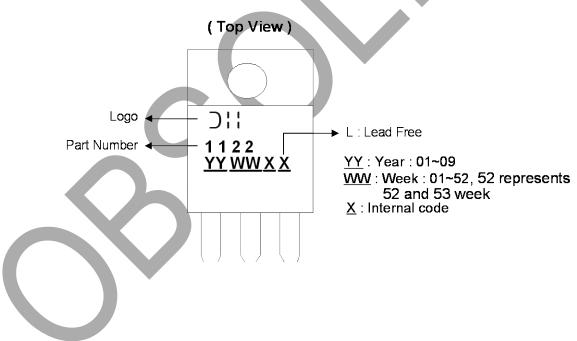


Marking Information (Continued)

(4) TO263-3L



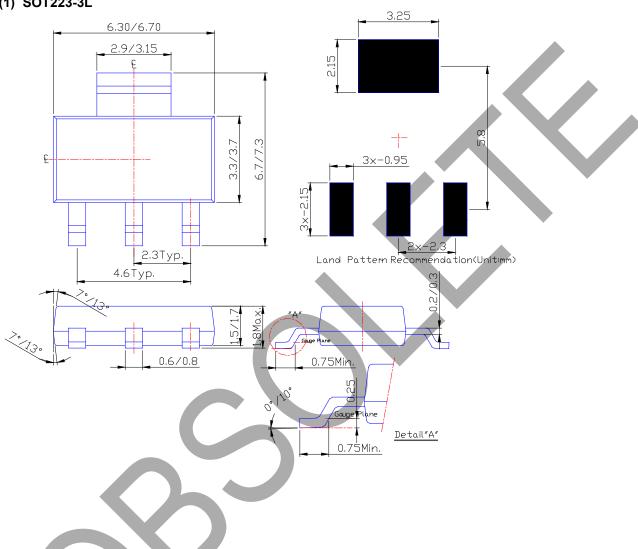
(5) TO220-3L





Package Outline Dimensions (All Dimensions in mm)

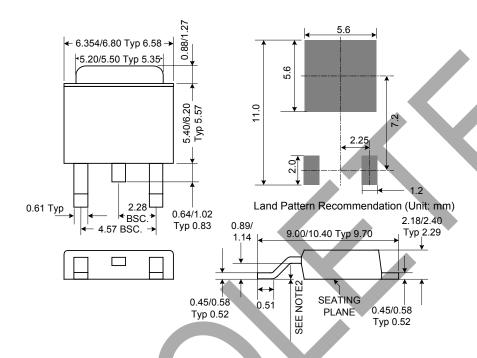
(1) SOT223-3L



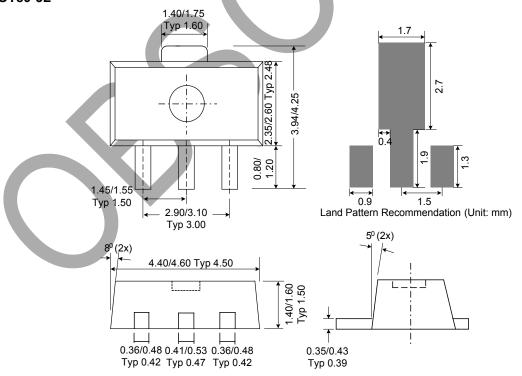


Package Outline Dimensions (Continued)

(2) TO252-3L



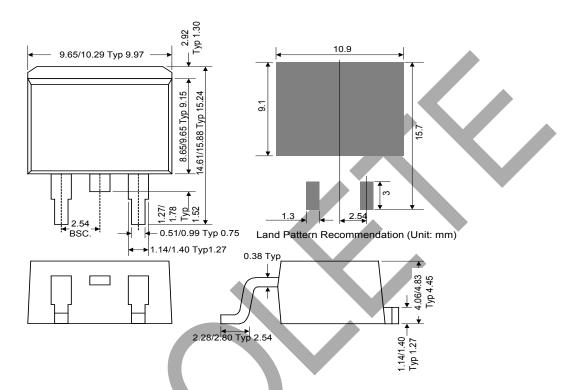
(3) SOT89-3L



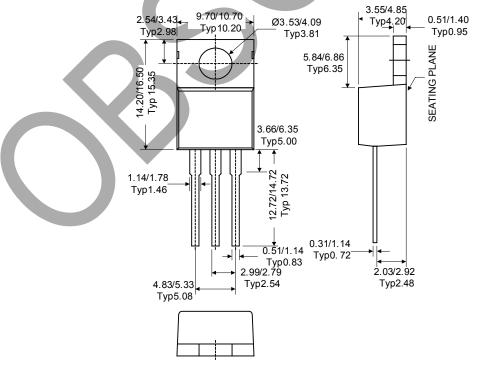


Package Outline Dimensions (Continued)

(4) TO263-3L



(5) TO220-3L





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