

## Description

The AP139 is a positive voltage linear regulator utilizing CMOS technology. The features that include low quiescent current (45µA typ), low dropout voltage, and high output voltage accuracy, make it ideal for battery applications. EN input connected to the device will produce a low bias current. The space-saving SOT25 package is attractive for "pocket" and "hand held" applications.

This rugged device has both thermal shutdown and current limit protections to prevent device failure under the "worst" operating conditions.

In a low noise, regulated supply application, a 10nF capacitor is necessary to be placed in between Bypass and Ground.

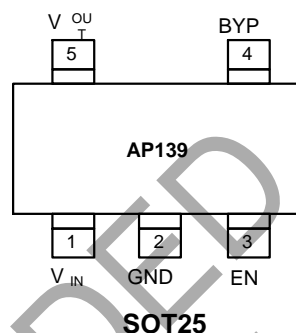
The AP139 is stable with a low ESR output capacitor of 1.0µF or greater.

## Features

- Very Low Dropout Voltage
- Low Current Consumption: typ 45µA, max 60µA
- Output Voltage: 1.5V, 1.8V, 2.0V, 2.5V, 2.8V, 3.0V, 3.3V, and 3.5V
- Guaranteed 300mA Output
- Input Range from 2.7V up to 5.5V
- Thermal Shutdown
- Current Limiting
- Stability with Low ESR Capacitors
- Low Temperature Coefficient
- SOT25
  - **Lead-Free Finish; RoHS Compliant (Notes 1 & 2)**
- Available in "Green" Package: SOT25
  - **Lead-Free Finish; RoHS Compliant (Notes 1 & 2)**
  - **Halogen and Antimony Free. "Green" Device (Note 3)**
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please [contact us](#) or your local Diodes representative. <https://www.diodes.com/quality/product-definitions/>

## Pin Assignments

( Top View )

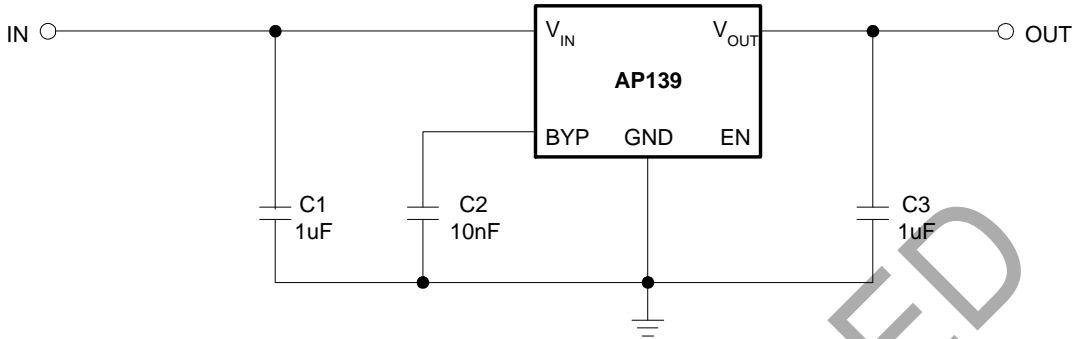


## Applications

- Personal communication devices
- Home electric/electronic appliances
- PC peripherals
- Battery-powered devices

Notes: 1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.  
 2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.  
 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

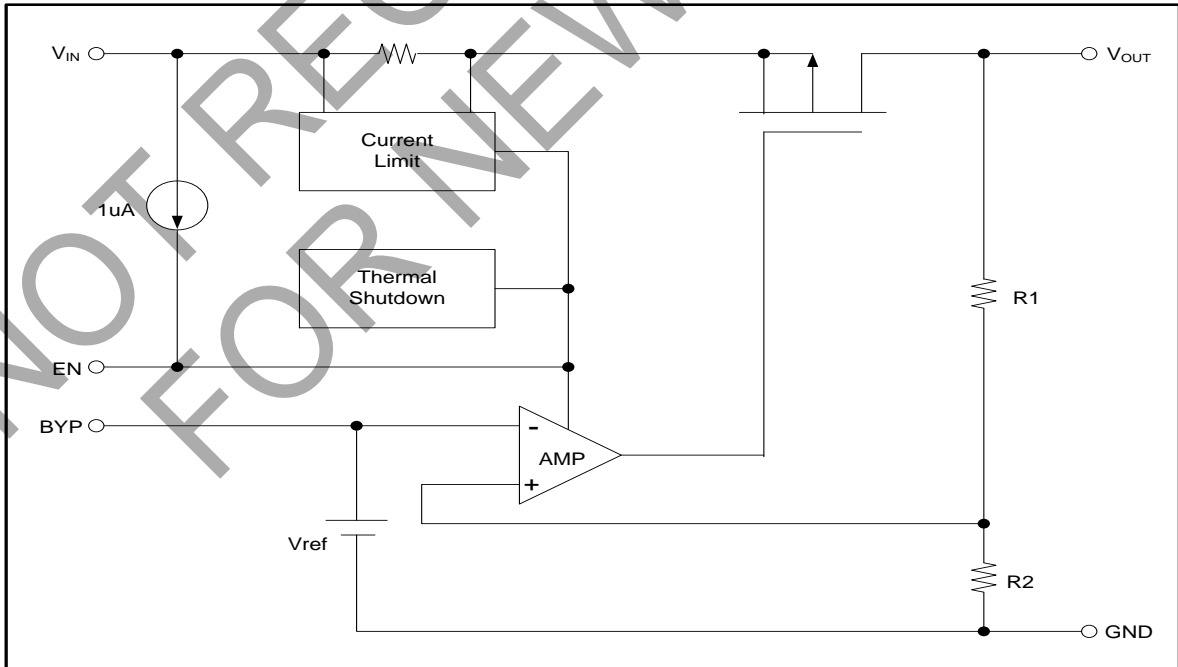
**Typical Application**



**Pin Descriptions**

Pin Name	Pin Number	Description
V <sub>IN</sub>	1	Power Supply
GND	2	Ground
EN	3	Enable Pin
BYP	4	Bypass Signal Pin
V <sub>OUT</sub>	5	Output

**Block Diagram**



### Absolute Maximum Ratings (Note 4)

Symbol	Parameter	Rating	Unit
V <sub>IN</sub>	Input Voltage	+6	V
I <sub>OUT</sub>	Output Current	P <sub>D</sub> / (V <sub>IN</sub> -V <sub>O</sub> )	mA
V <sub>OUT</sub>	Output Voltage	GND - 0.3 to V <sub>IN</sub> + 0.3	V
—	ESD Classification	B	—
T <sub>OP</sub>	Operating Junction Temperature Range	-40 to +125	°C
T <sub>MJ</sub>	Maximum Junction Temperature	+150	°C
P <sub>D</sub>	Internal Power Dissipation	250	mW

Note: 4. Stresses greater than those listed under *Absolute Maximum Ratings* can cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under *Recommended Operating Conditions* is not implied. Exposure to *Absolute Maximum Ratings* for extended periods can affect device reliability.

### Recommended Operating Conditions

Symbol	Parameter	Min	Max	Unit
V <sub>IN</sub>	Input Voltage	2.7	5.5	V
I <sub>OUT</sub>	Output Current	0	300	mA
T <sub>A</sub>	Operating Ambient Temperature	-40	+85	°C

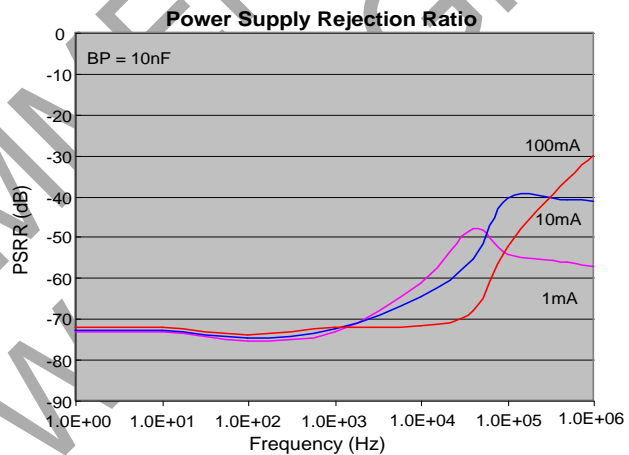
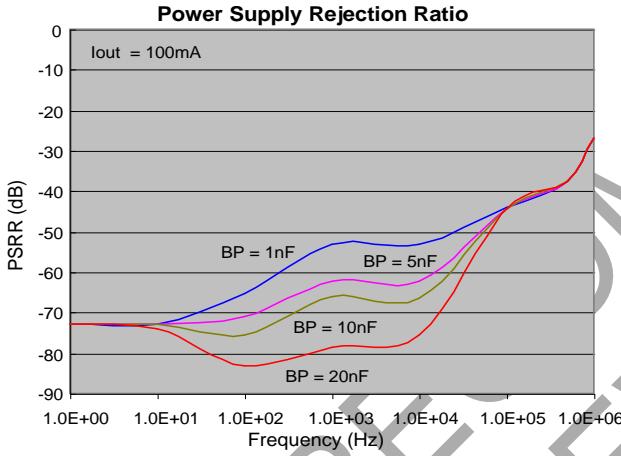
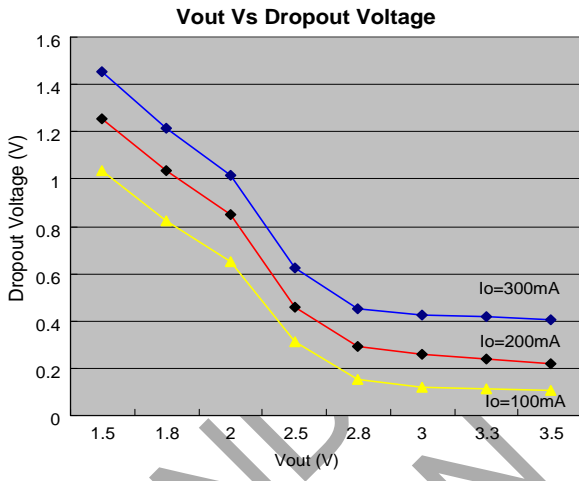
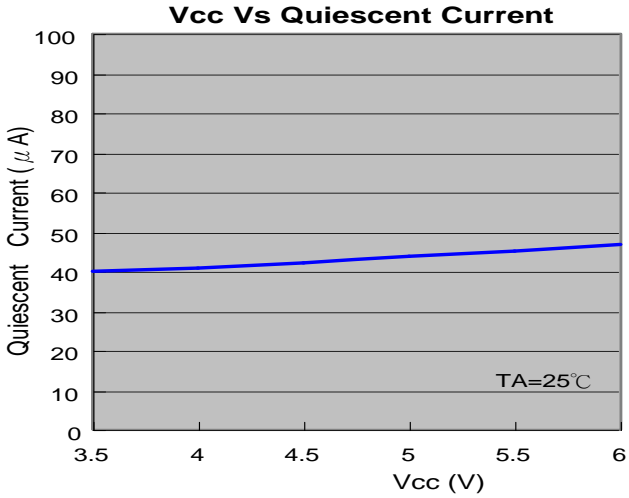
### Electrical Characteristics (T<sub>A</sub> = +25°C, unless otherwise noted)

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit	
V <sub>IN</sub>	Input Voltage	—	Note 5	—	5.5	V	
I <sub>Q</sub>	Quiescent Current	I <sub>O</sub> = 0mA	—	45	60	µA	
I <sub>STB</sub>	Standby Current	V <sub>IN</sub> = 5.0V, V <sub>OUT</sub> = 0V, V <sub>EN</sub> < V <sub>EL</sub>	—	2.0	3.0	µA	
V <sub>OUT</sub>	Output Voltage Accuracy	I <sub>O</sub> = 1mA, V <sub>IN</sub> = 5V	-2	—	2	%	
	V <sub>OUT</sub> Temperature Coefficient	—	—	50	—	ppm/°C	
V <sub>DROPOUT</sub>	Dropout Voltage	I <sub>O</sub> = 1mA to 300mA V <sub>OUT</sub> = V <sub>O(NOM)</sub> - 1.5% V <sub>O</sub> ≥ 2.8V	—	—	0.45	V	
I <sub>OUT</sub>	Output Current	—	300	—	—	mA	
I <sub>LIMIT</sub>	Current Limit	V <sub>OUT</sub> > 1.05V	300	450	—	mA	
I <sub>short</sub>	Short-Circuit Current	V <sub>CC</sub> = 5V, V <sub>OUT</sub> < 1.05V	—	150	300	mA	
ΔV <sub>LINE</sub>	Line Regulation	I <sub>OUT</sub> = 1mA, V <sub>IN</sub> = (V <sub>OUT</sub> +1V) to 5.5V	—	0.1	0.3	%	
ΔV <sub>LOAD</sub>	Load Regulation	I <sub>O</sub> = 1mA to 300mA, V <sub>IN</sub> = 5V	—	0.3	1	%	
PSRR	Power Supply Rejection	I <sub>O</sub> = 100mA C <sub>O</sub> = 2.2µF ceramic	f = 1kHz	—	60	—	dB
			f = 10kHz	—	50	—	
			f = 100kHz	—	40	—	
PSRR	Power Supply Rejection	I <sub>O</sub> = 100mA C <sub>O</sub> = 2.2µF ceramic C <sub>BYP</sub> = 20nF	f = 1kHz	—	75	—	dB
			f = 10kHz	—	55	—	
			f = 100kHz	—	30	—	
V <sub>EH</sub>	EN Input Threshold	Output ON	1.7	—	—	V	
V <sub>EL</sub>		Output OFF	—	—	0.8	V	
I <sub>EN</sub>	Enable Pin Current	—	—	—	<0.1	µA	
OTS	Overtemperature Shutdown	—	—	+130	—	°C	
OTH	Overtemperature Hysteresis	—	—	+20	—	°C	
θ <sub>JA</sub>	Thermal Resistance	SOT25 (Note 6)	—	226	—	°C/W	
θ <sub>JC</sub>	Thermal Resistance	SOT25 (Note 6)	—	34	—	°C/W	

Notes: 5. V<sub>IN(MIN)</sub> = V<sub>OUT</sub> + V<sub>DROPOUT</sub>.

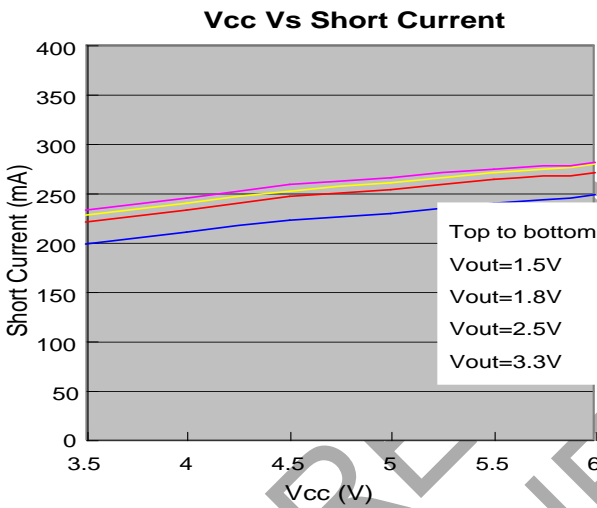
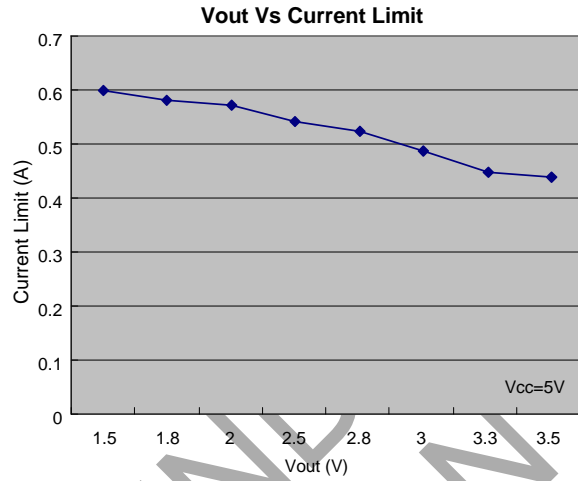
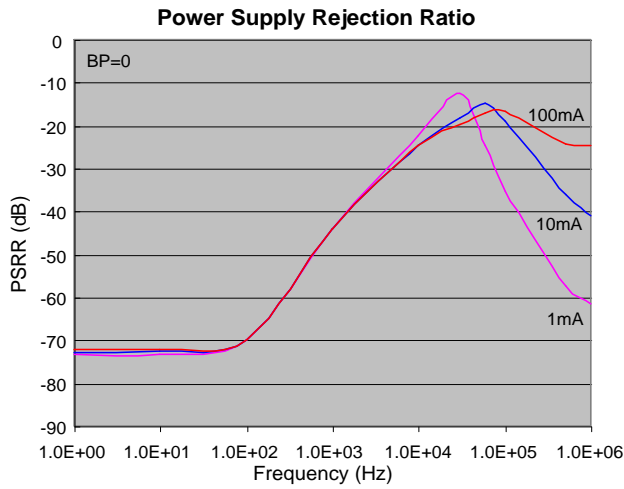
6. Test conditions for SOT25: devices mounted on FR-4 PC board, MRP, 1oz. copper, single sided, calibrate at T<sub>J</sub> = +85°C, measure at T<sub>A</sub> = +25°C, no heatsink, no air flow.

**Typical Performance Characteristics**



NOT RECOMMENDED FOR NEW DESIGN

**Typical Performance Characteristics** (continued)



NOT FOR IMMEDIATE DESIGN

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## Functional Description

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The AP139 of CMOS regulators contain a pMOS pass transistor, voltage reference, error amplifier, overcurrent protection, and thermal shutdown.

The p-channel pass transistor receives data from the error amplifier, overcurrent protection, and thermal protection circuits. During normal operation, the error amplifier compares the output voltage to a precision reference. The overcurrent and thermal shutdown circuits become active when the junction temperature exceeds +130°C, or the current exceeds 300mA. During thermal shutdown, the output voltage remains low. Normal operation is restored when the junction temperature drops below +110°C.

The AP139 switches from voltage mode to current mode when the load exceeds the rated output current. This prevents over-stress.

### Enable

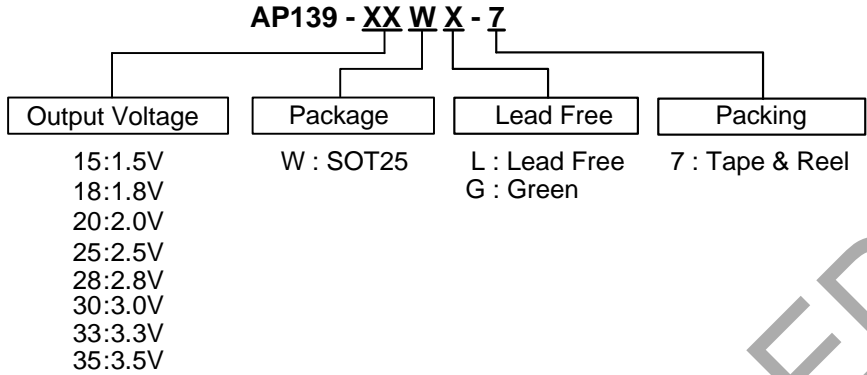
The enable pin normally floats high. When active, pulled low, the pMOS pass transistor shuts off, and all internal circuits are powered down. In this state, the quiescent current is less than 2μA. This pin behaves much like an electronic switch.

### External Capacitor

The AP139 is stable with a low ESR output capacitor to ground of 1.0μF or greater. It can keep stable even with higher ESR capacitors. A second capacitor is recommended between the input and ground to stabilize  $V_{IN}$ . The input capacitor should be larger than 0.1μF to have a beneficial effect. All capacitors should be placed in close proximity to the pins. A “quiet” ground termination is desirable.

NOT RECOMMENDED  
FOR NEW DESIGN

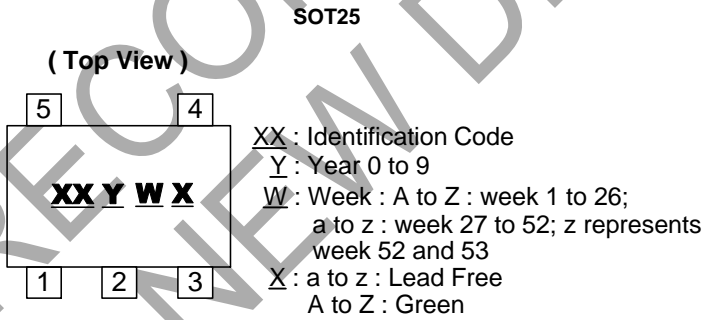
**Ordering Information**



Part Number	Part Number Suffix	Package Code	Package (Note 7)	Packing		Status (Note 8)
				Qty.	Carrier	
AP139-XXWL-7	-7	W	SOT25	3000	7" Tape & Reel	EOL
AP139-XXWG-7	-7	W	SOT25	3000	7" Tape & Reel	NRND

Notes: 7. Pad layout as shown on Diodes Incorporated's suggested pad layout, which can be found on our website at <http://www.diodes.com/package-outlines.html>.  
8. EOL = End of Life; NRND = Not Recommended for New Design. Please [contact us](#).

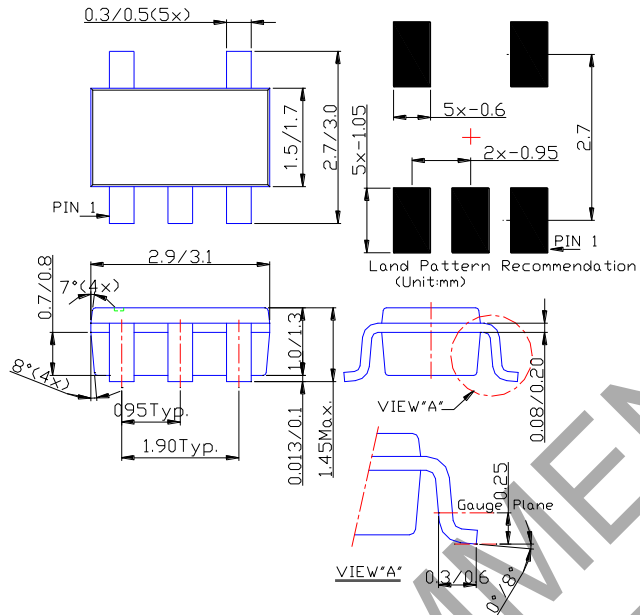
**Marking Information**



Part Number	Package	Identification Code
AP139-15W	SOT25	N0
AP139-18W	SOT25	N1
AP139-20W	SOT25	N2
AP139-25W	SOT25	N3
AP139-28W	SOT25	N4
AP139-30W	SOT25	N5
AP139-33W	SOT25	N6
AP139-35W	SOT25	N7

**Package Information** (All Dimensions in mm)

Please see <http://www.diodes.com/package-outlines.html> for the latest version.



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