

**4-CHANNEL CHARGE PUMP LED DRIVER
WITH CURRENT BALANCING AND WIDE RANGE PWM DIMMING**

Description

The AP3605 is a step-up DC/DC converter based on 1.5x charge pump current source, it is specially designed for LED supplies in backlight display.

The AP3605 can provide constant current up to 20mA for each LED, which is programmed by an external resistor, so it has a total capability to provide 80mA for 4 LEDs. The chip has a good performance of LED current matching and allows PWM brightness dimming control. Additionally, high switching frequency up to 1MHz enables the use of two small external flying capacitors. Internal soft-start circuitry prevents excessive inrush current during start-up.

The AP3605 supply voltage range is from 2.7V to 5.5V, ideally suited for applications powered by the Li-ion battery.

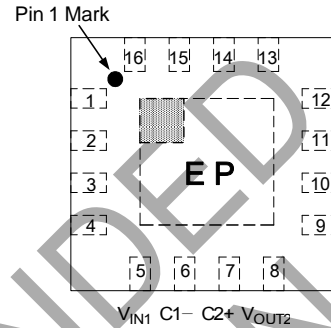
The AP3605 is available in a 3mmx3mm QFN-3x3-16 tiny package. Its operating temperature range is -40°C to +85°C.

Features

- Regulated Output Current with $\pm 3\%$ Matching
- Regulated $\pm 10\%$ Output Current Source
- Drive up to 4 LEDs at 20mA Each
- Wide Operating Voltage Range: 2.7V to 5.5V
- High Efficiency up to 93%
- High Operating Frequency: 1MHz
- Built-In Soft-Start to Limit the Inrush Current
- LED Brightness Control through PWM and Analog Signal
- PWM Dimming Frequency up to 50kHz
- Built-In Standby Mode to Get PWM Dimming Duty Cycle Control Linearity
- Built-In OTSD (Over Temperature Shutdown) Function to Protect the Device from Burn Out

Pin Assignments

(Top View)



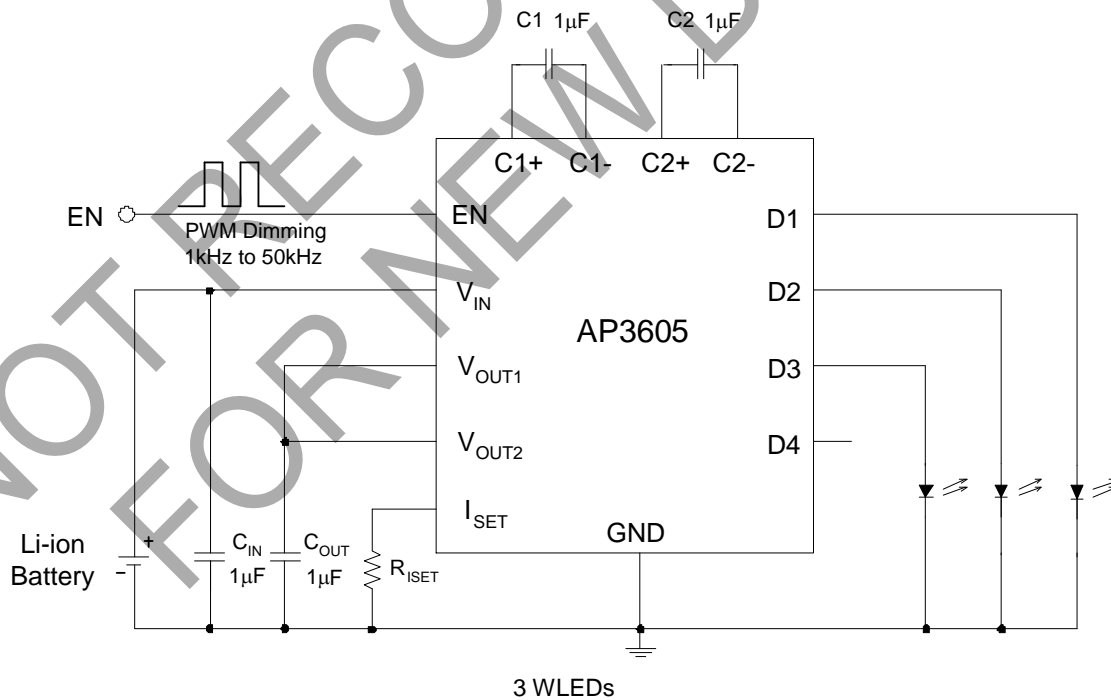
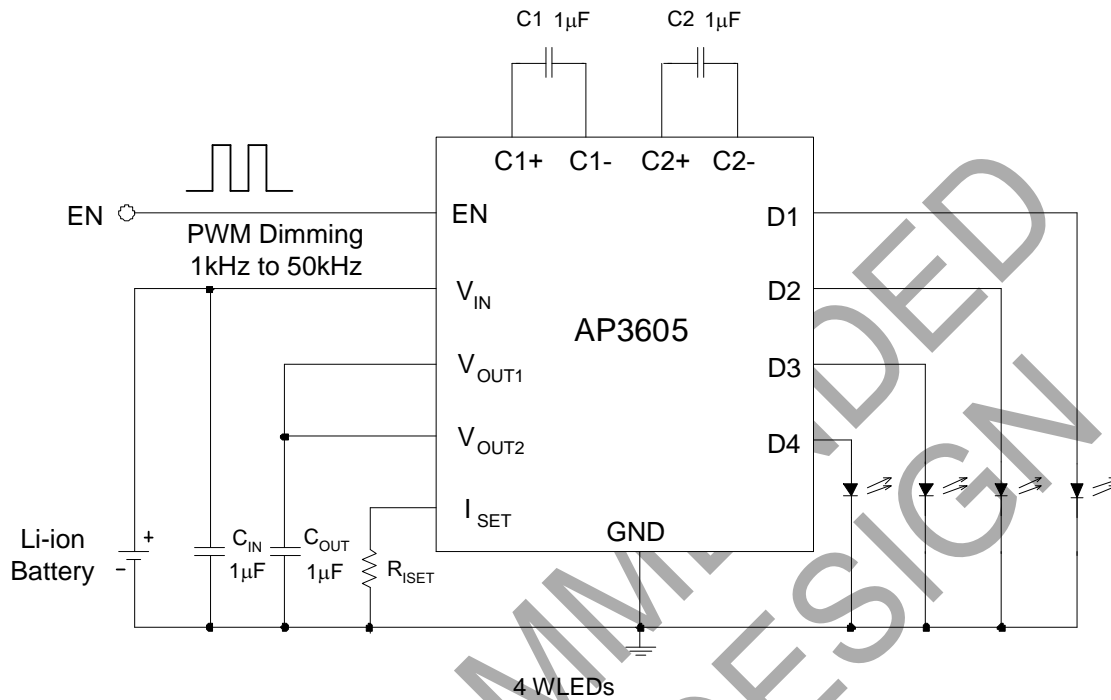
Note: Pin 2 should be connected with Pin 8 and Pin 5 should be connected with Pin 13 on PCB board.

QFN-3x3-16

Applications

- Mobile Phone
- MP3, MP4
- White LED Backlight in Mobile Phone, PDA

Typical Applications Circuit

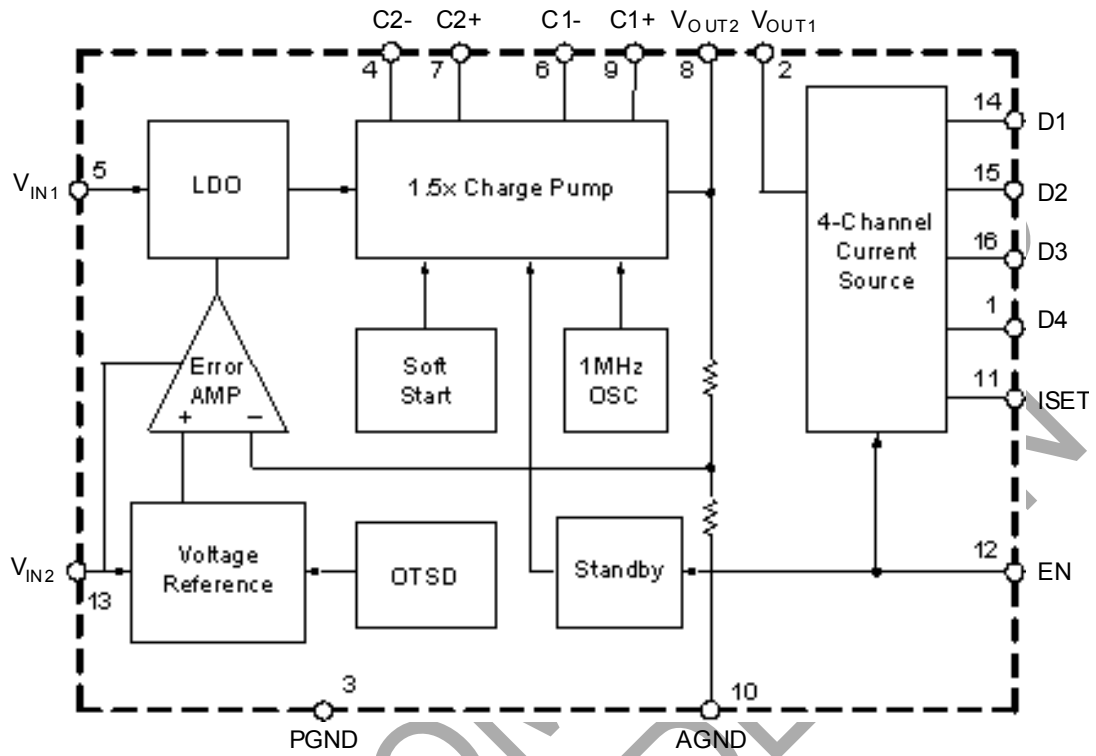


Pin Descriptions

| Pin Number | Pin Name | Function |
|---------------|---------------------------------------|--|
| 1, 16, 15, 14 | D4 to D1 | Current Source Output. Connect the anode of the white LEDs to these outputs |
| 2, 8 | V _{OUT1} , V _{OUT2} | Output Pin 1 and 2, must be connected together. The output capacitor should be placed closely to these pins |
| 3 | PGND | Power Ground. Connect this pin with power ground plane |
| 4 | C2- | Flying Capacitor 2 Negative Terminal. The flying capacitor 2 should be connected as close to this pin as possible |
| 5, 13 | V _{IN1} , V _{IN2} | Supply Voltage Input 1 and 2, must be connected together |
| 6 | C1- | Flying Capacitor 1 Negative Terminal. The flying capacitor 1 should be connected as close to this pin as possible |
| 7 | C2+ | Flying Capacitor 2 Positive Terminal. The flying capacitor 2 should be connected as close to this pin as possible |
| 9 | C1+ | Flying Capacitor 1 Positive Terminal. The flying capacitor 1 should be connected as close to this pin as possible |
| 10 | AGND | Analog Ground. Connect this pin with control signal ground plane. PGND, AGND and the exposed PAD should be connected together |
| 11 | ISET | Current Source Set Pin. Connect a resistor between this pin and GND to set the maximum LED current |
| 12 | EN | Enable Control Input. Logic high enables the IC; while logic low forces the device into shut-down mode to reduce the supply current to less than 1 μ A. Add a PWM signal to this pin to achieve brightness control |

NOT RECOMMENDED FOR NEW DESIGN

Functional Block Diagram



Absolute Maximum Ratings (Note 1)

| Symbol | Parameter | Value | Unit |
|-------------------|--|-------------|------|
| V _{IN} | Input Voltage | -0.3 to 6 | V |
| V _{EN} | EN Pin Voltage | -0.3 to 6 | V |
| V _{OUT1} | V _{OUT1} Pin Voltage | -0.3 to 6 | V |
| V _{OUT2} | V _{OUT2} Pin Voltage | -0.3 to 6 | V |
| V _{ISET} | ISET Pin Voltage | -0.3 to 6 | V |
| I _{OUT2} | Output Current at V _{OUT2} Pin | 150 | mA |
| R _{θJA} | Thermal Resistance (Junction to Ambient, no Heat sink) | 60 | °C/W |
| T _J | Operating Junction Temperature | +150 | °C |
| T _{STG} | Storage Temperature Range | -65 to +150 | °C |
| T _{LEAD} | Lead Temperature (Soldering, 10sec) | +260 | °C |
| – | ESD (Human Body Model) | 2000 | V |

Note 1: Stresses greater than those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "Recommended Operating Conditions" is not implied. Exposure to "Absolute Maximum Ratings" for extended periods may affect device reliability.

Recommended Operating Conditions

| Symbol | Parameter | Min | Max | Unit |
|-------------------|-----------------------------|------|-----|------|
| V _{IN} | Input Voltage | 2.7 | 5.5 | V |
| T _A | Operating Temperature | -40 | +85 | °C |
| R _{ISET} | Current Source Set Resistor | 1.44 | – | kΩ |

Electrical Characteristics ($V_{IN}=3.5V$, $V_{EN}=V_{IN}$, $R_{ISET}=1.8k\Omega$, $C_{FLY1}=C_{FLY2}=C_{IN}=C_{OUT}=1\mu F$, $T_A=+25^\circ C$, $V_{D1}=V_{D2}=V_{D3}=V_{D4}=3.4V$, unless otherwise specified.)

| Symbol | Parameter | Conditions | Min | Typ | Max | Unit |
|-------------------------------|--|--|-------|------|-------|---------|
| Input Section | | | | | | |
| V_{IN} | Input Voltage | $I_D=0$ to 40 mA | 2.7 | – | 5.5 | V |
| I_{CC} | Supply Current | No Load, ISET floating | – | 1.5 | 2.5 | mA |
| I_{SHDN} | Shutdown Supply Current | $V_{EN}=GND$ | – | 0.1 | 1 | μA |
| Charge Pump Section | | | | | | |
| f_{osc} | Switching Frequency | – | 0.65 | 1 | 1.35 | MHz |
| η | Efficiency | $V_{IN}=3.5V$, $I_D=40mA$ Total | – | 93 | – | % |
| Current Source Section | | | | | | |
| I_{DX} | Maximum Output Current per Source | $3.2V \leq V_{IN} \leq 5.5V$, $T_A=-40^\circ C$ to $+85^\circ C$ | 18 | 20 | 22 | mA |
| $I_{D-MATCH}$ | Current Matching between Any Two Outputs | – | -3 | – | 3 | % |
| $(\Delta I_D/I_D)/\Delta V$ | Output Current Line Regulation | $3.5V \leq V_{IN} \leq 5.5V$ | -2 | – | 2 | %/V |
| $\Delta I_D/I_D$ | Current Matching between Any Two Outputs under Different LED Forward Voltage | $3.0V \leq V_D \leq 4.0V$, $V_{IN}=3.5V$ | -5 | – | 5 | % |
| V_{ISET} | Reference Voltage for Current Set | – | 1.193 | 1.23 | 1.267 | V |
| K | I_D to I_{SET} Current Ratio | – | 100 | 120 | 140 | – |
| Enable Section | | | | | | |
| V_{IH} | EN High Level Threshold Voltage | – | 1.4 | – | – | V |
| V_{IL} | EN Low Level Threshold Voltage | – | – | – | 0.5 | V |
| I_{EN} | EN Input Leakage Current | $V_{EN}=5.5V$ | -1 | – | 1 | μA |
| t_{STB} | EN Low Threshold Time for Standby State | – | – | 2 | – | ms |

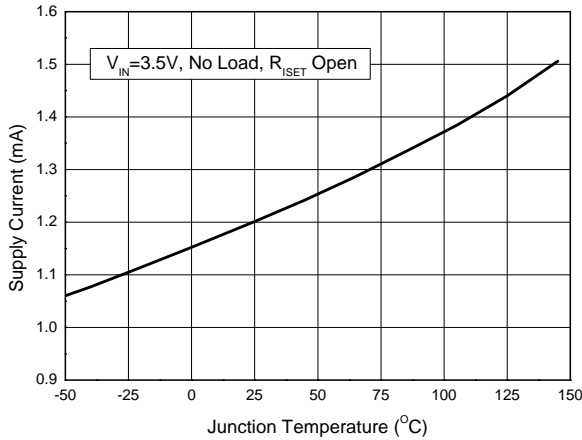
Electrical Characteristics ($V_{IN}=3.5V$, $V_{EN}=V_{IN}$, $R_{ISET}=1.8k\Omega$, $C_{FLY1}=C_{FLY2}=C_{IN}=C_{OUT}=1\mu F$, $T_A=+25^\circ C$, $V_{D1}=V_{D2}=V_{D3}=V_{D4}=3.4V$, unless otherwise specified.) (Cont.)

| Symbol | Parameter | Conditions | Min | Typ | Max | Unit |
|---------------------|-----------------------------|----------------------------|-----|------|-----|------|
| Total Device | | | | | | |
| t _{SS} | Soft-Start Time | I _D =80mA Total | – | 400 | – | μs |
| T _{OTSD} | Thermal Shutdown | – | – | +160 | – | °C |
| T _{HYS} | Thermal Shutdown Hysteresis | – | – | +20 | – | °C |

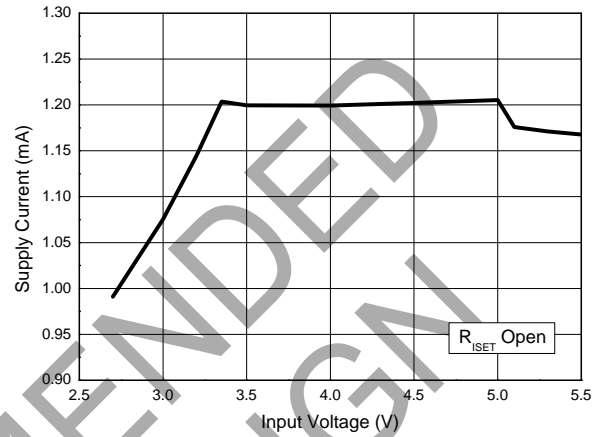
NOT RECOMMENDED
 FOR NEW DESIGN

Typical Performance Characteristics ($V_{IN}=3.5V$, $V_{EN}=V_{IN}$, $R_{ISET}=1.8k\Omega$, $C_{FLY1}=C_{FLY2}=C_{IN}=C_{OUT}=1\mu F$, $T_A=+25^\circ C$, $V_{D1}=V_{D2}=V_{D3}=V_{D4}=3.4V$, unless otherwise specified. V_{OUT} is the output voltage when V_{OUT1} and V_{OUT2} are connected.)

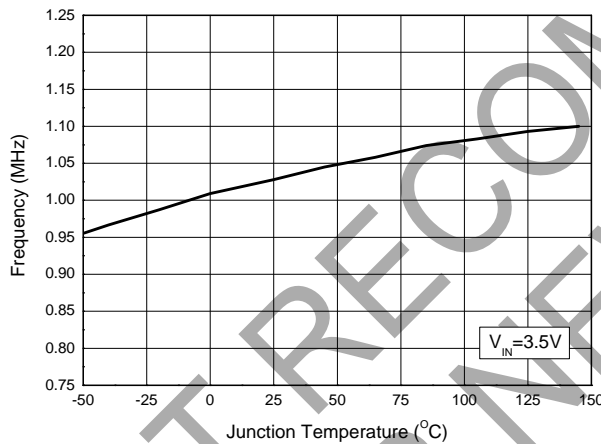
Supply Current vs. Junction Temperature



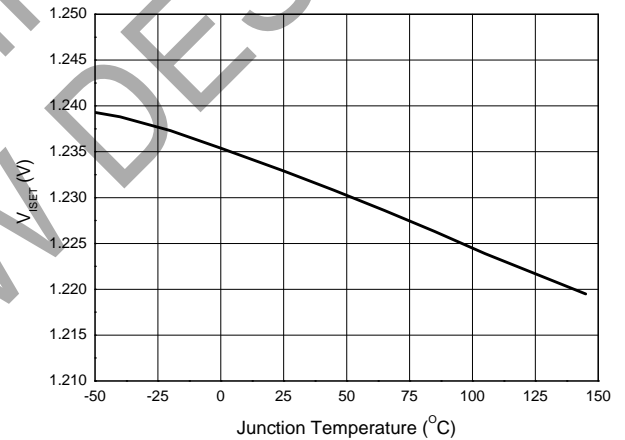
Supply Current vs. Input Voltage



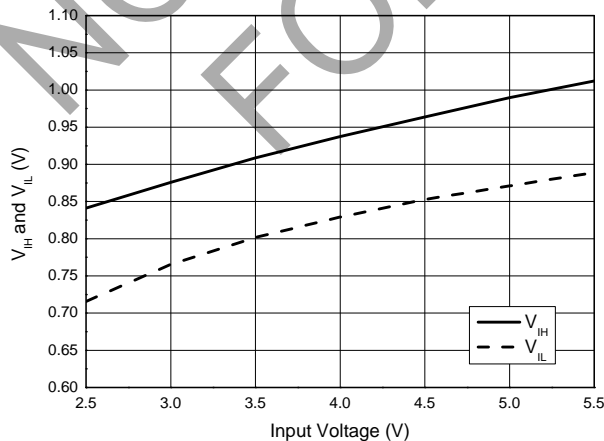
Frequency vs. Junction Temperature



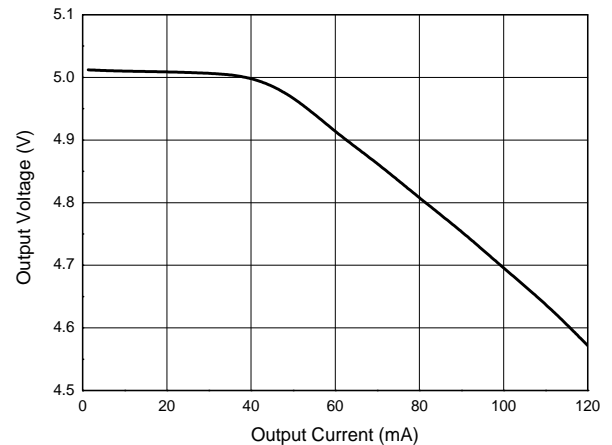
Reference Voltage vs. Junction Temperature



V_{IH} and V_{IL} vs. Input Voltage

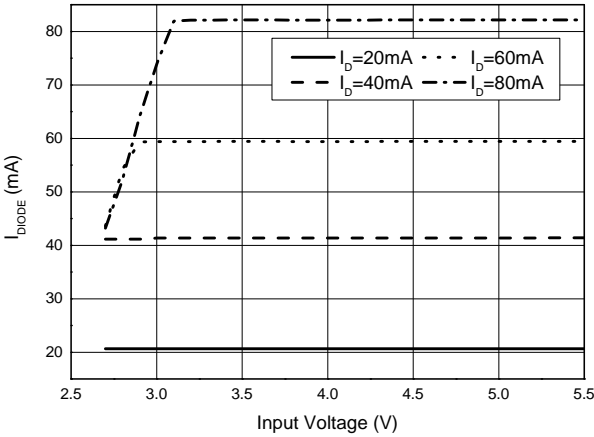


Output Voltage vs. Output Current

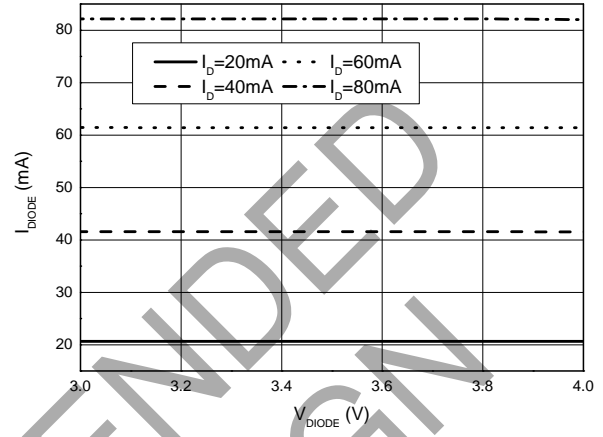


Typical Performance Characteristics ($V_{IN}=3.5V$, $V_{EN}=V_{IN}$, $R_{ISET}=1.8k\Omega$, $C_{FLY1}=C_{FLY2}=C_{IN}=C_{OUT}=1\mu F$, $T_A=+25^\circ C$, $V_{D1}=V_{D2}=V_{D3}=V_{D4}=3.4V$, unless otherwise specified. V_{OUT} is the output voltage when V_{OUT1} and V_{OUT2} are connected.) (Cont.)

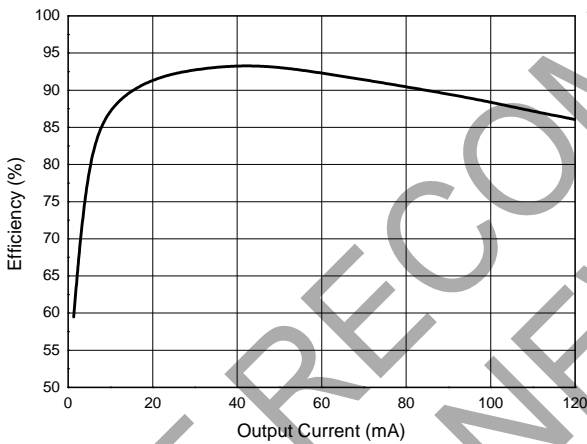
I_{DIODE} vs. Input Voltage



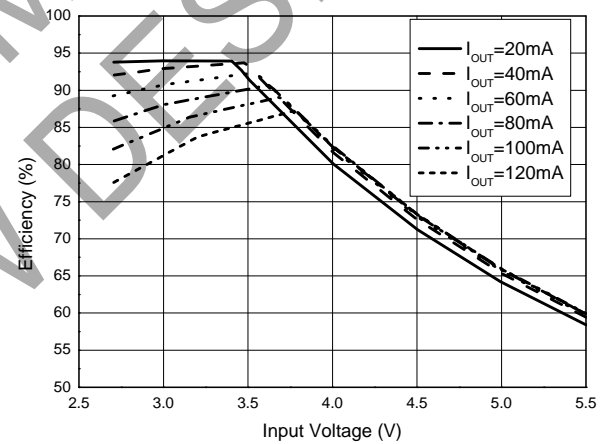
I_{DIODE} vs. V_{DIODE}



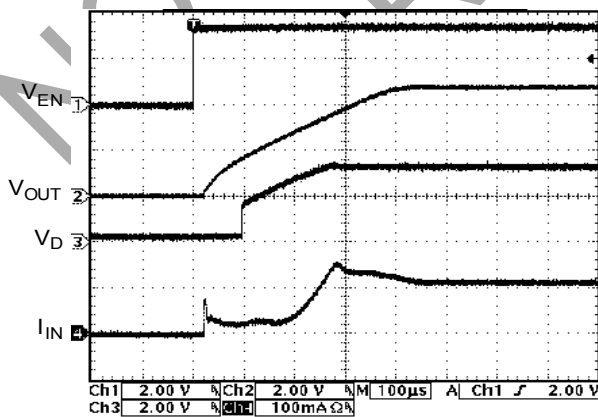
Efficiency vs. Output Current



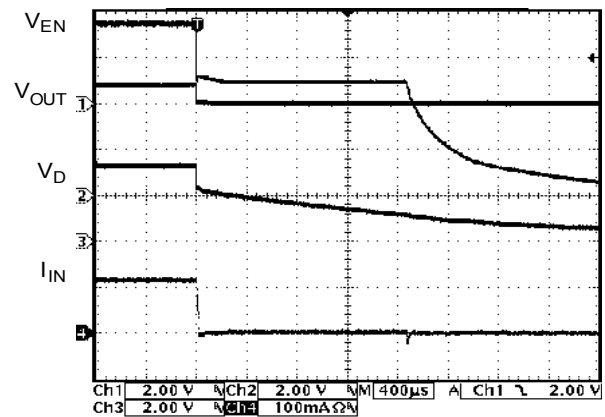
Efficiency vs. Input Current



Turn on Characteristic

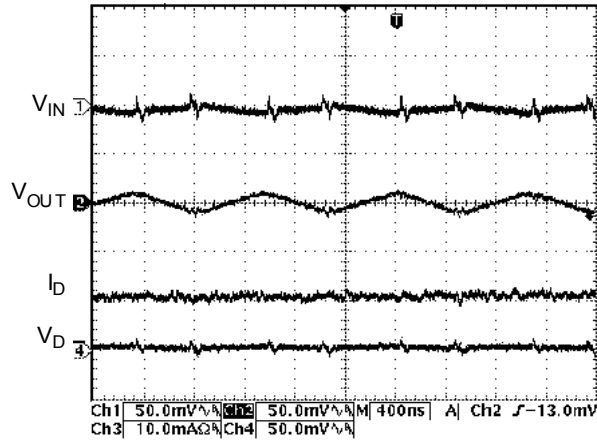


Turn off Characteristic

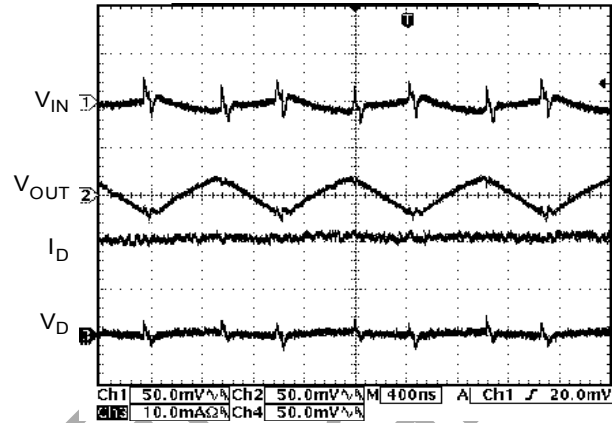


Typical Performance Characteristics ($V_{IN}=3.5V$, $V_{EN}=V_{IN}$, $R_{ISET}=1.8k\Omega$, $C_{FLY1}=C_{FLY2}=C_{IN}=C_{OUT}=1\mu F$, $T_A=+25^\circ C$, $V_{D1}=V_{D2}=V_{D3}=V_{D4}=3.4V$, unless otherwise specified. V_{OUT} is the output voltage when V_{OUT1} and V_{OUT2} are connected.) (Cont.)

Output Ripple @ $I_D=40mA$



Output Ripple @ $I_D=80mA$



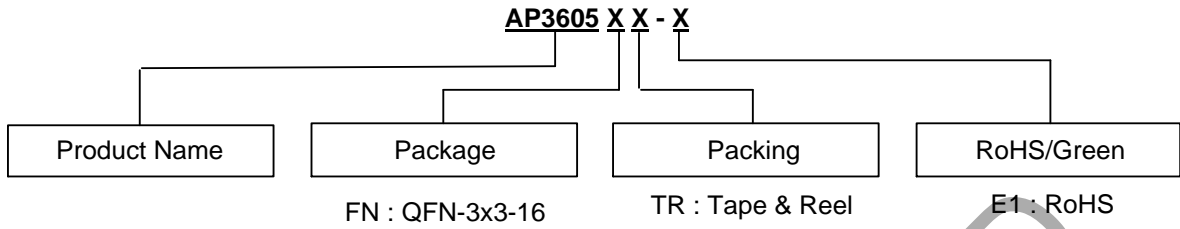
Operation

The AP3605 is a high efficiency 1.5x fractional charge pump with 4 channels of integrated current source for white LED backlight applications.

The AP3605 consists of a linear regulator followed by a 1.5x charge pump which operates at 1MHz, 4 channels current source, a reference and other control circuits. The linear regulator regulates its output voltage to supply charge pump, guarantees that the charge pump always operates at 5V output with 1.5x mode. This configuration minimizes the output ripple.

The charge pump can generate 80mA of output current, so each of the 4 WLED can be powered with up to 20mA of current. The maximum LED current is set by a resistor connected to the ISET pin which programs a reference current, then the reference current is mirrored to set the LED current. Applying a PWM signal to the EN pin can be used to achieve LED brightness dimming. Integrated 2ms standby function helps to enhance the dimming control. Detailed descriptions please see the related application note.

Ordering Information



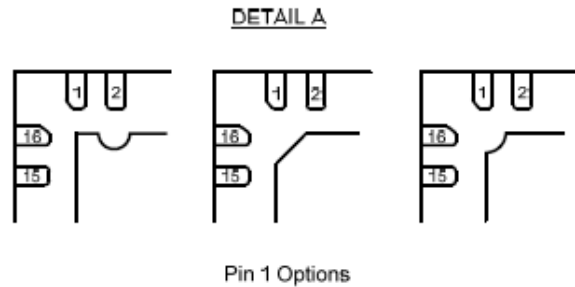
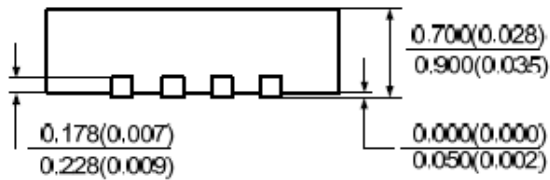
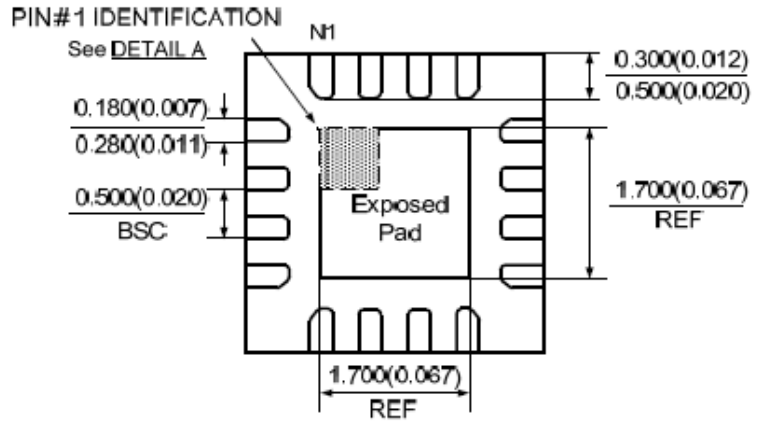
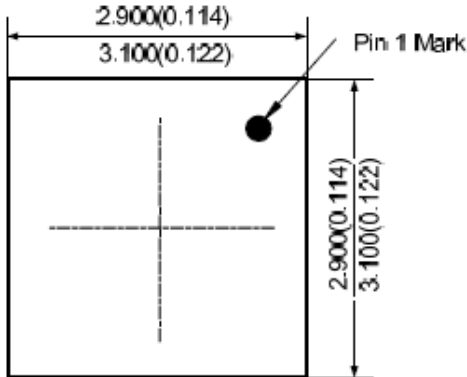
| Package | Temperature Range | Part Number | Marking ID | Packing |
|------------|-------------------|------------------------|------------|-------------|
| QFN-3x3-16 | -40 to +85°C | AP3605FNTR-E1 (Note 2) | F1A | Tape & Reel |

Note 2: AP3605FNTR-E1 is a green product.

NOT RECOMMENDED FOR NEW DESIGN

Package Outline Dimensions (All dimensions in mm(inch).)

(1) Package Type: QFN-3x3-16



NOT RECOMMENDED FOR NEW DESIGN

IMPORTANT NOTICE

DIODES INCORPORATED MAKES NO WARRANTY OF ANY KIND, EXPRESS OR IMPLIED, WITH REGARDS TO THIS DOCUMENT, INCLUDING, BUT NOT LIMITED TO, THE IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A PARTICULAR PURPOSE (AND THEIR EQUIVALENTS UNDER THE LAWS OF ANY JURISDICTION).

Diodes Incorporated and its subsidiaries reserve the right to make modifications, enhancements, improvements, corrections or other changes without further notice to this document and any product described herein. Diodes Incorporated does not assume any liability arising out of the application or use of this document or any product described herein; neither does Diodes Incorporated convey any license under its patent or trademark rights, nor the rights of others. Any Customer or user of this document or products described herein in such applications shall assume all risks of such use and will agree to hold Diodes Incorporated and all the companies whose products are represented on Diodes Incorporated website, harmless against all damages.

Diodes Incorporated does not warrant or accept any liability whatsoever in respect of any products purchased through unauthorized sales channel. Should Customers purchase or use Diodes Incorporated products for any unintended or unauthorized application, Customers shall indemnify and hold Diodes Incorporated and its representatives harmless against all claims, damages, expenses, and attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized application.

Products described herein may be covered by one or more United States, international or foreign patents pending. Product names and markings noted herein may also be covered by one or more United States, international or foreign trademarks.

This document is written in English but may be translated into multiple languages for reference. Only the English version of this document is the final and determinative format released by Diodes Incorporated.

LIFE SUPPORT

Diodes Incorporated products are specifically not authorized for use as critical components in life support devices or systems without the express written approval of the Chief Executive Officer of Diodes Incorporated. As used herein:

A. Life support devices or systems are devices or systems which:

1. are intended to implant into the body, or
2. support or sustain life and whose failure to perform when properly used in accordance with instructions for use provided in the labeling can be reasonably expected to result in significant injury to the user.

B. A critical component is any component in a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or to affect its safety or effectiveness.

Customers represent that they have all necessary expertise in the safety and regulatory ramifications of their life support devices or systems, and acknowledge and agree that they are solely responsible for all legal, regulatory and safety-related requirements concerning their products and any use of Diodes Incorporated products in such safety-critical, life support devices or systems, notwithstanding any devices- or systems-related information or support that may be provided by Diodes Incorporated. Further, Customers must fully indemnify Diodes Incorporated and its representatives against any damages arising out of the use of Diodes Incorporated products in such safety-critical, life support devices or systems.

Copyright © 2018, Diodes Incorporated

www.diodes.com