

#### EIGHT-CHANNEL CONSTANT CURRENT SINK WITH CURRENT MATCH

### Description

The AP3609 is an eight-channel constant current sink with current match used for LED driver. It uses an external resistor to set the current for eight LED strings with an accuracy of  $\pm 1.5\%$ . The full scale LED current can be adjusted from 10mA to 100mA for each channel. The LED light can be adjusted by PWM dimming function.

The device can keep working when LED opens without damage. It features under voltage lockout protection and over temperature protection.

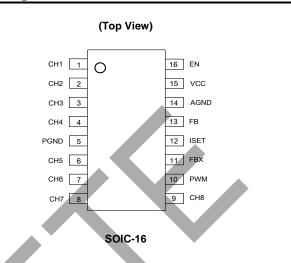
The AP3609 has two interface terminals (FB and FBX). They enable the device to be connected in parallel.

The AP3609 is available in SOIC-16 package.

### Features

- Input Voltage Range: 4.2V to 5.5V
- Typical Output Current: 480mA (60mA/1V per Channel)
- Maximum Output Current: 800mA (100mA/1.5V per Channel)
- Current Match Accuracy: ±1.5%
- PWM Dimming Control
- Open LED Self-check and Protection
- Under Voltage Lockout Protection
- Over Temperature Protection
- Enable Parallel Application

### **Pin Assignments**

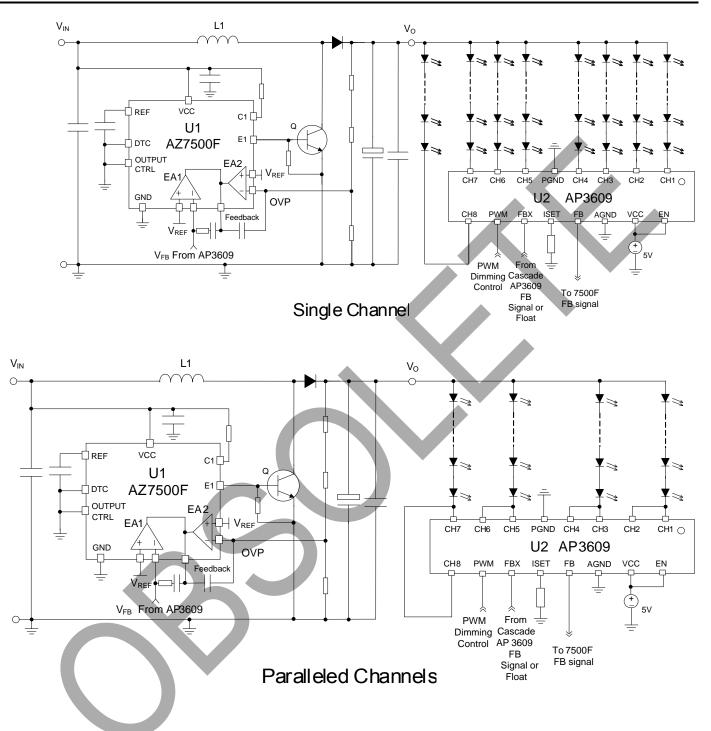


## Applications

- Notebook
- LCD Display Modules
- LCD Monitor
- LCD TV



# **Typical Applications Circuit**



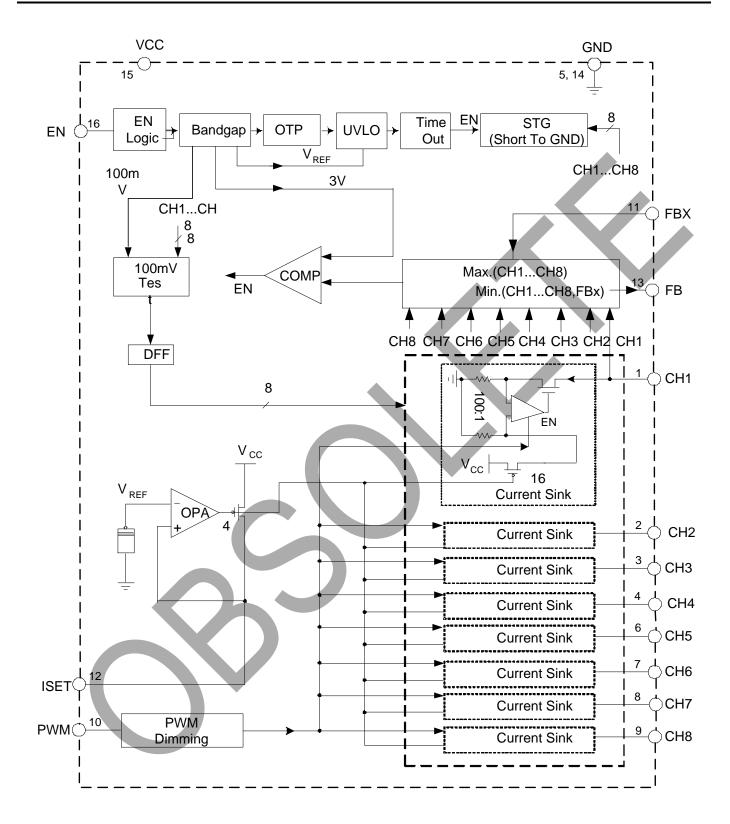


## **Pin Descriptions**

Pin Number	Pin Name	Function
1, 2, 3, 4, 6, 7, 8, 9	CH1 to CH8	White LED cathode connection pin. These pins should be connected to GND if not used
5	PGND	Ground pin. It would be useful when connected with PGND and exposed pad
10	PWM	PWM dimming control pin. Adding a PWM signal to this pin to control LED dimming (see Figure 1 for detail dimming control mode). If not used, connect it to the high level
11	FBX	This pin is an interface terminal. Connecting it with FB pin can achieve parallel application. If not used, leave it unconnected
12	ISET	LED current set pin. An external resistor is connected to this pin to set the current of each channel according to ICHANNEL=1.194*400/RISET
13	FB	Feedback pin. This pin is an interface terminal, which samples the voltage of each channel, and outputs the lowest voltage of the string to DC/DC converter
14	AGND	Ground pin. It would be useful when connected with PGND and exposed pad
15	VCC	Input voltage pin
16	EN	Enable pin. Logic high enables the IC and logic low disables the IC



**Functional Block Diagram** 





## Absolute Maximum Ratings (Note 1)

Symbol	Parameter	Value	Unit
V <sub>CC</sub>	Input Voltage	-0.3 to 6	V
VISET	ISET Pin Voltage	-0.3 to 6	V
V <sub>EN</sub>	EN Pin Voltage	-0.3 to 6	V
V <sub>FB</sub>	Feedback Pin Voltage	-0.3 to 6	V
V <sub>PWM</sub>	PWM Pin Voltage	-0.3 to 6	V
V <sub>CHX</sub>	Voltage per Channel (Note 2)	-0.3 to 40	V
θ <sub>JA</sub>	Thermal Resistance (Junction to Ambient, no Heat Sink)	80	°C/W
TJ	Operating Junction Temperature	+150	°C
T <sub>STG</sub>	Storage Temperature Range	-65 to +150	°C
T <sub>LEAD</sub>	Lead Temperature (Soldering, 10sec)	+260	°C
_	ESD (Machine Model)	200	V
_	ESD (Human Body Model)	6000	V

Notes 1: Stresses greater than those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "Recommended Operating Conditions" is not implied. Exposure to "Absolute Maximum Ratings" for extended periods may affect device reliability.

2: Breakdown voltage.

# **Recommended Operating Conditions**

Symbol	Paramete	Min	Max	Unit	
Vcc	Input Voltage		4.2	5.5	V
fpwm	Recommended PWM Dimming Fre	equency	0.1	25	kHz
	Full Scale Setting Current per	V <sub>CHX</sub> ≥0.5V	10	25	
Існх	Channel	V <sub>CHX</sub> ≥1V	10	65	mA
-017		V <sub>CHX</sub> ≥1.5V	10	110	
T <sub>A</sub>	Operating Temperature Range	·	-40	+85	°C



**Electrical Characteristics** (Limits in standard typeface are guaranteed for  $V_{IN}=V_{EN}=5V$ ,  $R_{ISET}=8k\Omega$ ,  $T_{A}=+25^{\circ}C$ , unless otherwise specified.)

Symbol Parameter Conditions Min Max Unit Тур Input Section 4.2 5.5 V VIN Input Voltage Quiescent Current No Load 1 lQ 0.5 mΑ \_ I<sub>SDB</sub> Shutdown Quiescent Current V<sub>EN</sub>=0V 0.1 μA v  $V_{\text{UVLO}}$ 3.8 Under Voltage Lockout Threshold Falling Edge 3.6 4.0 Under Voltage Lockout Hysteresis 200 VHUVLO mV \_ **Current Sink Section** VISET ISET Reference Voltage 1.170 1.194 1.218 V κ **Current Multiplication Ratio** ICHX/ISET 370 400 430 \_ V<sub>CHX</sub>=0.5V 23 45 \_ V<sub>CHX</sub>=1V Maximum Output Current per Channel 65 70 mΑ ICHX\_MAX \_ V<sub>CHX</sub>=1.5V 110 120 \_ Current Matching between Any Two Channels ICHX=60mA VCHX=1V Існ-матсн -1.5 1.5 % I<sub>CHX</sub>=20mA 0.45 \_ \_ Current Sink Saturation Voltage per Channel I<sub>CHX</sub>=60mA 0.8 V VCHX SAT \_ \_ I<sub>CHX</sub>=100mA 1.2 \_ \_ **Output Current Line Regulation** V<sub>CC</sub>=4.2V to 5.5V 2 %/V \_ \_ \_ Output Current Load Regulation V<sub>CHX</sub>=0.5V to 2.8V 4 % \_ \_ \_



**Electrical Characteristics** (Cont. Limits in standard typeface are guaranteed for  $V_{IN}=V_{EN}=5V$ ,  $R_{ISET}=8k\Omega$ ,  $T_{A}=+25^{\circ}C$ , unless otherwise specified.)

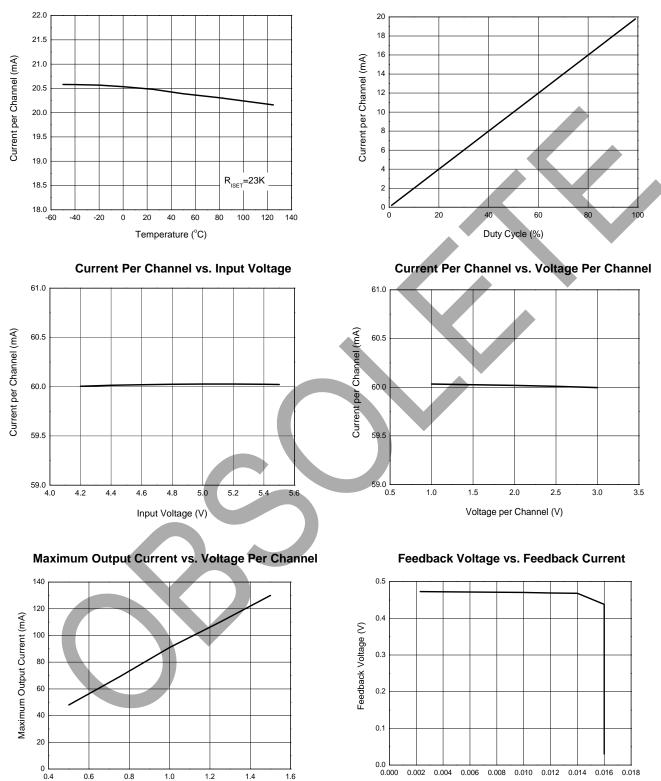
Symbol	Parameter	Conditions	Min	Тур	Max	Unit				
Enable Section				I	l					
V <sub>IH_EN</sub>	EN Pin High Level Threshold Voltage	_	1.8	_	_	V				
$V_{\text{IL}_{\text{EN}}}$	EN Pin Low Level Threshold Voltage	_	_	-	0.8	V				
PWM Dimming	Section									
V <sub>IH_PWM</sub>	PWM High Level Threshold Voltage	-	1.8	-	-	V				
$V_{\text{IL}_{\text{PWM}}}$	PWM Low Level Threshold Voltage	-		-	0.8	V				
Interface Sectio	Interface Section									
I <sub>FB</sub>	Feedback Output Current	-	5	15	_	μA				
Total Device	Fotal Device									
VCHECK	Self-check Voltage @ Open LED		-	3.0	_	V				
T <sub>OTSD</sub>	Thermal Shutdown Temperature	-	-	+160	_	°C				
T <sub>HYS</sub>	Thermal Shutdown Hysteresis	-	-	+20	_	°C				



### $\label{eq:performance Characteristics} (V_{IN}=V_{EN}=5V, \ R_{ISET}=8k\Omega, \ T_{A}=+25^{\circ}C, \ unless \ otherwise \ specified.)$



Current Per Channel vs. Duty Cycle



**OLETE – PART DISCONTINUED** 

Voltage per Channel (V)





### **Function Description**

The AP3609 is designed for LED display application which contains eight well-matched current sinks to provide constant current through LED. The full scale LED current can be adjusted from 10mA to 100mA per channel with an external resistor. If there is some channel unused, the channel pin should be connected to ground. The LED bright dimming can be achieved through PWM dimming.

The AP3609 can work with a DC/DC converter to drive LED arrays for good performance. The device can keep working when LED opens without damage, and it features under voltage lockout protection and over temperature protection. The detailed information will be discussed in open LED self-check and protection section.

### 1. LED Current Setting

The maximum LED current can be set up to 100mA per channel by ISET pin. When the LED current is greater than 100mA, two or more channels can be paralleled to achieve larger drive current. To set the reference current  $I_{SET}$ , connect a resistor  $R_{ISET}$  between this pin and ground. The value of  $R_{ISET}$  can be calculated by the following formula:

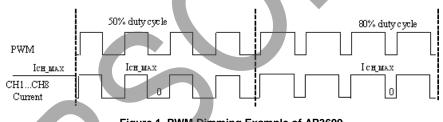
This reference current is multiplied internally with a gain (K) of 400, and then mirrored on all enabled channels. This sets the maximum LED current, referred to as 100% current (I<sub>CHX MAX</sub>). The value can be calculated by the following formula:

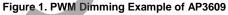
The LED current can be reduced from 100% by PWM dimming control.

### 2. PWM Dimming Mode

The LED current can be adjusted by applying the PWM signal to PWM pin. On this mode, all enabled channels are adjusted at the same time and the brightness can be adjusted from 1%<sup>\*</sup>I<sub>CHX\_MAX</sub> to 100%<sup>\*</sup>I<sub>CHX\_MAX</sub>(@f<sub>Dimming</sub>=2kHz). During the "high level" time of the PWM signal, the LED turns on and the 100% current flows through LED. During the "low level" time of the PWM signal, the LED turns off and almost no current flows through LED. So the average current through LED is changed and the brightness is adjusted. The external PWM signal frequency applied to PWM pin can be allowed to 100Hz or higher.

An example for PWM dimming is shown in Figure 1. All 8 channels are set to the maximum current  $I_{CHX\_MAX}$  at the beginning. When a 50% duty cycle PWM signal is applied to PWM pin, average current valued 50%<sup>\*</sup>  $I_{CHX\_MAX}$  flows through the 8 channels. When an 80% duty cycle PWM signal is applied to PWM pin, average current valued 80%<sup>\*</sup> $I_{CHX\_MAX}$  flows through the 8 channels.





### 3. Open LED Self-Check and Protection

The AP3609 can work with a DC/DC converter to achieve good performance, such as self-check and protection against open LED. The FB pin samples voltage of each channel, and outputs the lowest voltage to DC/DC converter. When AP3609 gets shutdown signal from EN pin or PWM voltage is low, AP3609 outputs a high Z signal (floating). The FB voltage should be decided by external pull-up resistor connected to it. Then the DC/DC converter can be controlled by AP3609.

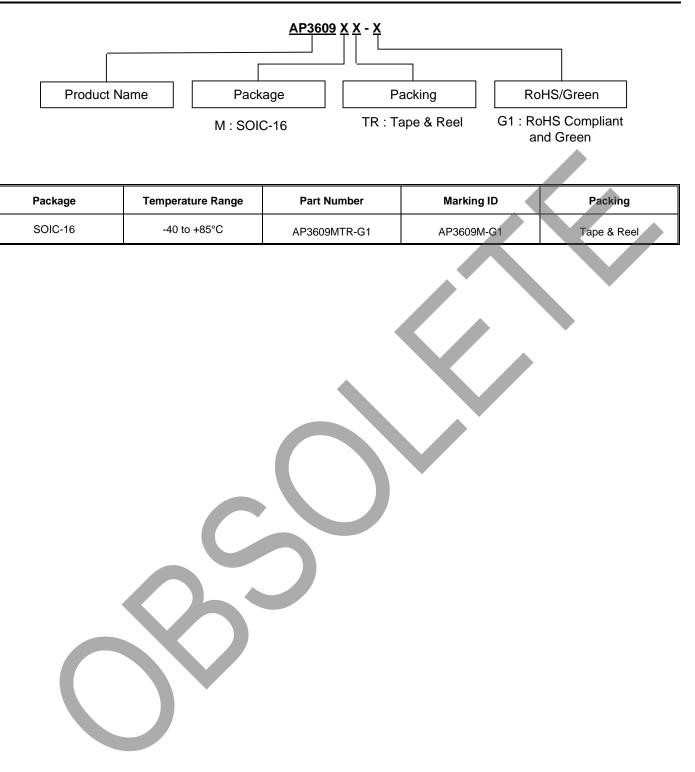
If any enabled LED string opens, voltage on the corresponding CHX pin goes to zero and the FB pin outputs the zero voltage to boost converter. So the boost converter operates in open loop and the voltage on remaining CHX pin goes higher. Once the voltage on remaining CHX pin reaches the self-check voltage 3V, the AP3609 begins looking up the open string. After finding the open channel, AP3609 removes the corresponding CHX pin from boost control loop, then the boost circuit is controlled in the normal manner. Once the circuit returns normal operation, the voltage on the CHX pin is regulated to the normal level. It is necessary to pay attention that the open strings are removed from boost regulation, but not disabled. If the open LED string is reconnected, it will sink current up to the programmed current level.

#### 4. Parallel Operation Mode

The AP3609 can be paralleled to drive more strings of LED. Connecting an AP3609 FB pin with another AP3609 FBX pin can achieve parallel application. More details please refer to Figure of typical applications of AP3609.



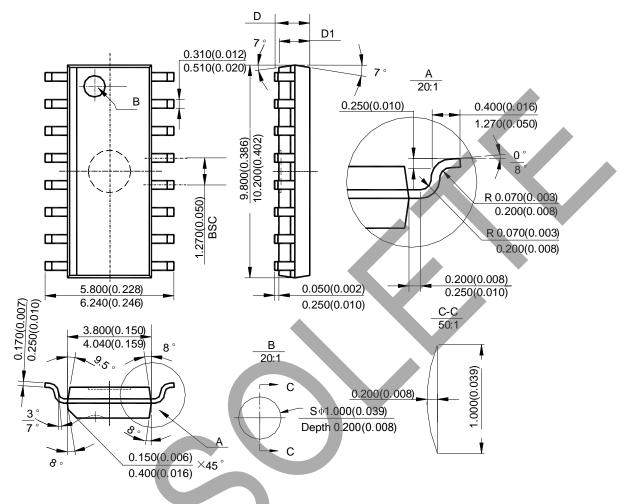
## **Ordering Information**





### Package Outline Dimensions (All dimensions in mm(inch).)

### (1) Package Type: SOIC-16



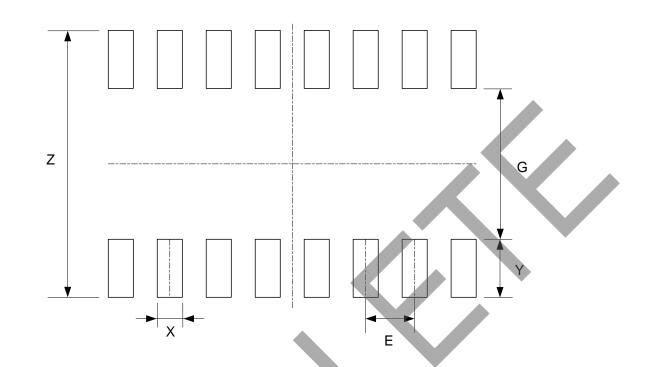
Note: Eject hole, oriented hole and mold mark is optional.

Symbol	D				D1				
	mi	n(mm)	max(mm)	min(inch)	max(inch)	min(mm)	max(mm)	min(inch)	max(inch)
Option1	1	.350	1.750	0.053	0.069	1.250	1.650	0.049	0.065
Option2		-	1.260	-	0.050	1.020	-	0.040	-
				•			•		



# Suggested Pad Layout

### (1) Package Type: SOIC-16



Dimensions	Z	G	X	Y	E
	(mm)/(inch)	(mm)/(inch)	(mm)/(inch)	(mm)/(inch)	(mm)/(inch)
Value	6.900/0.272	3.900/0.154	0.650/0.026	1.500/0.059	1.270/0.050





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