



DYNAMIC PSR ACCELERATOR

Description

The DIODES™ AP4340S is an output voltage detector for Primary Side Control System. It is a low power loss solution. It detects the output voltage and provides a periodical signal when the output voltage is lower than a certain threshold. The periodical signal can be coupled by the transformer to the primary side and provided as an awakening signal for the main primary side controller. By fast response to secondary side voltage, the AP4340S can effectively improve the transient performance of Primary Side Control System.

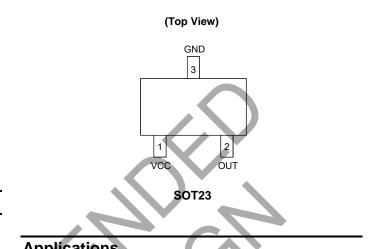
AP4340S will enable a discharge circuit when it detects the output voltage is higher than a certain threshold.

The AP4340S is available in SOT23 package.

Features

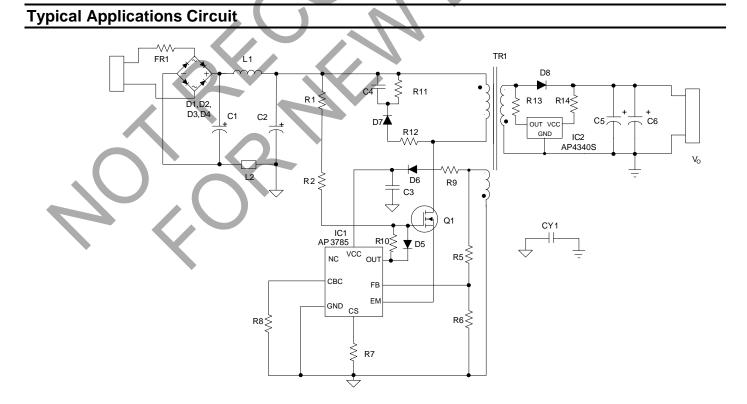
- Fast Detector of Supply Voltages
- 33kHz Output Pulse
- No External Components
- Low Power Loss for Green Mode Applications
- Totally Lead-free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

Pin Assignments



Applications

- Adapters/Chargers for Cell/Cordless Phones, ADSL Modems, MP3 and Other Portable Apparatus
- Standby and Auxiliary Power Supplies
- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" Notes: and Lead-free
 - 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

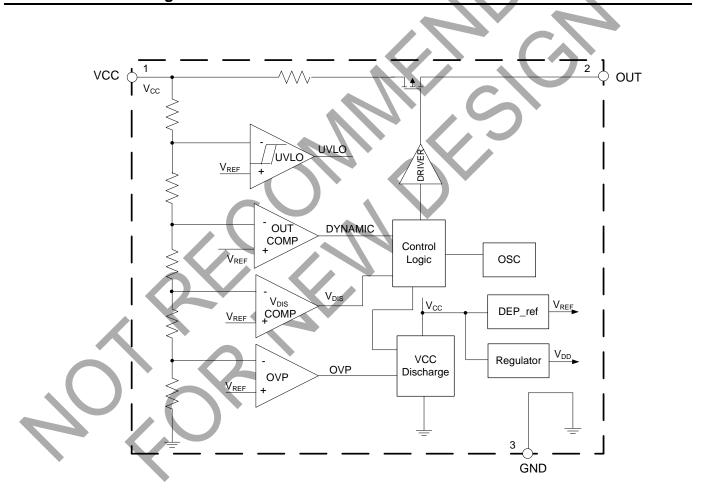




Pin Descriptions

Pin Number	Pin Name	Function
1	VCC	Power supply pin, connected with one end of the secondary winding and the output capacitor
2	OUT	Secondary detecting pin, connected with the other end of the secondary winding
3	GND	GND pin, connected with secondary side GND of the system

Functional Block Diagram





Absolute Maximum Ratings (Note 4)

Parameter	Rating			
Supply Voltage	-0.3 to 9			
Voltage at OUT	-40 to 7	V		
Voltage from VCC to OUT	-7 to 49	V		
Output Current at OUT	Internally limited	А		
Power Dissipation at T _A =+25°C	1.4	W		
Operating Junction Temperature	+150	٥C		
Storage Temperature	-65 to +150	٥C		
Lead Temperature (Soldering, 10 sec)	+300	٥C		
Thermal Resistance (Junction to Case)	140	°C/W		
Thermal Resistance (Junction to Ambient)	200	°C/W		

Note 4: Stresses greater than those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "Recommended Operating Conditions" is not implied. Exposure to "Absolute Maximum Ratings" for extended periods may affect device reliability.

Recommended Operating Conditions

Symbol	Parameter	Min	Max	Unit
V _{cc}	Supply Voltage	4	5.5	V
T _A	Ambient Temperature Range	-40	+85	°C

Electrical Characteristics (@V_{CC}=5V, T_A=+25°C, unless otherwise specified.)

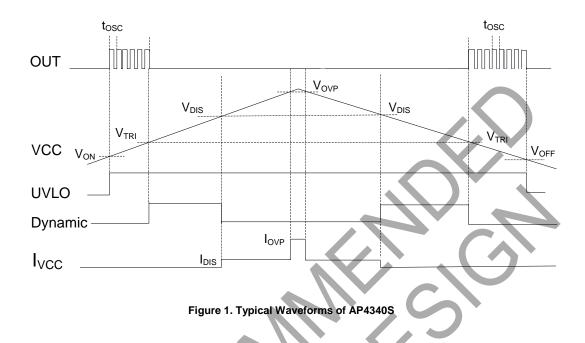
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Supply Voltag	je (VCC Pin)			•	•	•
V _{ON}	Power-on Voltage	-	2.5	3.1	4.6	V
I _{ST}	Startup Current	V _{CC} =V _{ON} -0.2V	-	5	10	μΑ
I _{OP}	Operating Current	OUT pin floating, V _{CC} =5V	10	15	20	μA
V _{OFF}	Power-off Voltage	-	2	2.8	4.2	V
V _{TRI}	Internal Trigger Voltage	-	4.65	4.73	4.8	V
Output Sectio	n/Oscillator Section					
DUTY	Duty Cycle	V _{CC} =4.5V	5	8.5	12	%
tosc	Oscillation Period	V _{CC} =4.5V	25	30	35	μs
I _{OUT}	Output Maximum Current	V _{CC} =4.5V	27	34	38	mA
V _{DIS}	Discharge Voltage	-	5.15	5.35	5.55	V
I _{DIS}	Discharge Current	_	1.5	3	4.5	mA
VOVP	Overshoot Voltage for Discharge	_	5.6	5.78	5.95	V
I _{OVP}	Overshoot Current for Discharge	-	120	170	240	mA

Note 5: The system output voltage is 5V.



AP4340S

Operation Description



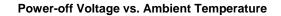
When VCC voltage is beyond power-on voltage (V_{ON}), the AP4340S starts up. The OUT pin asserts a periodical pulse and oscillation period is t_{OSC} . When VCC voltage is lower than trigger voltage (V_{TRI}), the periodical pulse in OUT pin is discontinued. When VCC voltage exceeds discharge voltage (V_{DIS}), the discharge circuit will be enabled, a 1mA current will flow into VCC pin. When VCC voltage is higher than overshoot voltage (V_{OVP}), AP4340S will enable a discharge circuit until the VCC voltage falls below the overshoot voltage. At the same time, the periodical pulse in OUT pin will be disabled.

When the VCC voltage is below power-off voltage (V_{OFF}), the AP4340S will be shut down.

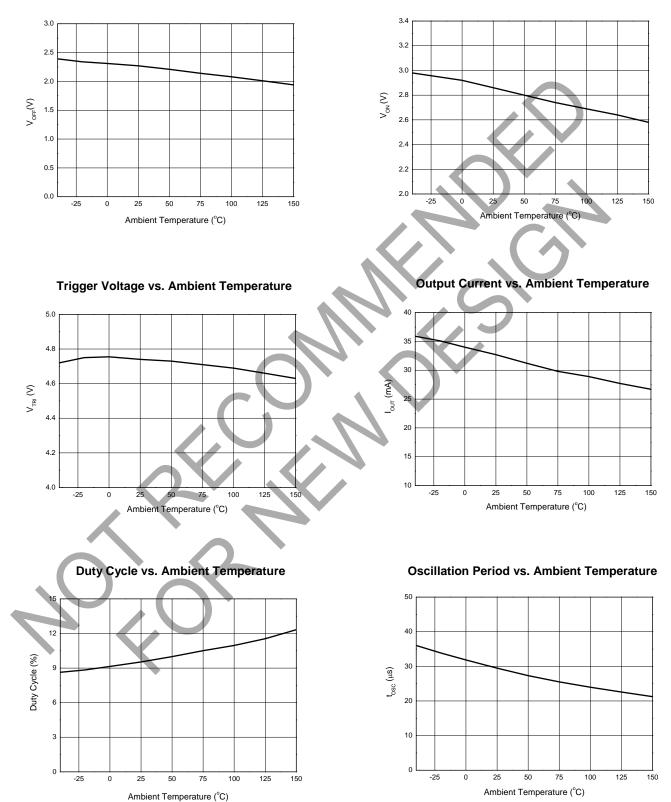




Performance Characteristics

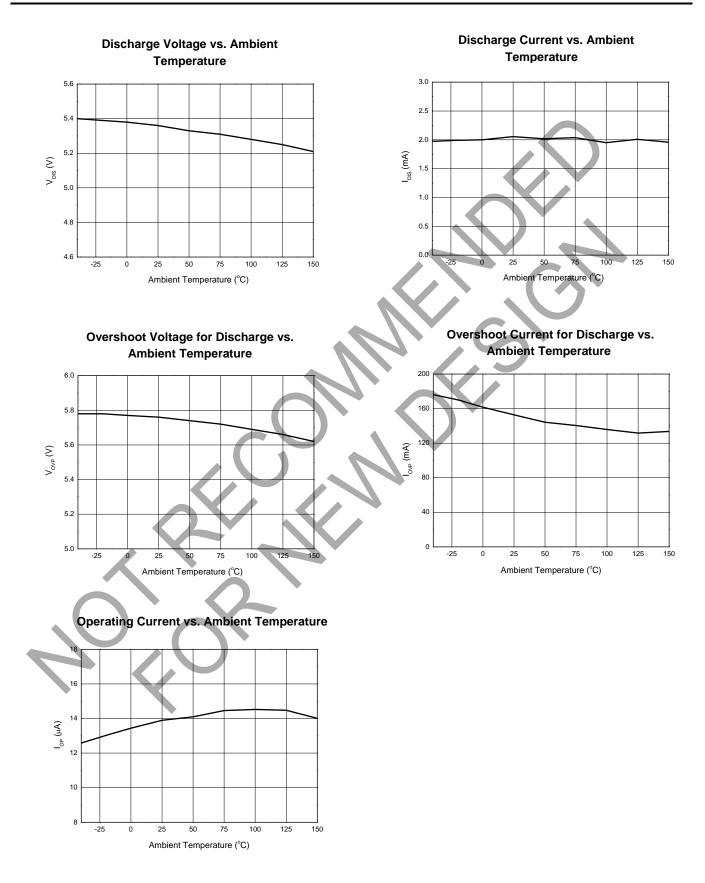


Power-on Voltage vs. Ambient Temperature





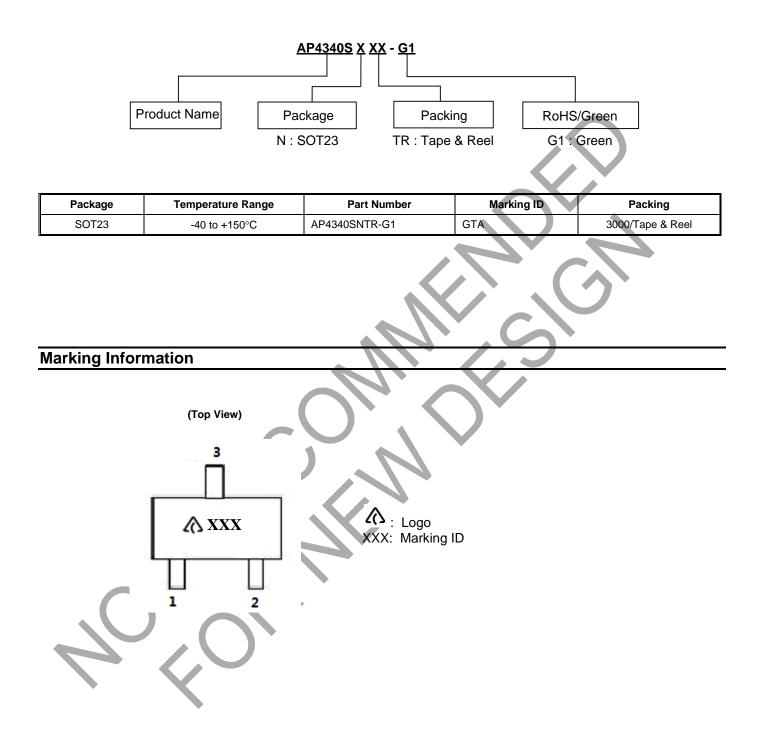
Performance Characteristics (Cont.)





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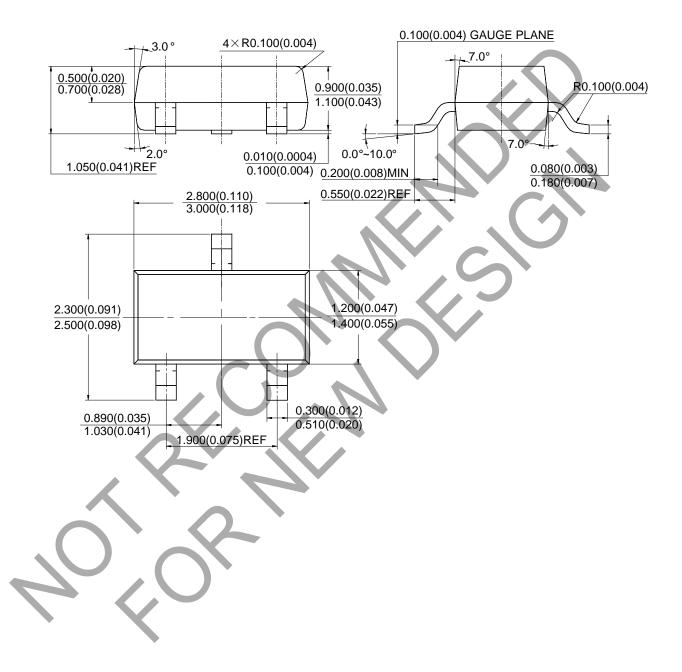
Ordering Information





Package Outline Dimensions (All dimensions in mm(inch).)

(1) Package Type: SOT23

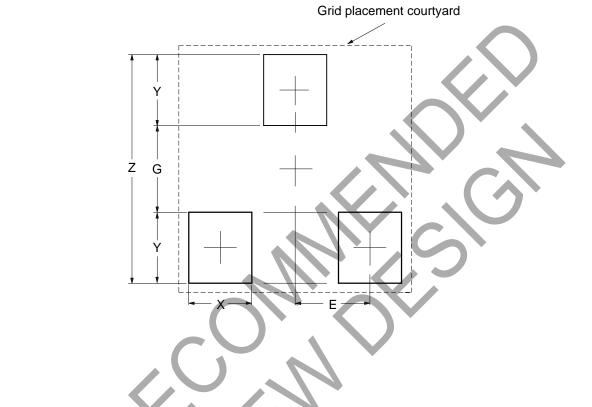




AP4340S

Suggested Pad Layout

(1) Package Type: SOT23



Dimensions	Z	G	X	Y	E
	(mm)/(inch)	(mm)/(inch)	(mm)/(inch)	(mm)/(inch)	(mm)/(inch)
Value	2.900/0.114	1.100/0.043	0.800/0.031	0.900/0.035	0.950/0.037





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