

Product Summary

VBR (Min)	IPP (Max)	CT (Typ)
15.5V	12A	60pF

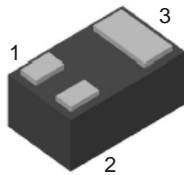
Description

This new generation TVS is designed to protect sensitive electronics from ESD and surge damages. The dual channels product with advantages of small size and high ESD surge capability is ideal for use in the Type-C CC protection for portable and computer applications.

Applications

- Cellular handsets
- Portable electronics
- Computers and peripherals
- Type-C CC pin protections

X1-DFN1006-3 (Type SA)



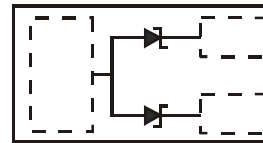
Bottom View

Features

- Two Channels of ESD and Surge Protection
- Provides ESD Protection per IEC 61000-4-2 Standard: Air $\pm 30\text{kV}$, Contact $\pm 30\text{kV}$
- Provides Surge and Lightning Protection per IEC 61000-4-5 Standard: IPP Max 12A
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- Halogen and Antimony Free. "Green" Device (Note 3)**
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please [contact us](https://www.diodes.com/contact-us) or your local Diodes representative. <https://www.diodes.com/quality/product-definitions/>**

Mechanical Data

- Package: X1-DFN1006-3
- Package Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Plated over Copper Leadframe. Solderable per MIL-STD-202, Method 208 $\text{\textcircled{3}}$
- Weight: 0.001 grams (Approximate)



Device Schematic

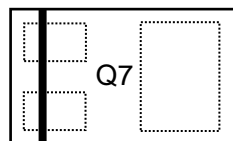
Ordering Information (Note 4)

Part Number	Package	Marking	Reel Size (inches)	Tape Width (mm)	Packing	
					Qty.	Carrier
D15V0M2U3SLP-7B	X1-DFN1006-3 (Type SA)	Q7	7	8	10,000	Tape & Reel

- Notes:
- No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
 - See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 - Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 - For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

Marking Information

X1-DFN1006-3 (Type SA)



Q7 = Product Type Marking Code
Bar Denotes Cathode Side

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	Conditions
Peak Pulse Current	I _{PP}	12	A	8/20μs (Figure 3)
ESD Protection—Contact Discharge	V _{ESD_CONTACT}	±30	kV	IEC 61000-4-2 Standard
ESD Protection—Air Discharge	V _{ESD_AIR}	±30	kV	IEC 61000-4-2 Standard

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Package Power Dissipation (Note 5)	P _D	250	mW
Thermal Resistance, Junction to Ambient (Note 5)	R _{θJA}	500	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Conditions
Reverse Working Voltage	V _{RWM}	—	—	15	V	—
Reverse Current (Note 6)	I _R	—	—	1	μA	V _R = V _{RWM}
Reverse Breakdown Voltage	V _{BR}	15.5	—	20	V	I _R = 1mA
Reverse Clamping Voltage (Note 7)	V _{CL}	—	18.8	—	V	I _{PP} = 1A, t _P = 8/20μs
		—	31.0	—		I _{PP} = 12A, t _P = 8/20μs
ESD Clamping Voltage (Note 8)	V _C	—	19.4	—	V	I _{PP} = 4A, t _P = 100ns
		—	22.6	—		I _{PP} = 16A, t _P = 100ns
Dynamic Resistance	R _{DYN}	—	0.28	—	Ω	TLP, t _P = 100ns
Capacitance	C _T	—	60	—	pF	V _R = 0V, f = 1MHz

- Notes:
- Device mounted on FR-4 PCB pad layout (2oz copper) as shown on Diodes Incorporated's suggested pad layout, which can be found on our website at <http://www.diodes.com/package-outlines.html>.
 - Short duration pulse test used to minimize self-heating effect.
 - Clamping voltage value is based on an 8x20μs peak pulse current (I_{PP}) waveform.
 - Transmission Line Pulse Test (TLP) settings: t_P=100ns, t_R=10ns, I_{TLP} and V_{TLP} averaging window is from 70ns to 90ns.

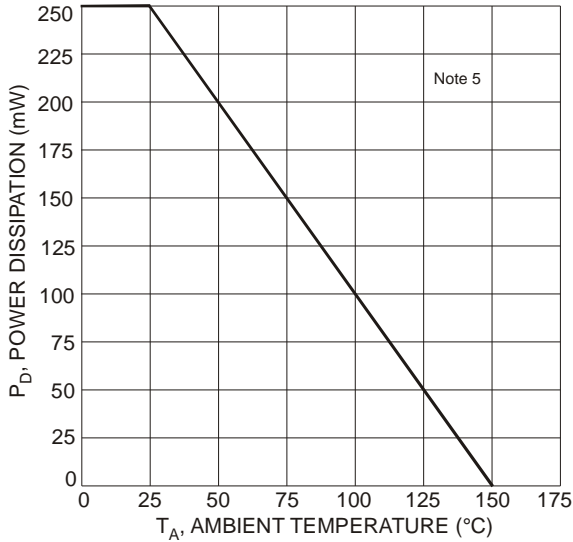


Figure 1 Power Derating Curve

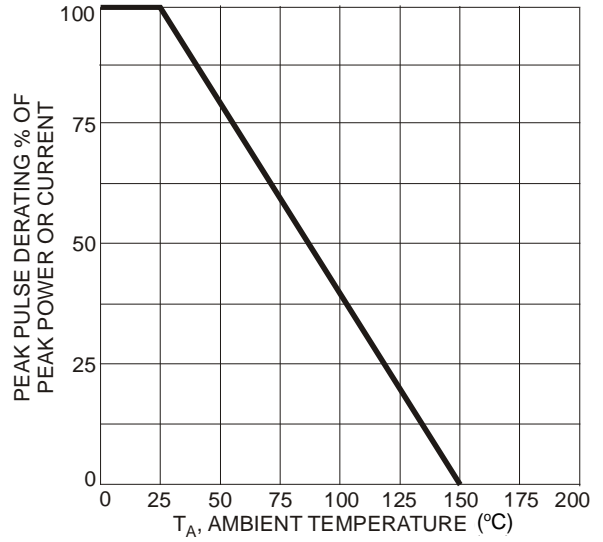


Figure 2 Pulse Derating Curve

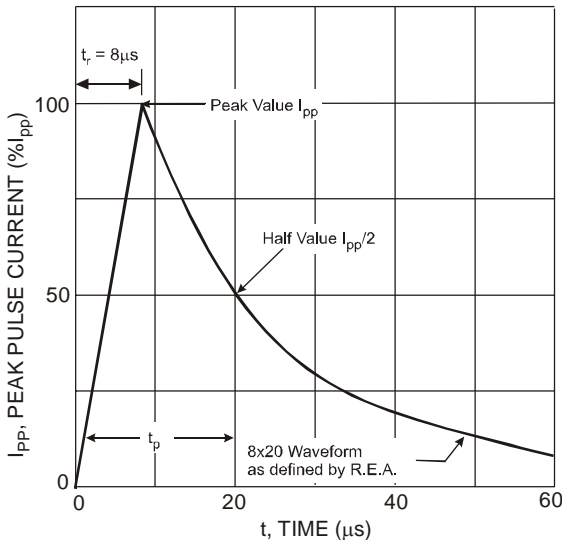


Figure 3 Typical 8 x 20µs Pulse Waveform

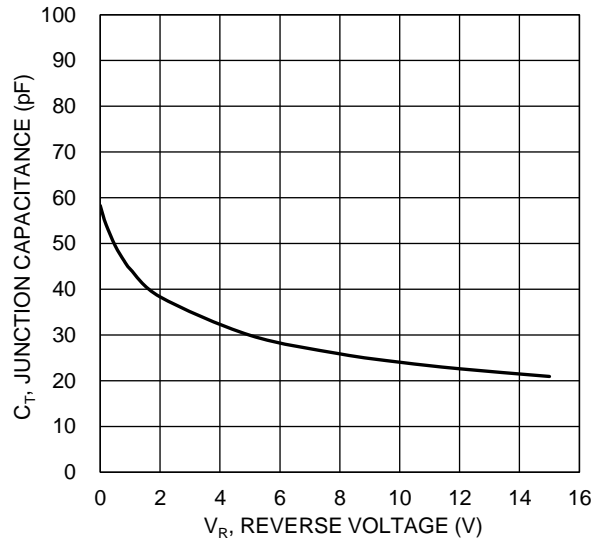


Figure 4 Typical Input Capacitance

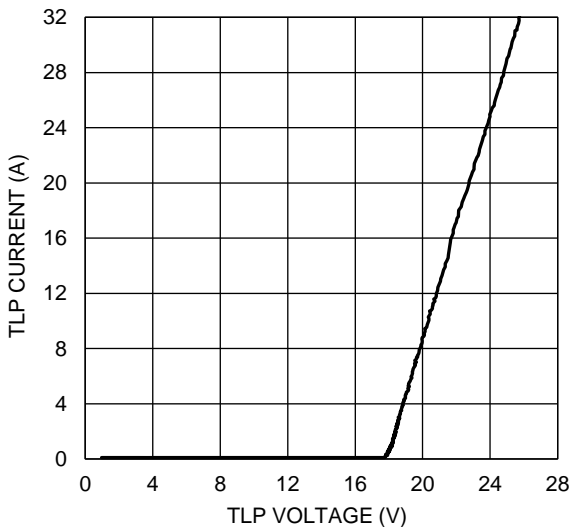


Figure 5 TLP Curve ($t_p = 100\text{ns}$)

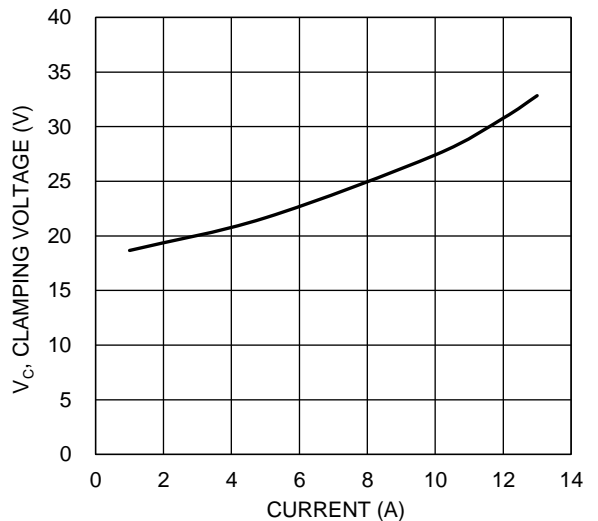
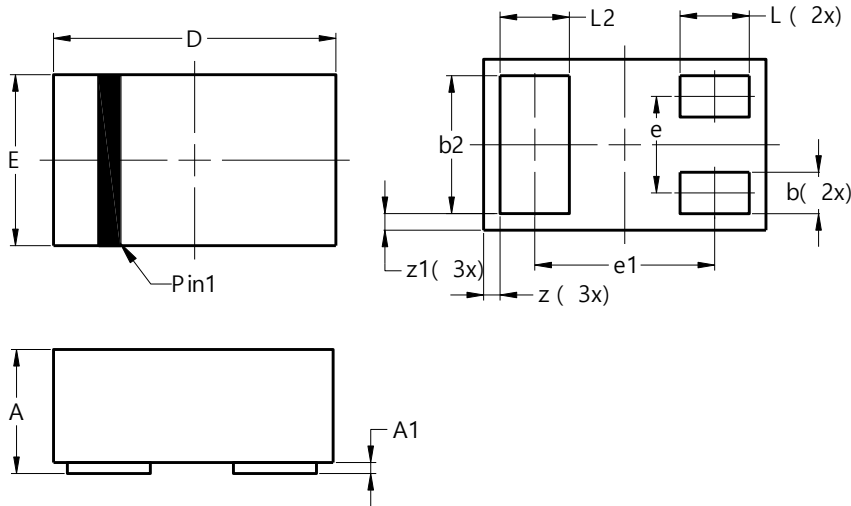


Figure 6 Clamping Voltage Characteristic ($t_p=8/20\mu\text{s}$)

Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

X1-DFN1006-3 (Type SA)

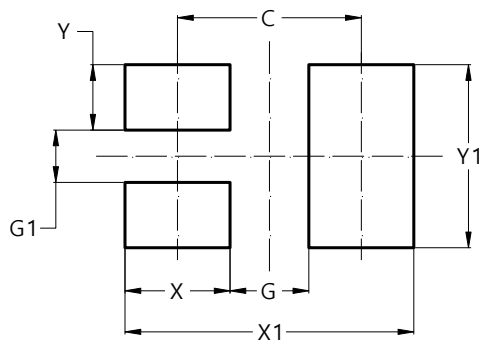


X1-DFN1006-3 (Type SA)			
Dim	Min	Max	Typ
A	0.400	0.500	--
A1	--	0.050	--
b	0.100	0.200	0.150
b2	0.450	0.550	0.500
D	0.990	1.050	1.020
E	0.590	0.650	0.620
e	0.350 BSC		
e1	0.650 BSC		
L	0.200	0.300	0.250
L2	0.200	0.300	0.250
z	0.020	0.100	0.060
z1	0.020	0.100	0.060
All Dimensions in mm			

Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

X1-DFN1006-3 (Type SA)



Dimensions	Value (in mm)
C	0.700
G	0.300
G1	0.200
X	0.400
X1	1.100
Y	0.250
Y1	0.700

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