



# D1V8L1BS2LP3Q

### ONE CHANNEL BI-DIRECTIONAL TVS

### **Product Summary**

VBR (Min)	IPP (Max)	Ст (Тур)
2.1V	22A	32pF

### Description

This new generation TVS is designed to protect sensitive electronics from the damage due to ESD and surge. The combination of small size and high ESD surge capability makes it ideal for use in portable applications such as cellular phones, digital cameras, and MP3 players.

### Features

- Low-Profile Package (0.30mm typical) and Ultra-Small PCB Footprint Area (0.61mm × 0.3mm max) Suitable for Compact Portable Electronics
- One Channel of ESD and Surge Protection
- Provides ESD Protection per IEC 61000-4-2 Standard: Air ±30kV, Contact ±30kV
- Provides Surge and Lightning Protection per IEC 61000-4-5 Standard: IPP max 22A
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- The D1V8L1BS2LP3Q is suitable for automotive applications requiring specific change control; this part is AEC-Q101 qualified, PPAP capable, and manufactured in IATF 16949 certified facilities.

https://www.diodes.com/quality/product-definitions/

## Applications

- Cellular handsets
- Portable electronics
- Computers and peripherals

## **Mechanical Data**

- Package: X3-DFN0603-2
- Package Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin over Copper Leadframe, Solderable per MIL-STD-202, Method 208 3
- Weight: 0.0002 grams (Approximate)

#### X3-DFN0603-2



Top View

Bottom View



**Device Schematic** 

### Ordering Information (Note 4)

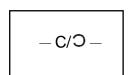
Orderable Part Number	t Number Package Marking Reel Size (inches) Tape Width (mm)					Packing		
Orderable Part Number Package Marking		Reel Size (Inches)	Tape Width (mm)	Qty.	Carrier			
D1V8L1BS2LP3Q-7	X3-DFN0603-2	C/C (Reversed)	7	8	10,000	Tape & Reel		

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.</p>

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

## **Marking Information**



#### X3-DFN0603-2

- C/ O- = Product Type Marking Code



## Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	Conditions
Peak Pulse Power Dissipation (Pin1 to Pin2)	P <sub>PP</sub>	175	W	8/20µs, per Figure 3
Peak Pulse Current	IPP	22	А	8/20µs, per Figure 3
ESD Protection — Contact Discharge	Vesd_contact	±30	kV	IEC 61000-4-2 Standard
ESD Protection — Air Discharge	Vesd_air	±30	kV	IEC 61000-4-2 Standard

# **Thermal Characteristics**

Characteristic	Symbol	Value	Unit
Package Power Dissipation (Note 5)	PD	250	mW
Thermal Resistance, Junction to Ambient (Note 5)	Reja	500	°C/W
Operating and Storage Temperature Range	TJ, TSTG	-55 to +150	°C

## Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Conditions
Reverse Working Voltage	Vrwm	_	_	1.8	V	—
Reverse Current (Note 6)	IR	_	_	1.0	μA	VR = VRWM
Reverse Breakdown Voltage	VBR	2.1	—	_	V	I <sub>R</sub> = 1mA
		—	3.0	—	V	IPP = 1A, tP = 8/20µs
Reverse Clamping Voltage (Note 7)	V <sub>CL</sub>	_	6.0	_		IPP = 22A, tp = 8/20µs
ESD Clamping Voltage (Note 8)		_	4.4	_	V	IPP = 8A, tP = 10/100ns
	Vc	_	5.8	—		IPP = 16A, tP = 10/100ns
Capacitance	Ст		32	_	pF	$V_R = 0V, f = 1MHz$

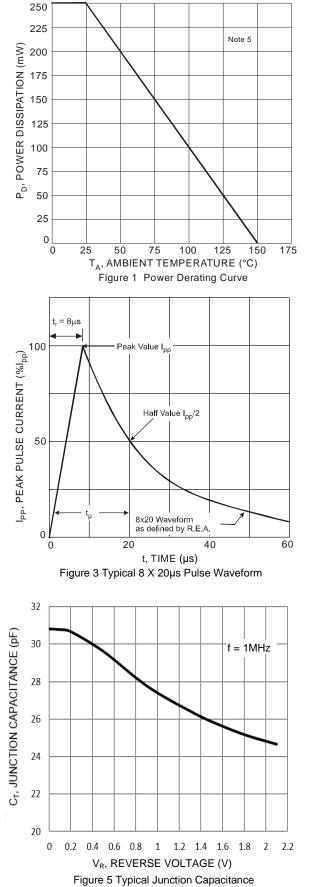
5. Device mounted on FR-4 PCB pad layout (2oz copper) as shown on Diodes Incorporated's suggested pad layout, which can be found on our website at Notes: http://www.diodes.com/package-outlines.html. 6. Short duration pulse test used to minimize self-heating effect.

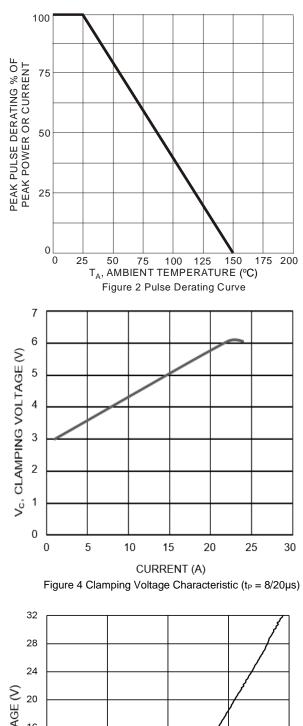
7. Clamping voltage value is based on an 8 x 20µs peak pulse current (I<sub>PP</sub>) waveform.

8. Transmission Line Pulse Test (TLP) settings:  $t_P = 100ns$ ,  $t_R = 10ns$ ,  $I_{TLP}$  and  $V_{TLP}$  averaging window is from 70ns to 90ns.



# D1V8L1BS2LP3Q





TLP VOLTAGE (V) 16 12 8 4 0 0 2 4 TLP CURRENT (A) Figure 6 TLP Voltage vs TLP Current

6

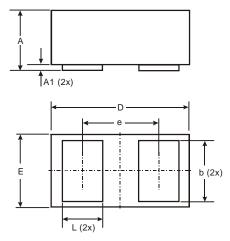
8



# **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.



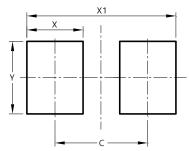


X3-DFN0603-2						
Dim	Min	Max	Тур			
Α	0.27	0.35	0.30			
A1	0.00	0.03	0.02			
b	0.19	0.29	0.24			
D	0.595	0.645	0.62			
ш	0.295	0.345	0.32			
e	-	-	0.355			
L	0.14	0.24	0.19			
All Dimensions in mm						

# **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.

X3-DFN0603-2



Dimensions	Value (in mm)
С	0.380
Х	0.230
X1	0.610
Y	0.300



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