Product Summary

V_{BR} (Min)	I_{PP} (Max)	C_T (Typ)
6.2V	40A	210pF

Features

- Low-Profile Package (0.53mm max) and Ultra-Small PCB Footprint Area (1.08mm x 0.68mm max) Suitable for Compact Portable Electronics
- Provides ESD Protection per IEC 61000-4-2 Standard: Air ±30kV, Contact ±30kV
- One Channel of ESD Protection
- Low Channel Input Capacitance
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **The D5V0S1U2LPSQ is suitable for automotive applications requiring specific change control; this part is AEC-Q101 qualified, PPAP capable, and manufactured in IATF16949 certified facilities.**
  https://www.diodes.com/quality/product-definitions/

Description and Applications

This new generation TVS is designed to protect sensitive electronics from the damage due to ESD. The combination of small size and high ESD surge capability makes it ideal for use in automotive infotainment applications including,

- USB modules
- HDMI inputs
- Infotainment consoles

Mechanical Data

- Package: U-DFN1006-2 with Sidewall Plating
- Package Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: 100% Sn (Tin)
  Solderable per MIL-STD-202, Method 208 e3
- Weight: 0.001 grams (Approximate)

U-DFN1006-2/SWP

Bottom View

Device Schematic

Ordering Information (Note 4)

Orderable Part Number	Package	Marking	Reel Size (inches)	Tape Width (mm)	Packing	
					Qty.	Carrier
D5V0S1U2LPSQ-7B	U-DFN1006-2/SWP	SK	7	8	10,000	Tape & Reel

Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information

SK = Product Type Marking Code
Bar Denotes Pin 1 or Cathode Side

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	Conditions
Peak Pulse Power Dissipation	P_{PP}	400	W	8/20µs, per Figure 3
Peak Pulse Current	I_{PP}	40	A	8/20µs, per Figure 3
ESD Protection – Contact Discharge	$V_{ESD\_CONTACT}$	±30	kV	IEC 61000-4-2 Standard
ESD Protection – Air Discharge	V_{ESD_AIR}	±30	kV	IEC 61000-4-2 Standard
ESD Protection – 1000 Contact Discharge (Open Alliance Spec)	$V_{ESD\_CONTACT1k}$	±30	kV	IEC 61000-4-2 Standard
ESD Protection – Contact Discharge (ISO Spec)	$V_{ESD\_CONTACT2}$	±30	kV	ISO 10605, 150pF, 330Ω
ESD Protection – Air Discharge (ISO Spec)	V_{ESD_AIR2}	±30	kV	ISO 10605, 150pF, 330Ω
ESD Protection – Contact Discharge (ISO Spec)	$V_{ESD\_CONTACT3}$	±30	kV	ISO 10605, 330pF, 330Ω
ESD Protection – Air Discharge (ISO Spec)	V_{ESD_AIR3}	±30	kV	ISO 10605, 330pF, 330Ω

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Package Power Dissipation (Note 5)	P_D	250	mW
Thermal Resistance, Junction to Ambient (Note 6)	$R_{\theta JA}$	500	°C/W
Operating and Storage Temperature Range	T_J, T_{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Conditions
Reverse Working Voltage	V_{RWM}	—	—	5.5	V	—
Reverse Current (Note 6)	I_R	—	0.1	1.0	µA	$V_R = V_{RWM} = 5.0V$
Reverse Breakdown Voltage	V_{BR}	6.2	—	7.4	V	I_R = 1mA
Reverse Clamping Voltage	V_{CL}	—	—	8.0	V	I_{PP} = 5A, t_P = 8/20µs
		—	—	10.0		I_{PP} = 30A, t_P = 8/20µs
		—	—	11.0		I_{PP} = 40A, t_P = 8/20µs
Capacitance	C_T	—	210	—	pF	V_R = 0V, f = 1MHz

Notes: 5. Device mounted on FR-4 PCB pad layout (2oz copper) as shown in Diodes Incorporated's package outline PDFs, which can be found on our website at http://www.diodes.com/package-outlines.html.
6. Short duration pulse test used to minimize self-heating effect.

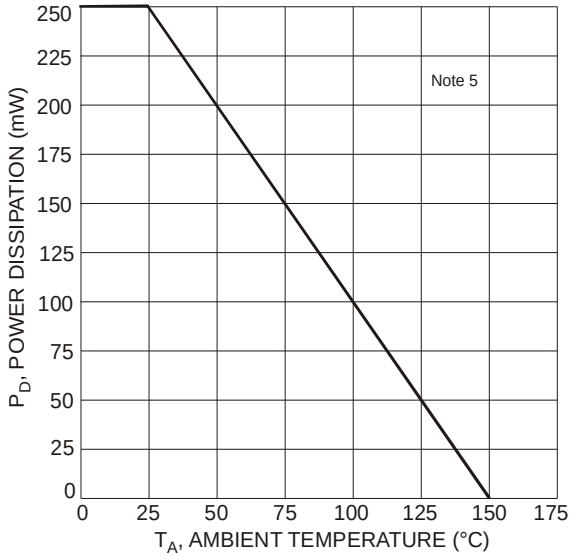


Figure 1 Power Derating Curve

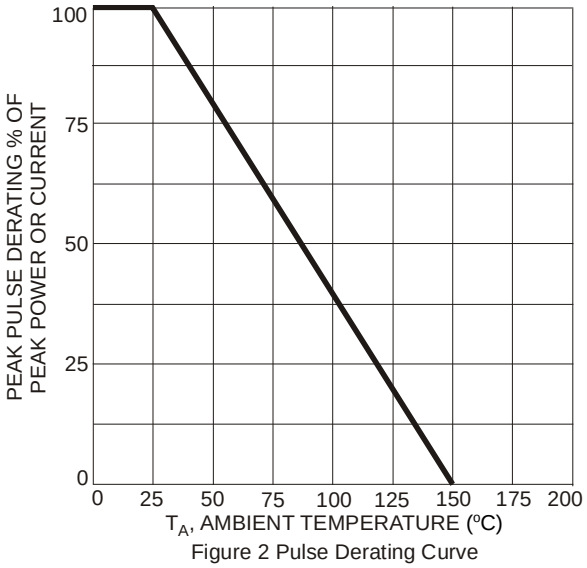


Figure 2 Pulse Derating Curve

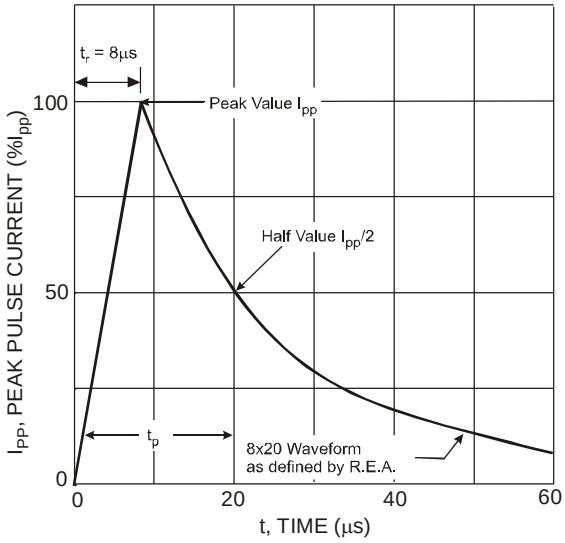


Figure 3 Typical 8 × 20µs Pulse Waveform

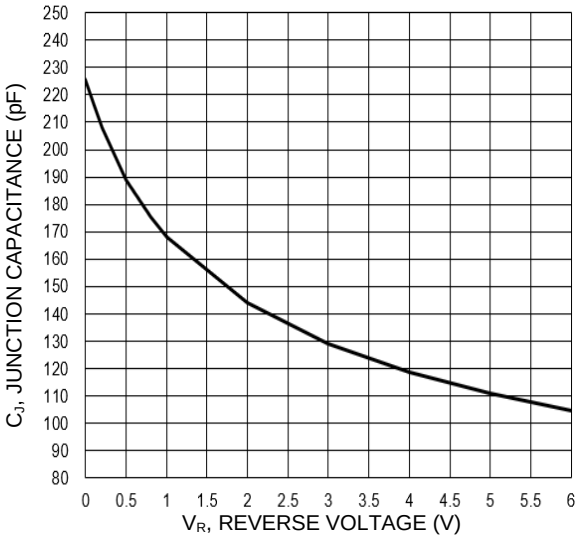


Figure 4 Typical Junction Capacitance

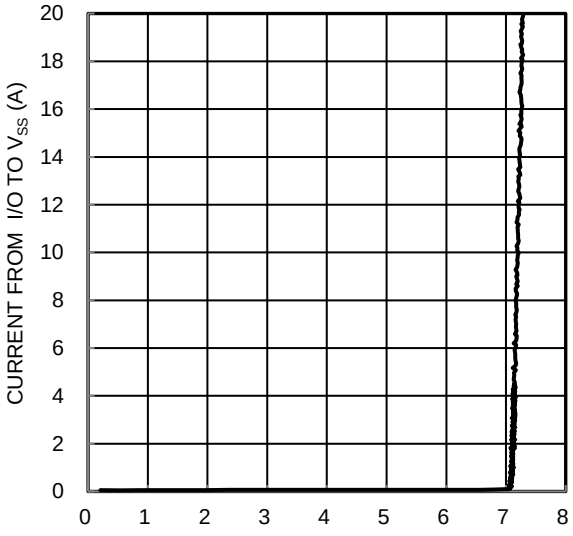


Figure 5 Current vs. Voltage

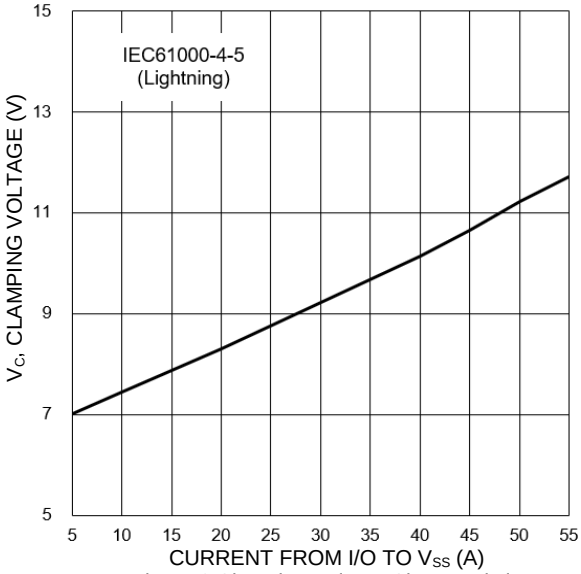
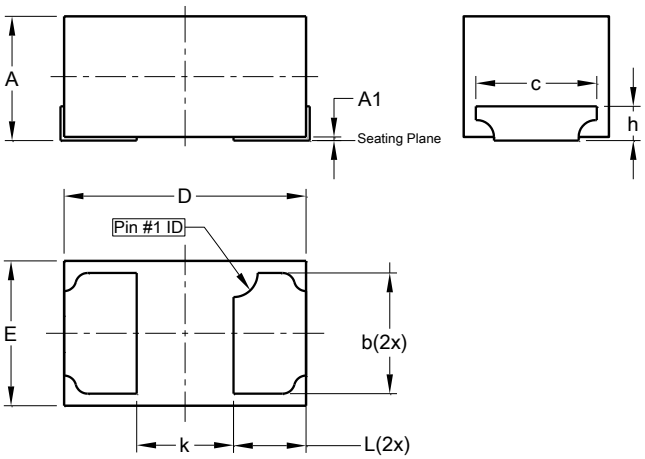


Figure 6 Clamping Voltage Characteristic

Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

U-DFN1006-2/SWP

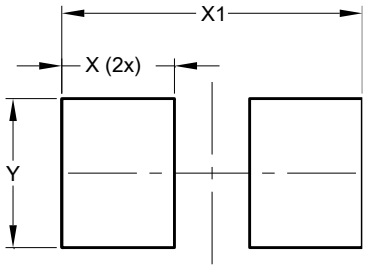


U-DFN1006-2/SWP			
Dim	**Min**	**Max**	**Typ**
A	0.47	0.53	0.50
A1	0.0	0.05	0.03
b	0.45	0.55	0.50
c	0.55 REF		
D	0.95	1.05	1.00
E	0.55	0.65	0.60
h	0.17 REF		
k	0.37 REF		
L	0.25	0.35	0.30
All Dimensions in mm			

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

U-DFN1006-2/SWP



Dimensions	Value (in mm)
X	0.45
X1	1.20
Y	0.60

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