



DCX56/-16

NPN SURFACE MOUNT TRANSISTOR

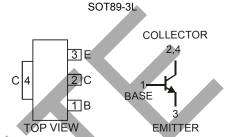
Features

- Epitaxial Planar Die Construction
- Complementary PNP Type Available (DCX53)
- Ideally Suited for Automated Assembly Processes
- Ideal for Medium Power Switching or Amplification Applications
- Lead Free By Design/RoHS Compliant (Note 1)
- "Green" Device (Note 2)

Mechanical Data

- Case: SOT89-3L
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020C
- Terminals: Finish Matte Tin annealed over Copper leadframe (Lead Free Plating). Solderable per MIL-STD-202, Method 208
- Marking & Type Code Information: See Page 3
- Ordering Information: See Page 3
- Weight: 0.072 grams (approximate)





Schematic and Pin Configuration

Maximum Ratings @TA = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V _{CBO}	100	V
Collector-Emitter Voltage	V _{CEO}	80	V
Emitter-Base Voltage	V _{EBO}	5	V
Collector Current	Ic	1	Α
Peak Pulse Current	Icm	1.5	A

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 3) @ T _A = 25°C	P _D	1	W
Operating and Storage Temperature Range	T _j , T _{STG}	-55 to +150	°C
Thermal Resistance, Junction to Ambient Air (Note 3) @T _A = 25°C	$R_{ hetaJA}$	125	°C/W

Electrical Characteristics @TA = 25°C unless otherwise specified

Charrata	wie 4i e	Currele el	Min	T	Mari	I I lasia	Took Conditions
		Symbol	Min	Тур	Max	Unit	Test Conditions
OFF CHARACTERISTICS (Not	OFF CHARACTERISTICS (Note 4)						
Collector-Base Breakdown Volta	age	$V_{(BR)CBO}$	100	_	_	V	$I_C = 100 \mu A, I_E = 0$
Collector-Emitter Breakdown Vo	ltage	$V_{(BR)CEO}$	80		_	V	$I_C = 10 \text{mA}, I_B = 0$
Emitter-Base Breakdown Voltag	е	$V_{(BR)EBO}$	5.0		_	V	$I_E = 10 \mu A, I_C = 0$
Collector-Base Cutoff Current					0.1	^	$V_{CB} = 30V, I_{E} = 0$
Collector-Base Cuton Current		Ісво			20	μΑ	$V_{CB} = 30V, I_{E} = 0, T_{A} = 150^{\circ}C$
Emitter-Base Cutoff Current		I _{EBO}	_		100	nA	$V_{EB} = 5.0V, I_{C} = 0$
ON CHARACTERISTICS (Note 4)							
	DCX56, DCX56-16	h _{FE} -	63		_	_	I _C = 5.0mA, V _{CE} = 2.0V
DC Current Cair	DCX56, DCX56-16		40		_		I _C = 500mA, V _{CE} = 2.0V
DC Current Gain	DCX56		63	_	250	_	I _C = 150mA, V _{CE} = 2.0V
	DCX56-16		100	_	250	_	I _C = 150mA, V _{CE} = 2.0V
Collector-Emitter Saturation Volt	tage	V _{CE(SAT)}	_	_	0.5	V	I _C = 500mA, I _B = 50mA
Base-Emitter Turn-On Voltage		V _{BE(ON)}	_	_	1.0	V	I _C = 500mA, V _{CE} = 2.0V
SMALL SIGNAL CHARACTERISTICS							
Current Gain-Bandwidth Produc	t	f _T	_	200	_	MHz	I _C = 50mA, V _{CE} = 5V, f = 100MHz
Output Capacitance		C_{obo}	_	_	15	pF	$V_{CB} = 10V, I_{E} = 0, f = 1MHz$

Notes: 1. No purposefully added lead.

- 2. Diodes Inc.'s "Green" policy can be found on our website at http://www.diodes.com/products/lead_free/index.php.
- 3. Device mounted on FR-4 PCB; pad layout as shown on page 4 or in Diodes Inc. suggested pad layout document AP02001, which can be found on our website at http://www.diodes.com/datasheets/ap02001.pdf.
- 4. Measured under pulsed conditions. Pulse width = $300\mu s$. Duty cycle $\leq 2\%$.



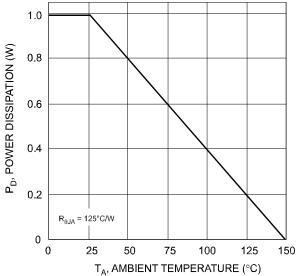
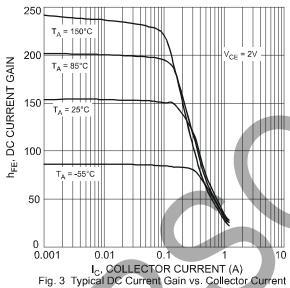
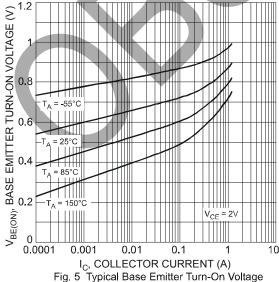
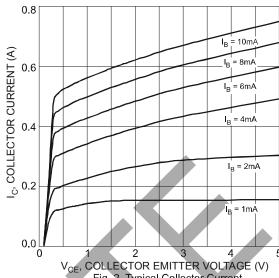


Fig. 1 Power Dissipation vs. Ambient Temperature





vs. Collector Current



V_{CE}, COLLECTOR EMITTER VOLTAGE (V) Fig. 2 Typical Collector Current vs. Collector Emitter Voltage

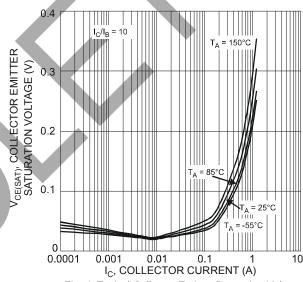


Fig. 4 Typical Collector Emitter Saturation Voltage vs. Collector Current

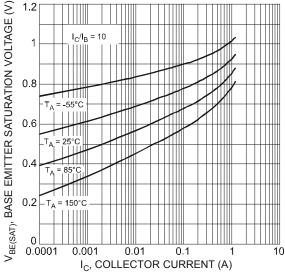
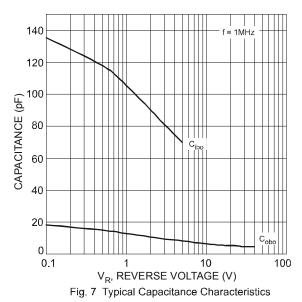
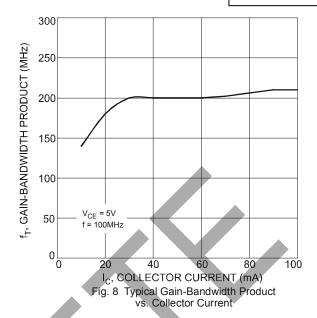


Fig. 6 Typical Base Emitter Saturation Voltage vs. Collector Current





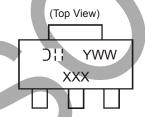


Ordering Information (Note 5)

Device	Packaging	Shipping
DCX56-13	SOT89-3L	2500/Tape & Reel
DCX56-16-13	SOT89-3L	2500/Tape & Reel

Notes: 5. For packaging details, go to our website at http://www.diodes.com/ap02007.pdf.

Marking Information

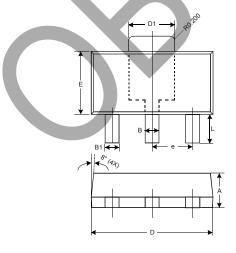


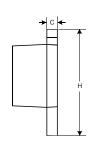
XXX = Product Type Marking Code ex. N18 = DCX56

N18 = DCX56 N18-16 = DCX56-16

Oll = Manufacturer's code marking YWW = Date Code Marking Y = Last digit of year ex: 7 = 2007 WW = Week code 01 - 52

Package Outline Dimensions

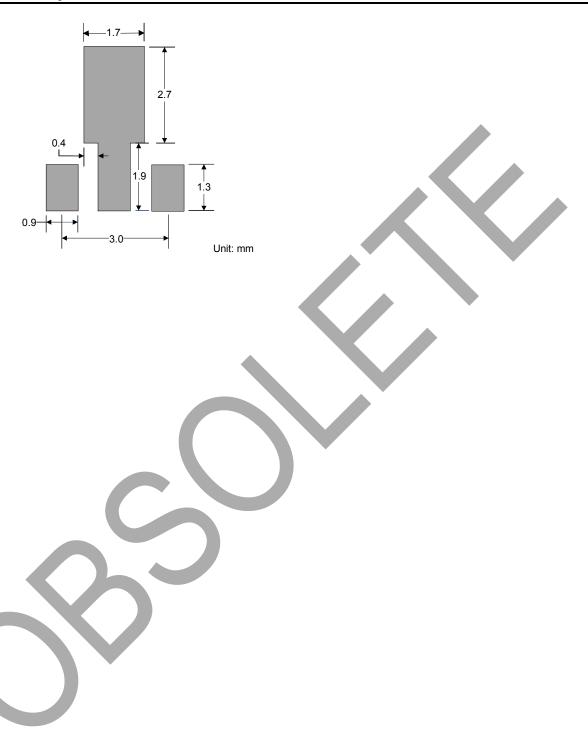




SOT89-3L					
Dim	Min	Max	Тур		
Α	1.40	1.60	1.50		
В	0.45	0.55	0.50		
B1	0.37	0.47	0.42		
С	0.35	0.43	0.38		
D	4.40	4.60	4.50		
D1	1.50	1.70	1.60		
Е	2.40	2.60	2.50		
е	-	-	1.50		
Н	3.95	4.25	4.10		
L	0.90	1.20	1.05		
All Dimensions in mm					



Suggested Pad Layout





IMPORTANT NOTICE

- 1. DIODES INCORPORATED AND ITS SUBSIDIARIES ("DIODES") MAKE NO WARRANTY OF ANY KIND, EXPRESS OR IMPLIED, WITH REGARDS TO ANY INFORMATION CONTAINED IN THIS DOCUMENT, INCLUDING, BUT NOT LIMITED TO, THE IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE OR NON-INFRINGEMENT OF THIRD PARTY INTELLECTUAL PROPERTY RIGHTS (AND THEIR EQUIVALENTS UNDER THE LAWS OF ANY JURISDICTION).
- 2. The Information contained herein is for informational purpose only and is provided only to illustrate the operation of Diodes products described herein and application examples. Diodes does not assume any liability arising out of the application or use of this document or any product described herein. This document is intended for skilled and technically trained engineering customers and users who design with Diodes products. Diodes products may be used to facilitate safety-related applications; however, in all instances customers and users are responsible for (a) selecting the appropriate Diodes products for their applications, (b) evaluating the suitability of the Diodes products for their intended applications, (c) ensuring their applications, which incorporate Diodes products, comply the applicable legal and regulatory requirements as well as safety and functional-safety related standards, and (d) ensuring they design with appropriate safeguards (including testing, validation, quality control techniques, redundancy, malfunction prevention, and appropriate treatment for aging degradation) to minimize the risks associated with their applications.
- 3. Diodes assumes no liability for any application-related information, support, assistance or feedback that may be provided by Diodes from time to time. Any customer or user of this document or products described herein will assume all risks and liabilities associated with such use, and will hold Diodes and all companies whose products are represented herein or on Diodes' websites, harmless against all damages and liabilities.
- 4. Products described herein may be covered by one or more United States, international or foreign patents and pending patent applications. Product names and markings noted herein may also be covered by one or more United States, international or foreign trademarks and trademark applications. Diodes does not convey any license under any of its intellectual property rights or the rights of any third parties (including third parties whose products and services may be described in this document or on Diodes' website) under this document.
- 5. Diodes products are provided subject to Diodes' Standard Terms and Conditions of Sale (https://www.diodes.com/about/company/terms-and-conditions/terms-and-conditions-of-sales/) or other applicable terms. This document does not alter or expand the applicable warranties provided by Diodes. Diodes does not warrant or accept any liability whatsoever in respect of any products purchased through unauthorized sales channel.
- 6. Diodes products and technology may not be used for or incorporated into any products or systems whose manufacture, use or sale is prohibited under any applicable laws and regulations. Should customers or users use Diodes products in contravention of any applicable laws or regulations, or for any unintended or unauthorized application, customers and users will (a) be solely responsible for any damages, losses or penalties arising in connection therewith or as a result thereof, and (b) indemnify and hold Diodes and its representatives and agents harmless against any and all claims, damages, expenses, and attorney fees arising out of, directly or indirectly, any claim relating to any noncompliance with the applicable laws and regulations, as well as any unintended or unauthorized application.
- 7. While efforts have been made to ensure the information contained in this document is accurate, complete and current, it may contain technical inaccuracies, omissions and typographical errors. Diodes does not warrant that information contained in this document is error-free and Diodes is under no obligation to update or otherwise correct this information. Notwithstanding the foregoing, Diodes reserves the right to make modifications, enhancements, improvements, corrections or other changes without further notice to this document and any product described herein. This document is written in English but may be translated into multiple languages for reference. Only the English version of this document is the final and determinative format released by Diodes.
- 8. Any unauthorized copying, modification, distribution, transmission, display or other use of this document (or any portion hereof) is prohibited. Diodes assumes no responsibility for any losses incurred by the customers or users or any third parties arising from any such unauthorized use.

Copyright © 2021 Diodes Incorporated

www.diodes.com