OBSOLETE

DDTC (R2-ONLY SERIES) KA

NPN PRE-BIASED SMALL SIGNAL SURFACE MOUNT TRANSISTOR

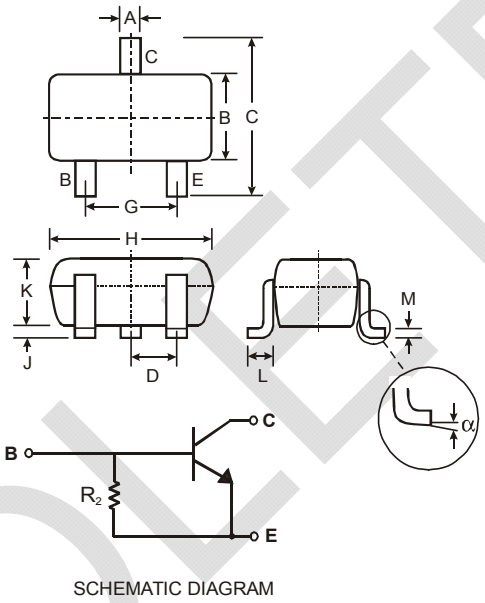
Obsolete Part Number	Alternative Part Number
DDTC114GKA	DDTC114GCA
DDTC115GKA	DDTC115GCA
DDTC124GKA	DDTC124GCA
DDTC144GKA	DDTC144GCA

Features

- Epitaxial Planar Die Construction
- Complementary PNP Types Available (DDTA)
- Built-In Biasing Resistor, R2 only
- **Lead Free/RoHS Compliant (Note 2)**
- **"Green" Device, Note 3 and 4**

Mechanical Data

- Case: SC-59
- Case Material: Molded Plastic, "Green" Molding Compound, Note 4. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020C
- Terminals: Solderable per MIL-STD-202, Method 208
- Lead Free Plating (Matte Tin Finish annealed over Copper leadframe).
- Terminal Connections: See Diagram
- Marking Information: See Table Below & Page 3
- Ordering Information: See Page 3
- Weight: 0.006 grams (approximate)



SCHEMATIC DIAGRAM

SC-59		
Dim	**Min**	**Max**
A	0.35	0.50
B	1.50	1.70
C	2.70	3.00
D	0.95	
G	1.90	
H	2.90	3.10
J	0.013	0.10
K	1.00	1.30
L	0.35	0.55
M	0.10	0.20
α	0°	8°
All Dimensions in mm		

P/N	R2 (NOM)	Type Code
DDTC114GKA	10KΩ	N26
DDTC124GKA	22KΩ	N27
DDTC144GKA	47KΩ	N28
DDTC115GKA	100KΩ	N29

Maximum Ratings @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V_{CBO}	50	V
Collector-Emitter Voltage	V_{CEO}	50	V
Emitter-Base Voltage	V_{EBO}	5	V
Collector Current	I_C (Max)	100	mA
Power Dissipation	P_d	200	mW
Thermal Resistance, Junction to Ambient Air (Note 1)	$R_{\theta JA}$	625	°C/W
Operating and Storage Temperature Range	T_j, T_{STG}	-55 to +150	°C

Notes:
1. Mounted on FR4 PC Board with recommended pad layout at http://www.diodes.com/datasheets/ap02001.pdf.
2. No purposefully added lead.
3. Diodes Inc.'s "Green" policy can be found on our website at http://www.diodes.com/products/lead_free/index.php.
4. Product manufactured with Date Code 0627 (week 27, 2006) and newer are built with Green Molding Compound. Product manufactured prior to Date Code 0627 are built with Non-Green Molding Compound and may contain Halogens or Sb2O3 Fire Retardants.

Electrical Characteristics @T_A = 25°C unless otherwise specified

Characteristic		Symbol	Min	Typ	Max	Unit	Test Condition
Collector-Base Breakdown Voltage		BV_{CBO}	50	—	—	V	I_C = 50μA
Collector-Emitter Breakdown Voltage		BV_{CEO}	50	—	—	V	I_C = 1mA
Emitter-Base Breakdown Voltage		BV_{EBO}	5	—	—	V	I_E = 720μA, DDTC114GKA I_E = 330μA, DDTC124GKA I_E = 160μA, DDTC144GKA I_E = 72μA, DDTC115GKA
Collector Cutoff Current		I_{CBO}	—	—	0.5	μA	V_{CB} = 50V
Emitter Cutoff Current	DDTC114GKA DDTC124GKA DDTC144GKA DDTC115GKA	I_{EBO}	300 140 65 30	—	580 260 130 58	μA	V_{EB} = 4V
Collector-Emitter Saturation Voltage		$V_{CE(sat)}$	—	—	0.3	V	I_C = 10mA, I_B = 0.5mA
DC Current Transfer Ratio	DDTC114GKA DDTC124GKA DDTC144GKA DDTC115GKA	h_{FE}	30 56 68 82	—	—	—	I_C = 5mA, V_{CE} = 5V
Bleeder Resistor (R_2) Tolerance		ΔR_2	-30	—	+30	%	—
Gain-Bandwidth Product*		f_T	—	250	—	MHz	V_{CE} = 10V, I_E = -5mA, f = 100MHz

* Transistor - For Reference Only

Typical Curves – DDTC114GKA

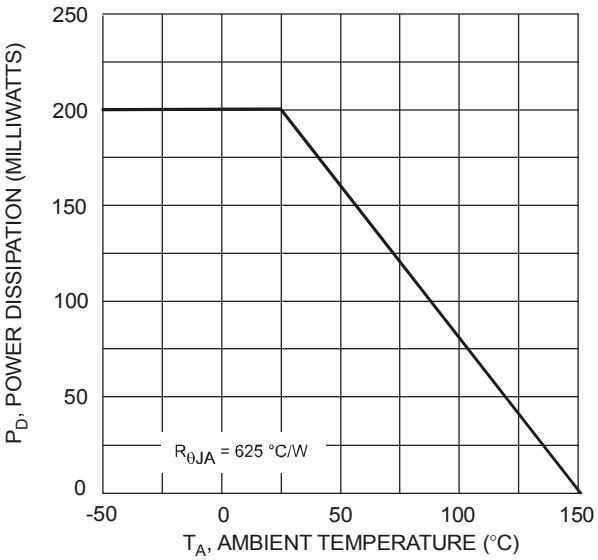


Fig. 1 Derating Curve

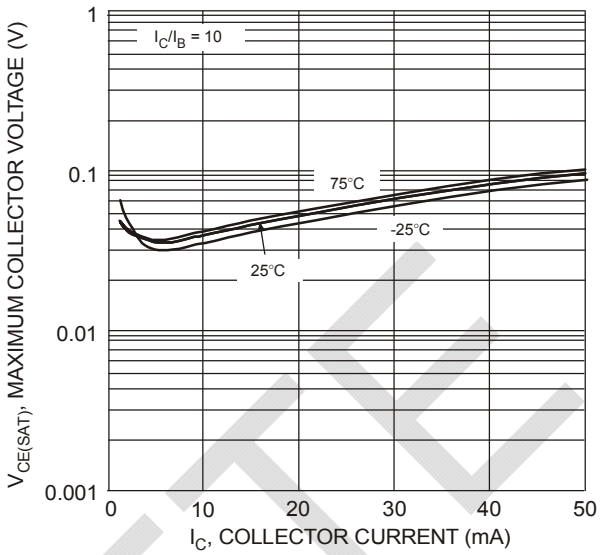


Fig. 2 $V_{CE(SAT)}$ vs. I_C

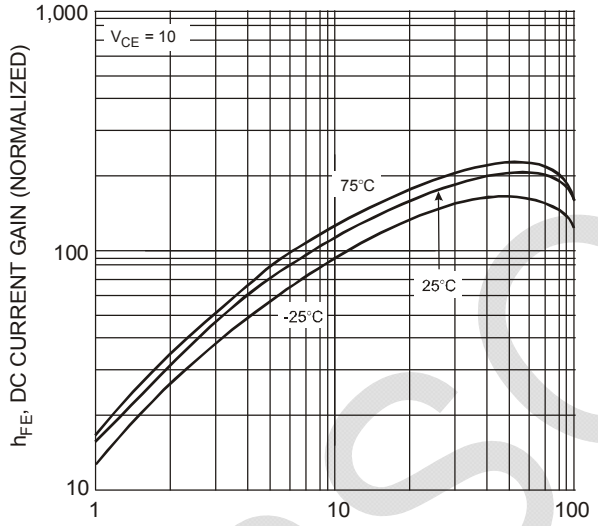


Fig. 3 DC Current Gain

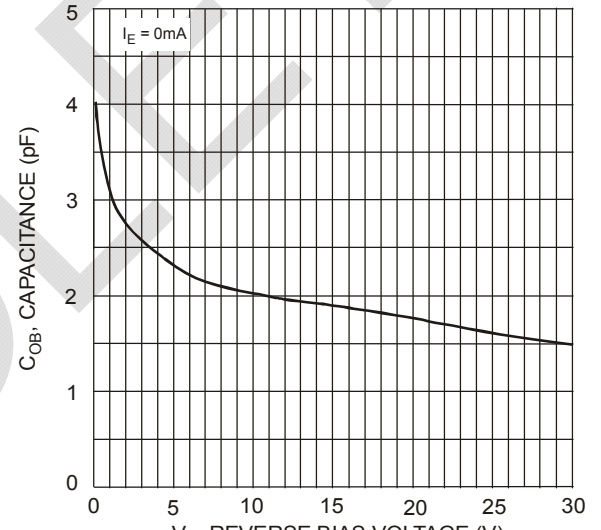


Fig. 4 Output Capacitance

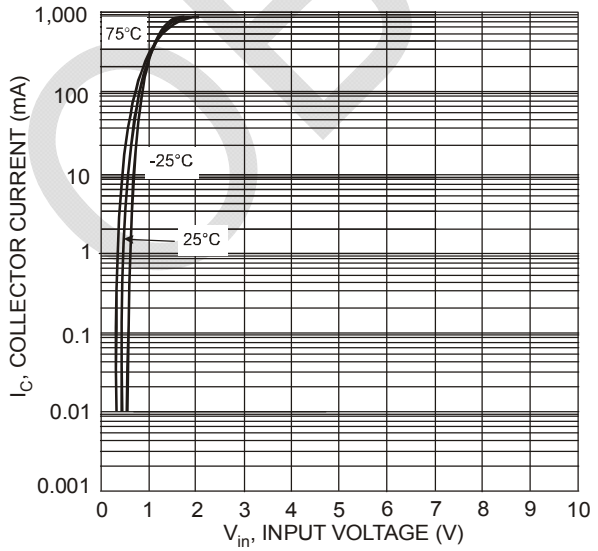


Fig. 5 Collector Current vs. Input Voltage

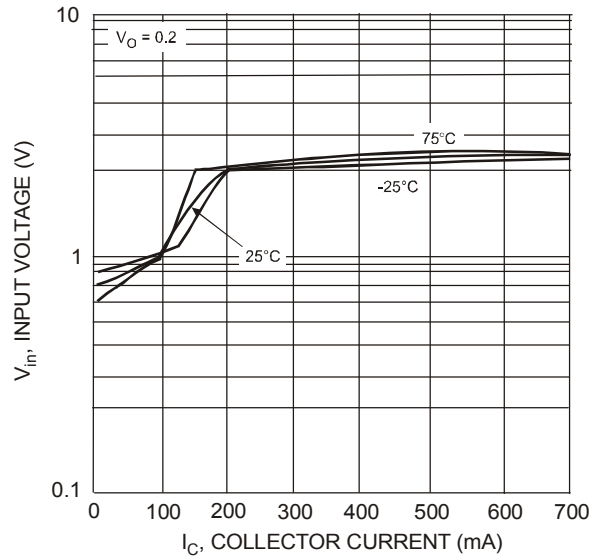


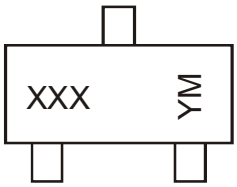
Fig. 6 Input Voltage vs. Collector Current

Ordering Information (Note 4 & 5)

Device	Packaging	Shipping
DDTC114GKA-7-F	SC-59	3000/Tape & Reel
DDTC124GKA-7-F	SC-59	3000/Tape & Reel
DDTC144GKA-7-F	SC-59	3000/Tape & Reel
DDTC115GKA-7-F	SC-59	3000/Tape & Reel

Notes: 5. For packaging details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

Marking Information



XXX = Product Type Marking Code, See Table on Page 1
YM = Date Code Marking
Y = Year ex: T = 2006
M = Month ex: 9 = September

Date Code Key

Year	2006	2007	2008	2009	2010	2011	2012
Code	T	U	V	W	X	Y	Z

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

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