

OBSOLETE - PART DISCONTINUED

Product Summary (@T_A = +25°C)

V _{RRM} (V)	I _o (A)	V _F Max (V)	I _R Max (μA)
800	1	1.1	10

Description and Applications

Packaged in the compact thermally efficient PowerDI[®]123 package, the DFLR1800 provides high surge capacity and high efficiency. It is ideally suited for use in:

- AC-DC adaptors/chargers
- DC-DC converters
- Power supplies

Features and Benefits

- Ideally Suited for Automated Assembly
- Patented Interlocking Clip Design for High Surge Capacity, US Patent #7,095,113
- **Lead-Free Finish; RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **For automotive applications requiring specific change control (i.e.: parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please refer to the related automotive grade (Q-suffix) part. A listing can be found at <https://www.diodes.com/products/automotive/automotive-products/>.**
- **This part is qualified to JEDEC standards (as references in AEC-Q) for High Reliability.** <https://www.diodes.com/quality/product-definitions/>

Mechanical Data

- Package: PowerDI123
- Package Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: Cathode Band
- Terminals: Finish – Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 Ⓢ
- Weight: 0.01 grams (Approximate)

PowerDI123

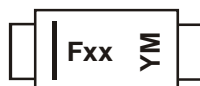
Top View

Ordering Information (Note 4)

Part Number	Marking Code	Package	Packing	
			Qty.	Carrier
DFLR1800-7	F18 or F1g	PowerDI123	3000	Tape & Reel

- Notes:
1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
 2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 4. For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

Marking Information



Fxx = Product Type Marking Code
 F18 or F1g = DFLR1800
 YM = Date Code Marking
 Y = Year (ex: K = 2023)
 M = Month (ex: 9 = September)

Date Code Key

Year	2010	-	2023	2024	2025	2026	2027	2028	2029	2030	2031	2032
Code	X	-	K	L	M	N	O	P	R	S	T	U

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Single phase, half wave, 60Hz, resistive or inductive load.
For capacitive load, derate current by 20%.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage	V _{RRM}	800	V
Working Peak Reverse Voltage	V _{RWM}		
DC Blocking Voltage	V _R		
Average Rectified Output Current (See Figure 4)	I _O	1.0	A
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine Wave Superimposed on Rated Load	I _{FSM}	25	A

Thermal Characteristics

Characteristic	Symbol	Typ	Max	Unit
Thermal Resistance, Junction to Ambient Air (Note 5)	R _{θJA}	134	—	°C/W
Thermal Resistance, Junction to Soldering Point (Note 6)	R _{θJS}	—	6	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	—	-65 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Conditions
Reverse Breakdown Voltage (Note 7)	V _{(BR)R}	800	—	—	V	I _R = 10μA
Forward Voltage Drop	V _F	—	0.65	—	V	I _F = 1.0mA, T _J = 0°C
		—	0.60	—		I _F = 1.0mA, T _J = +25°C
		—	0.48	—		I _F = 1.0mA, T _J = +85°C
		—	0.94	1.1		I _F = 1.0A, T _J = +25°C
		—	0.83	1.0		I _F = 1.0A, T _J = +125°C
Reverse Leakage Current (Note 7)	I _R	—	—	10 150	μA	V _R = 800V, T _J = +25°C V _R = 800V, T _J = +125°C
Reverse Recovery Time	t _{rr}	—	1.6	—	μs	I _F = 0.5A, I _R = 1A, I _{RR} = 0.25A
Total Capacitance	C _T	—	10	—	pF	V _R = 4.0V _{DC} , f = 1MHz

- Notes:
- Device mounted on 1in. x 1in., FR-4 PCB; 2oz Cu pad layout as shown on Diodes Incorporated's suggested pad layout, which can be found on website at <http://www.diodes.com/package-outlines.html>. T_A = +25°C.
 - Theoretical R_{θJS} calculated from the top center of the die straight down to the PCB/cathode tab solder junction.
 - Short duration test pulse used to minimize self-heating effect.

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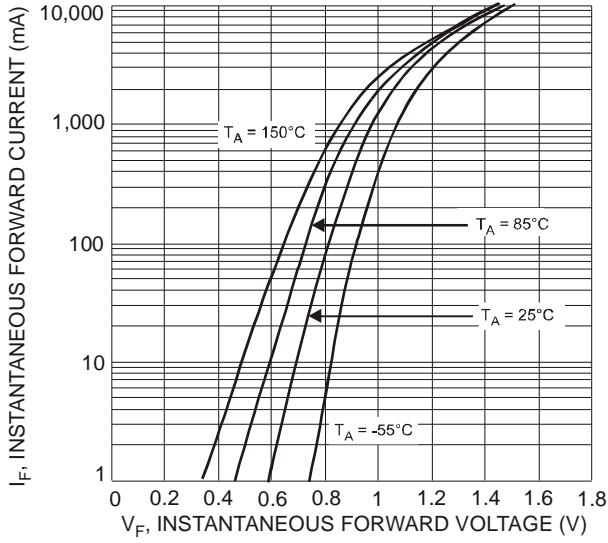


Figure 1 Typical Forward Characteristics

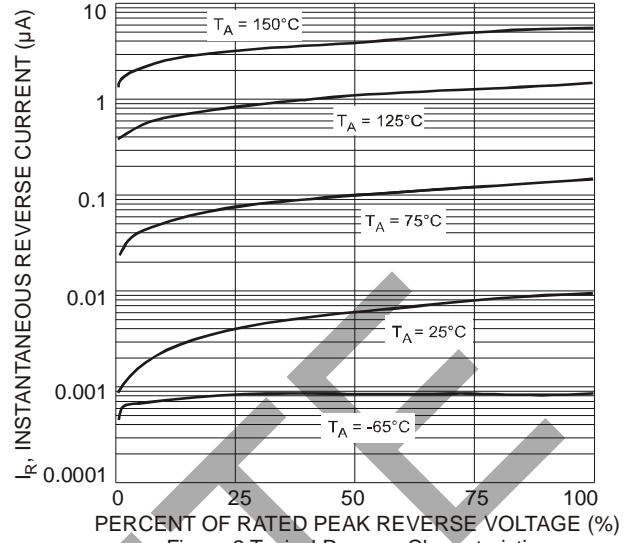


Figure 2 Typical Reverse Characteristics

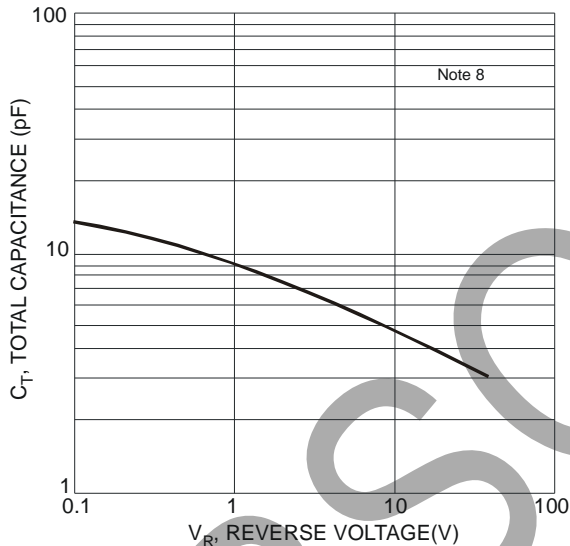


Figure 3 Typical Junction Capacitance

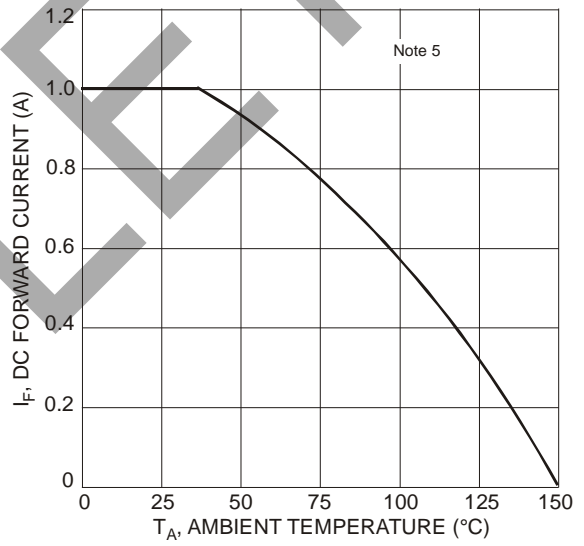


Figure 4 DC Forward Current Derating

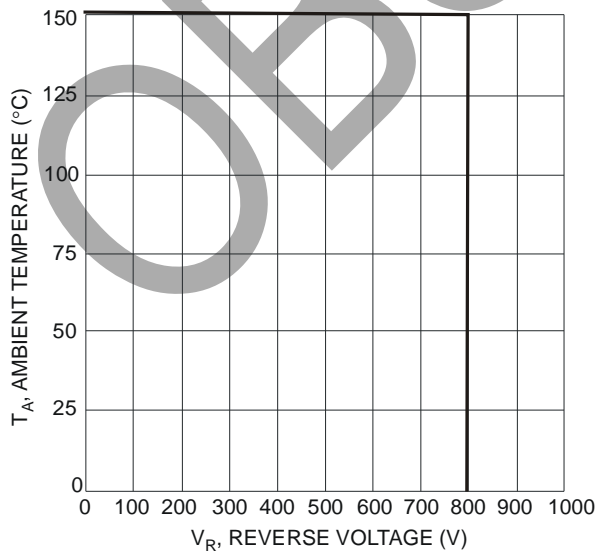


Figure 5 Reverse Voltage Derating

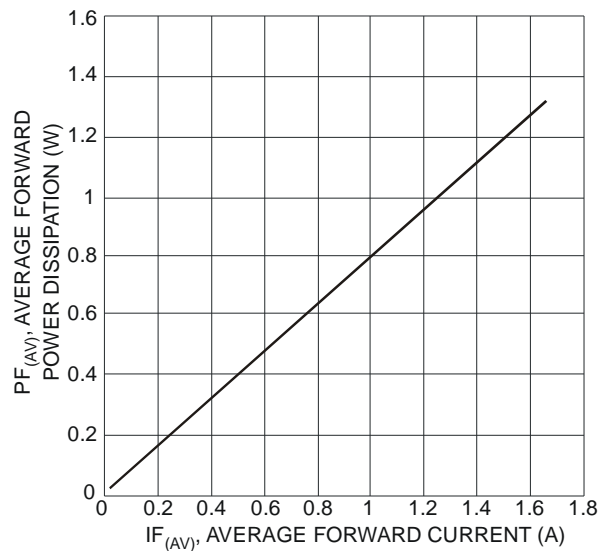


Figure 6 Forward Power Dissipation

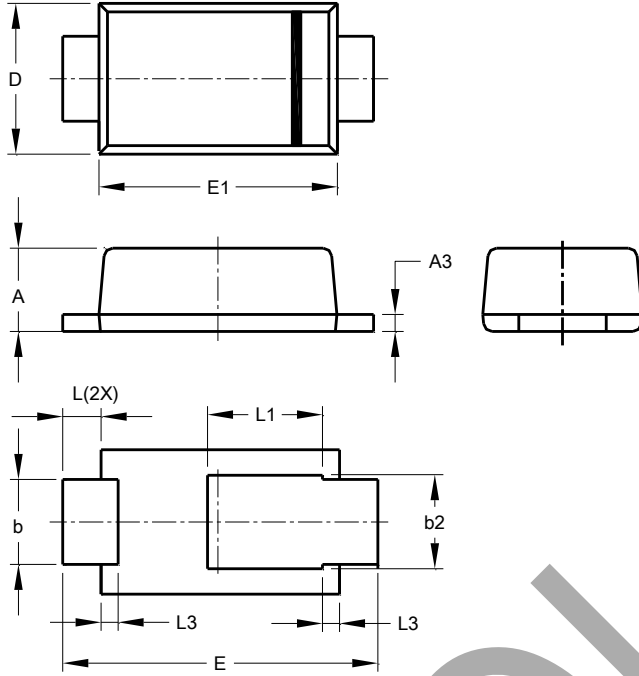
Note: 8. Measured at 1.0MHz and applied reverse voltage of 4.0V DC.

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Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

PowerDI123

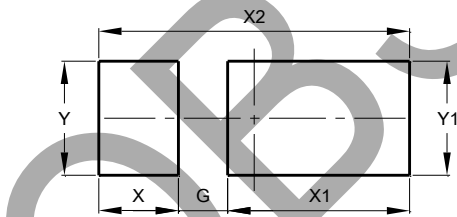


PowerDI123			
Dim	Min	Max	Typ
A	0.93	1.00	0.98
A3	0.15	0.25	0.20
b	0.85	1.25	1.00
b2	1.025	1.125	1.10
D	1.63	1.93	1.78
E	3.50	3.90	3.70
E1	2.60	3.00	2.80
L	0.40	0.50	0.45
L1	1.25	1.40	1.35
L3	0.125	0.275	0.20
All Dimensions in mm			

Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

PowerDI123



Dimensions	Value (in mm)
G	0.65
X	1.05
X1	2.40
X2	4.10
Y	1.50
Y1	1.50

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