



#### COMPLEMENTARY PAIR ENHANCEMENT MODE MOSFET

### **Product Summary**

Device	BV <sub>DSS</sub>	R <sub>DS(ON)</sub> Max	I <sub>D</sub> Max T <sub>A</sub> = +25°C
04	20	0.4Ω @ V <sub>GS</sub> = 10V	0.8A
Q1	30	0.7Ω @ V <sub>GS</sub> = 4.5V	0.62A
00	00	0.9Ω @ V <sub>GS</sub> = -10V	-0.55A
Q2	-30	1.7Ω @ V <sub>GS</sub> = -4.5V	-0.4A

### **Description and Applications**

This MOSFET is designed to minimize the on-state resistance (R<sub>DS(ON)</sub>) yet maintain superior switching performance, making it ideal for high-efficiency power-management applications.

- Motor control
- Power-management functions
- DC-DC converters

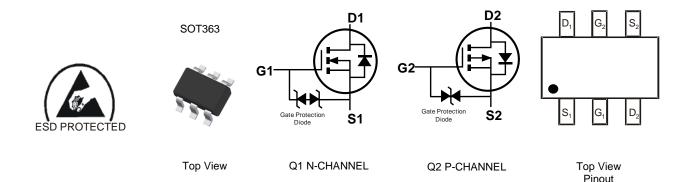
### **Features and Benefits**

- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- ESD Protected Gate
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please contact us or your local Diodes representative.

https://www.diodes.com/quality/product-definitions/

#### **Mechanical Data**

- Package: SOT363
- Package Material: Molded Plastic, "Green" Molding Compound.
   UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections Indicator: See Diagram
- Terminals: Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 @3
- Weight: 0.027 grams (Approximate)



### Ordering Information (Note 4)

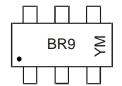
Part Number	Paakaga	Packing		
Part Number	Package	Qty.	Carrier	
DMC3401LDW-7	SOT363	3000	Tape & Reel	
DMC3401LDW-13	SOT363	10000	Tape & Reel	

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.



### **Marking Information**



BR9 = Product Type Marking Code YM = Date Code Marking Y or  $\overline{Y}$  or  $\underline{Y}$ = Year (ex: L = 2024) M = Month (ex: 9 = September)

#### Date Code Key

Year	2018		2024	2025	2026	2027	2028	2029	2030	2031	2032	2033
Code	F		L	М	N	Р	R	S	Т	U	V	W
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec

### **Maximum Ratings** (@TA = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value_Q1	Value_Q2	Unit
Drain-Source Voltage	Drain-Source Voltage			30	-30	V
Gate-Source Voltage			Vgss	±20	±20	V
Continuous Drain Current (Note 6) Q1: V <sub>GS</sub> = 10V Q2: V <sub>GS</sub> = -10V	Steady State	T <sub>A</sub> = +25°C T <sub>A</sub> = +70°C		0.8 0.6	-0.55 -0.44	А
Maximum Continuous Body Diode Forward Current (Note 6)			Is	0.4	-0.38	Α
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1	%)		I <sub>DM</sub>	4	-2.4	Α

# Thermal Characteristics ( $@T_A = +25^{\circ}C$ , unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)		PD	0.29	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	$R_{\theta JA}$	433	°C/W
Total Power Dissipation (Note 6)		PD	0.4	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	Reja	301	°C/W
Operating and Storage Temperature Range	·	T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

 Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
 Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate. Notes:



# Electrical Characteristics – N Channel – Q1 (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	30	_		٧	$V_{GS} = 0V, I_{D} = 250\mu A$
Zero Gate Voltage Drain Current	IDSS	_	_	1.0	μΑ	$V_{DS} = 30V$ , $V_{GS} = 0V$
Gate-Source Leakage	Igss	_		±10	μΑ	$V_{GS} = \pm 20V$ , $V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	Vgs(TH)	0.8	1.2	1.6	V	$V_{DS} = V_{GS}$ , $I_D = 250\mu A$
Static Drain-Source On-Resistance	Process	_	0.2	0.4	Ω	$V_{GS} = 10V, I_{D} = 0.59A$
Static Drain-Source Off-Resistance	Rds(on)	_	0.3	0.7	12	$V_{GS} = 4.5V, I_{D} = 0.2A$
Diode Forward Voltage	VsD	_	0.7	1.2	V	$V_{GS} = 0V, I_{S} = 0.1A$
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C <sub>iss</sub>	_	50		pF	15)()(
Output Capacitance	Coss	_	12		pF	V <sub>DS</sub> = 15V, V <sub>GS</sub> = 0V, f = 1.0MHz
Reverse Transfer Capacitance	Crss	_	10		pF	1 = 1.0WH IZ
Gate Resistance	Rg	_	58		Ω	$V_{DS} = V_{GS} = 0V, f = 1.0MHz$
Total Gate Charge (Vgs = 4.5V)	Qg	_	0.5		nC	
Total Gate Charge (V <sub>GS</sub> = 10V)	Qg	_	1.2		nC	\/ 10\/ I= 250mA
Gate-Source Charge	Qgs	_	0.2		nC	$V_{DS} = 10V, I_{D} = 250 \text{mA}$
Gate-Drain Charge	$Q_{gd}$	_	0.1		nC	
Turn-On Delay Time	tD(ON)	_	3.5		ns	
Turn-On Rise Time	t <sub>R</sub>	_	3.3		ns	$V_{GS} = 10V, V_{DS} = 30V,$
Turn-Off Delay Time	t <sub>D(OFF)</sub>	_	16.8		ns	$I_D = 100 \text{mA}, R_G = 25 \Omega$
Turn-Off Fall Time	tF	_	13.8	_	ns	

# Electrical Characteristics – P Channel – Q2 (@T<sub>A</sub> = +25°C, unless otherwise specified.)

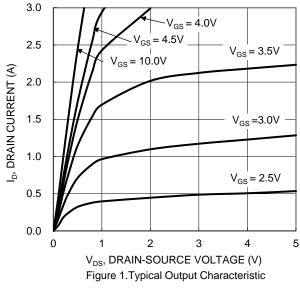
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BVDSS	-30	_	_	V	$V_{GS} = 0V, I_{D} = -250\mu A$	
Zero Gate Voltage Drain Current	IDSS		_	-1	μΑ	V <sub>DS</sub> = -24V, V <sub>GS</sub> = 0V	
Gate-Source Leakage	I <sub>GSS</sub>	_	_	±10	μΑ	$V_{GS} = \pm 16V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	V <sub>GS(TH)</sub>	-1	-2.2	-2.6	V	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	
Static Drain-Source On-Resistance	Descour	_	0.5	0.9	Ω	$V_{GS} = -10V, I_{D} = -0.42A$	
Static Dialii-Source Off-Resistance	RDS(ON)	_	0.78	1.7	12	$V_{GS} = -4.5V$ , $I_{D} = -0.2A$	
Diode Forward Voltage	$V_{SD}$	_	-0.8	-1.2	V	$V_{GS} = 0V$ , $I_{S} = -0.23A$	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	Ciss	_	19	_	pF	45)/ 1/ 0)/	
Output Capacitance	Coss	_	16	_	pF	V <sub>DS</sub> = -15V, V <sub>GS</sub> = 0V, -f = 1.0MHz	
Reverse Transfer Capacitance	Crss	_	3	_	pF	1 = 1.0101112	
Gate Resistance	$R_g$		729	_	Ω	$V_{DS} = V_{GS} = 0V$ , $f = 1.0MHz$	
Total Gate Charge (VGS = -4.5V)	Qg		0.36	_	nC		
Total Gate Charge (V <sub>GS</sub> = -10V)	Qg		0.8	_	nC	V <sub>DS</sub> = -10V, I <sub>D</sub> = -0.24A	
Gate-Source Charge	Qgs	_	0.1	_	nC	VDS = -10V, $ID = -0.24A$	
Gate-Drain Charge	Q <sub>gd</sub>	_	0.1	_	nC		
Turn-On Delay Time	tD(ON)	_	30	_	ns		
Turn-On Rise Time	t <sub>R</sub>	_	74	_	ns	$V_{GS} = -10V, V_{DD} = -15V,$	
Turn-Off Delay Time	t <sub>D(OFF)</sub>	_	28	_	ns	$I_D = -0.5A, R_G = 1\Omega$	
Turn-Off Fall Time	tF	_	31	_	ns		

Notes: 7. Short duration pulse test used to minimize self-heating effect.

8. Guaranteed by design. Not subject to product testing.



### **Typical Characteristics - N-CHANNEL**



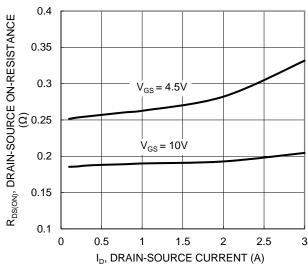


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

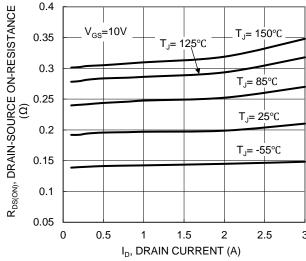


Figure 5. Typical On-Resistance vs. Drain Current and Temperature

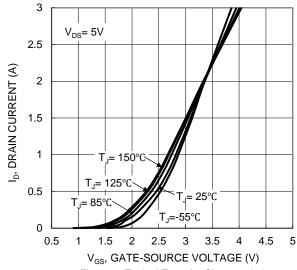


Figure 2. Typical Transfer Characteristic

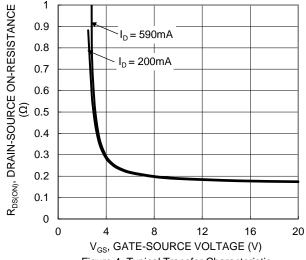


Figure 4. Typical Transfer Characteristic

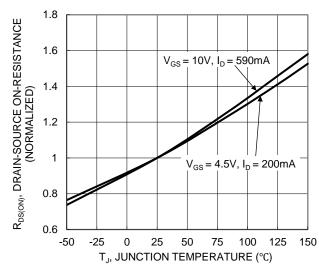


Figure 6. On-Resistance Variation with Temperature



### Typical Characteristics - N-CHANNEL (continued)

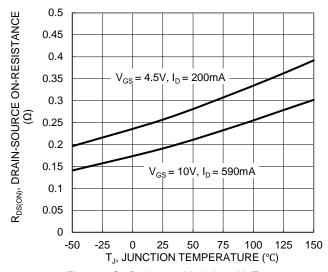
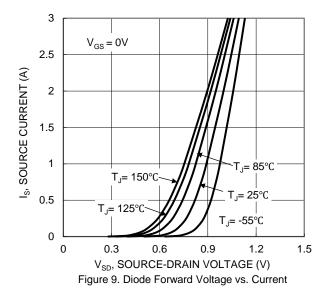
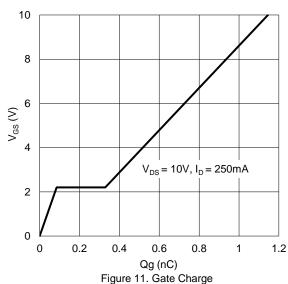


Figure 7. On-Resistance Variation with Temperature





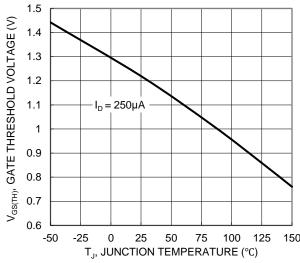
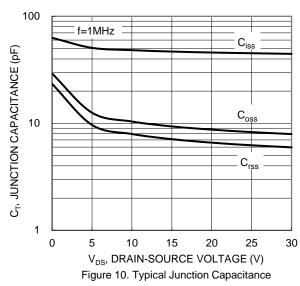


Figure 8. Gate Threshold Variation vs. JunctionTemperature

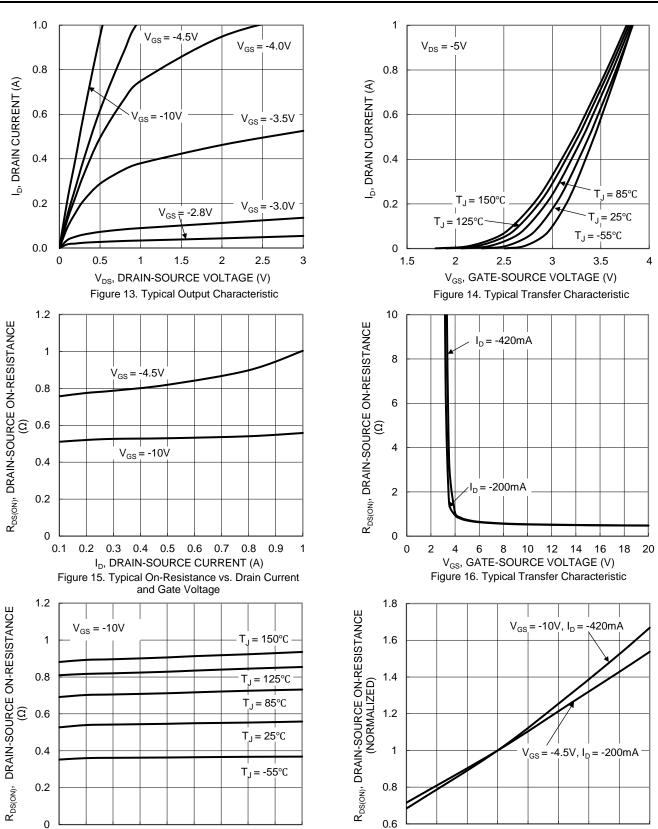


 $(V_{DS}, DRAIN-SOURCE VOLTAGE (V) Figure 12. SOA, Safe Operation Area$ 

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### **Typical Characteristics - P-CHANNEL**



0.1

0.2 0.3 0.4 0.5 0.6 0.7 0.8

I<sub>D</sub>, DRAIN CURRENT (A)

Figure 17. Typical On-Resistance vs. Drain Current

and Junction Temperature

-50

-25

0

25

50

T<sub>J</sub>, JUNCTION TEMPERATURE (°C)

Figure 18. On-Resistance Variation with Junction

Temperature

0.9

75

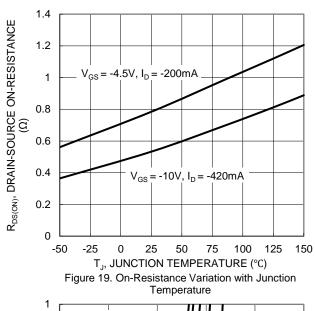
100

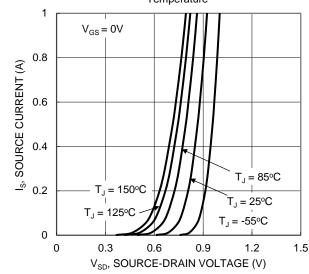
125

150



### Typical Characteristics - P-CHANNEL (continued)





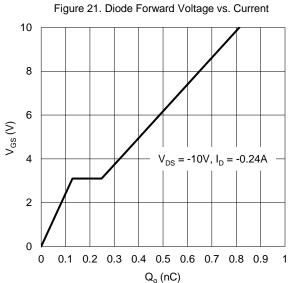


Figure 23. Gate Charge

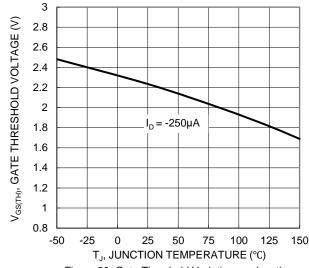
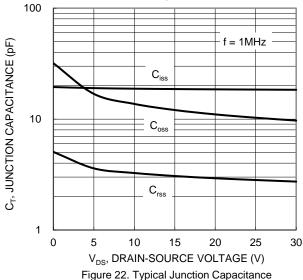


Figure 20. Gate Threshold Variation vs. Junction Temperature



10 R<sub>DS(ON)</sub> Limited DRAIN CURRENT (A) 0.1 T<sub>J(Max)</sub> = 150 ℃ T<sub>C</sub> = 25°C 0.01 Single Pulse # DC DUT on 1\*MRP **Board**  $V_{GS} = -10V$ 0.001 0.1 V<sub>DS</sub>, DRAIN-SOURCE VOLTAGE (V) Figure 24. SOA, Safe Operation Area



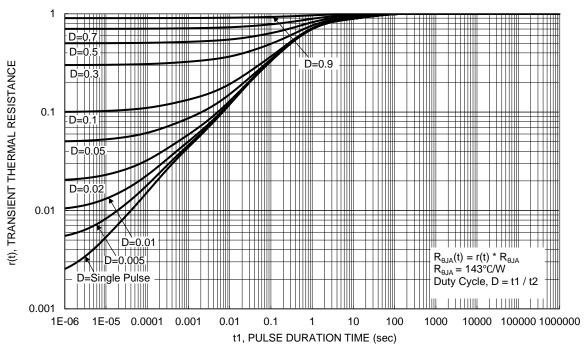


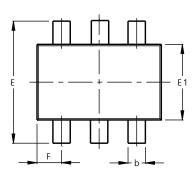
Figure 25. Transient Thermal Resistance

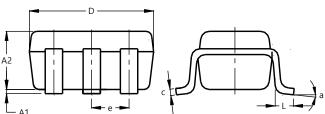


## **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

#### **SOT363**



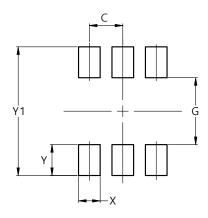


SOT363							
Dim	Min	Max	Тур				
A1	0.00	0.10	0.05				
A2	0.90	1.00	0.95				
b	0.10	0.30	0.25				
C	0.10	0.22	0.11				
D	1.80	2.20	2.15				
Е	2.00	2.20	2.10				
E1	1.15	1.35	1.30				
е	C	.650 E	SC				
F	0.40	0.45	0.425				
١	0.25	0.40	0.30				
а	0°	8°					
All I	Dimen	sions	in mm				

## **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.

### SOT363



Dimensions	Value (in mm)
C	0.650
G	1.300
Х	0.420
Y	0.600
V4	2.500



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