

Product Summary

Device	BV _{DSS}	R _{DS(ON)} Max	I _D Max T _A = +25°C
Q1	60V	1.7Ω @ V _{GS} = 10V	571mA
		3Ω @ V _{GS} = 4.5V	430mA
Q2	-50V	6Ω @ V _{GS} = -10V	-304mA
		8Ω @ V _{GS} = -5V	-263mA

Features and Benefits

- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Ultra-Small Surface-Mount Package
- ESD Protected Gate
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **The DMC62D0SVQ is suitable for automotive applications requiring specific change control; this part is AEC-Q101 qualified, PPAP capable, and manufactured in IATF16949 certified facilities.**

<https://www.diodes.com/quality/product-definitions/>

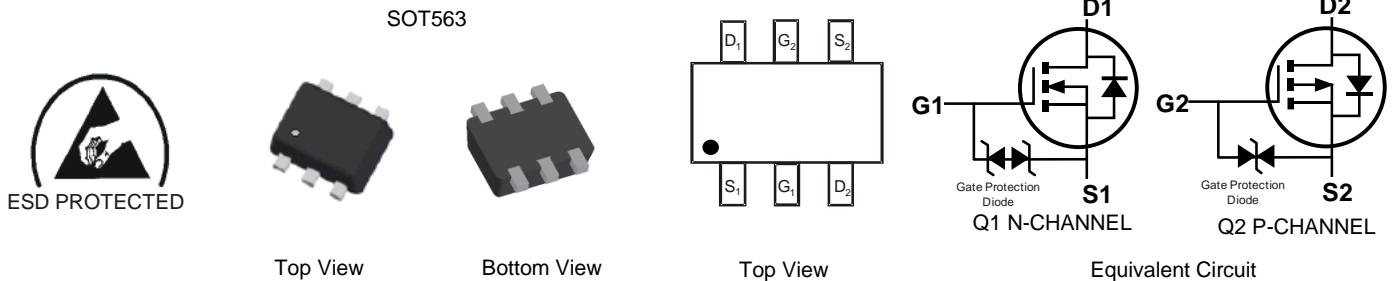
Description and Applications

This MOSFET is designed to meet the stringent requirements of automotive applications. It is qualified to AEC-Q101, supported by a PPAP and is ideal for use in:

- Power-management functions
- DC-DC converters
- Batteries

Mechanical Data

- Package: SOT563
- Package Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Terminals: Finish – Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 **e3**
- Weight: 0.003 grams (Approximate)

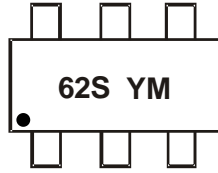


Ordering Information (Note 4)

Part Number	Package	Packing	
		Qty.	Carrier
DMC62D0SVQ-7	SOT563	3,000	Tape & Reel
DMC62D0SVQ-13	SOT563	10,000	Tape & Reel

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
 2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 4. For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

Marking Information



62S = Product Type Marking Code
 YM = Date Code Marking
 Y or \bar{Y} = Year (ex: L = 2024)
 M = Month (ex: 9 = September)

Date Code Key

Year	2017	-	2024	2025	2026	2027	2028	2029	2030	2031	2032	2033
Code	E	-	L	M	N	P	R	S	T	U	V	W

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Q1 Value	Q2 Value	Unit
Drain-Source Voltage			V _{DSS}	60	-50	V
Gate-Source Voltage			V _{GSS}	±20	±20	V
Continuous Drain Current (Note 5) N-Channel: V _{GS} = 10V P-Channel: V _{GS} = -10V	Steady State	T _A = +25°C	I _D	571	-304	mA
		T _A = +70°C		457	-243	
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)			I _{DM}	1,200	-800	mA
Maximum Body Diode Continuous Current (Note 5)			I _S	500	-300	mA

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 6)		P _D	0.51	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	R _{θJA}	250	°C/W
Total Power Dissipation (Note 5)		P _D	0.84	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	R _{θJA}	150	°C/W
Operating and Storage Temperature Range		T _J , T _{STG}	-55 to +150	°C

Notes: 5. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
 6. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.

Electrical Characteristics – Q1 N-CHANNEL (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV _{DSS}	60	—	—	V	V _{GS} = 0V, I _D = 250μA
Zero Gate Voltage Drain Current	I _{DSS}	—	—	1	μA	V _{DS} = 60V, V _{GS} = 0V
Gate-Source Leakage	I _{GSS}	—	—	±10	μA	V _{GS} = ±20V, V _{DS} = 0V
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V _{GS(TH)}	1.0	—	2.5	V	V _{DS} = V _{GS} , I _D = 250μA
Static Drain-Source On-Resistance	R _{DS(ON)}	—	1.3	1.7	Ω	V _{GS} = 10V, I _D = 500mA
		—	1.4	3		V _{GS} = 4.5V, I _D = 200mA
Diode Forward Voltage	V _{SD}	—	—	1.4	V	V _{GS} = 0V, I _S = 115mA
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C _{iss}	—	30	—	pF	V _{DS} = 25V, V _{GS} = 0V f = 1.0MHz
Output Capacitance	C _{oss}	—	4.2	—	pF	
Reverse Transfer Capacitance	C _{rss}	—	2.9	—	pF	
Total Gate Charge	Q _g	—	0.4	—	nC	V _{GS} = 4.5V, V _{DS} = 10V I _D = 250mA
Gate-Source Charge	Q _{gs}	—	0.15	—	nC	
Gate-Drain Charge	Q _{gd}	—	0.09	—	nC	
Turn-On Delay Time	t _{D(ON)}	—	4.3	—	ns	V _{DD} = 30V, V _{GS} = 10V R _g = 25Ω, I _D = 200mA
Turn-On Rise Time	t _R	—	2.7	—	ns	
Turn-Off Delay Time	t _{D(OFF)}	—	15.1	—	ns	
Turn-Off Fall Time	t _F	—	6.5	—	ns	

Electrical Characteristics – Q2 P-CHANNEL (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV _{DSS}	-50	—	—	V	V _{GS} = 0V, I _D = -250μA
Zero Gate Voltage Drain Current	I _{DSS}	—	—	-1	μA	V _{DS} = -50V, V _{GS} = 0V
Gate-Source Leakage	I _{GSS}	—	—	±10	μA	V _{GS} = ±16V, V _{DS} = 0V
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V _{GS(TH)}	-1	—	-2.5	V	V _{DS} = V _{GS} , I _D = -250μA
Static Drain-Source On-Resistance	R _{DS(ON)}	—	3.7	6	Ω	V _{GS} = -10V, I _D = -500mA
		—	3.7	8		V _{GS} = -5V, I _D = -200mA
Diode Forward Voltage	V _{SD}	—	—	-1.4	V	V _{GS} = 0V, I _S = -115mA
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C _{iss}	—	26	—	pF	V _{DS} = -25V, V _{GS} = 0V f = 1.0MHz
Output Capacitance	C _{oss}	—	4.2	—	pF	
Reverse Transfer Capacitance	C _{rss}	—	2.4	—	pF	
Total Gate Charge	Q _g	—	0.3	—	nC	V _{GS} = -4.5V, V _{DS} = -10V I _D = -500mA
Gate-Source Charge	Q _{gs}	—	0.14	—	nC	
Gate-Drain Charge	Q _{gd}	—	0.12	—	nC	
Turn-On Delay Time	t _{D(ON)}	—	4.1	—	ns	V _{DD} = -30V, V _{GS} = -10V R _g = 50Ω, I _D = -270mA
Turn-On Rise Time	t _R	—	2.8	—	ns	
Turn-Off Delay Time	t _{D(OFF)}	—	20.2	—	ns	
Turn-Off Fall Time	t _F	—	9.15	—	ns	

Notes: 7. Short duration pulse test used to minimize self-heating effect.
8. Guaranteed by design. Not subject to product testing.

Typical Characteristics – N-CHANNEL

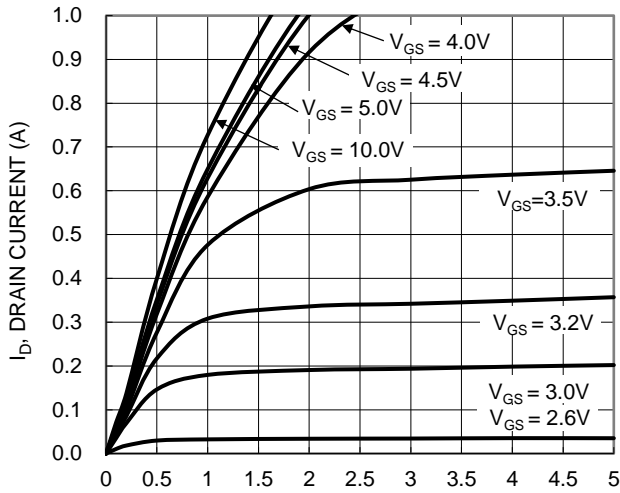


Figure 1. Typical Output Characteristic

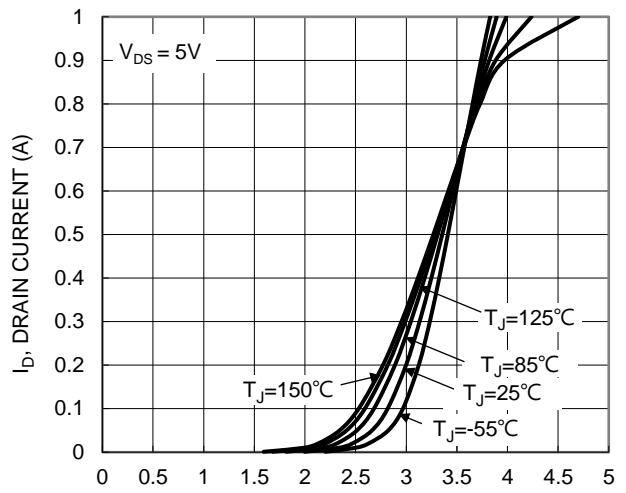


Figure 2. Typical Transfer Characteristic

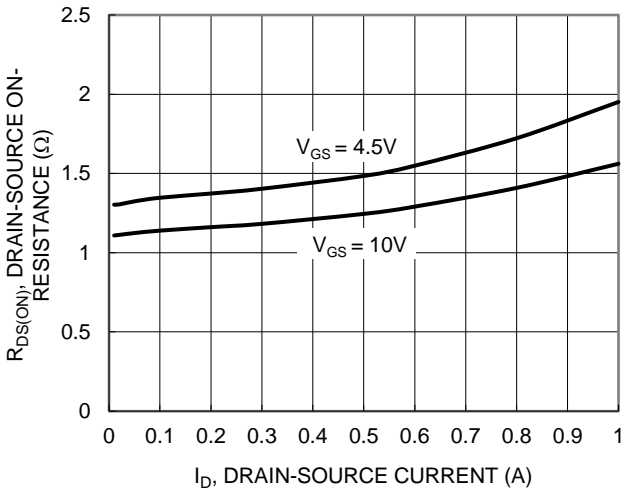


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

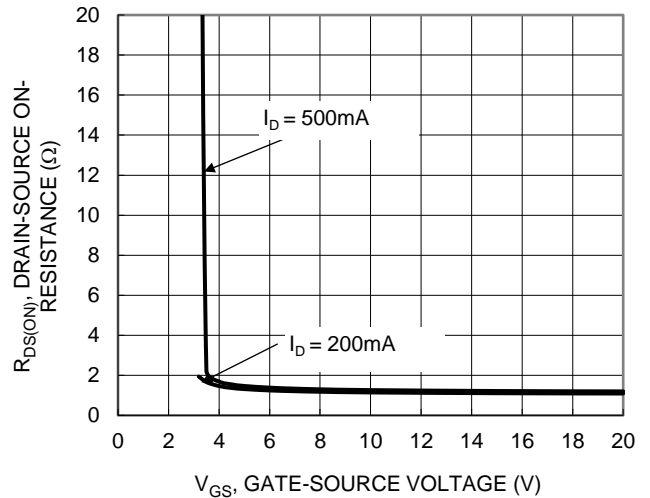


Figure 4. Typical Transfer Characteristic

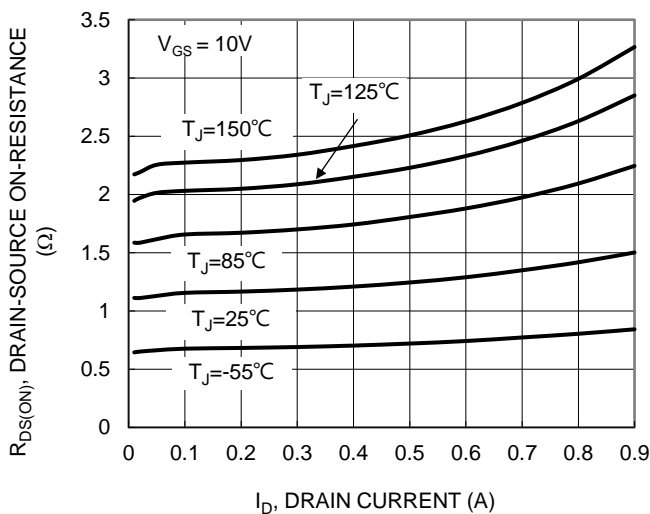


Figure 5. Typical On-Resistance vs. Drain Current and Temperature

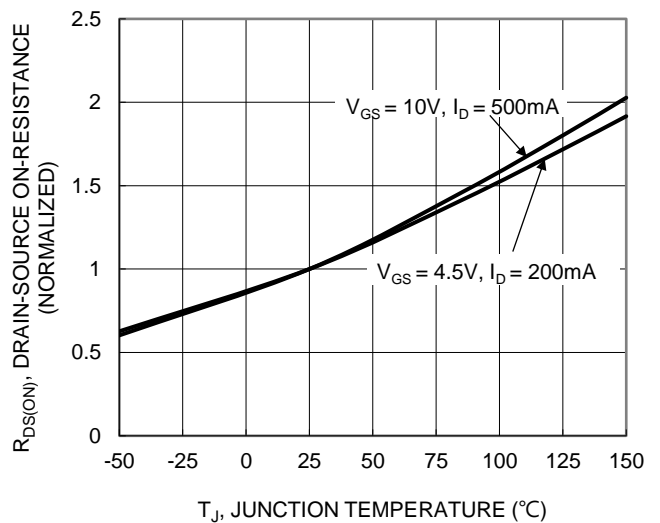


Figure 6. On-Resistance Variation with Temperature

Typical Characteristics – N-CHANNEL (continued)

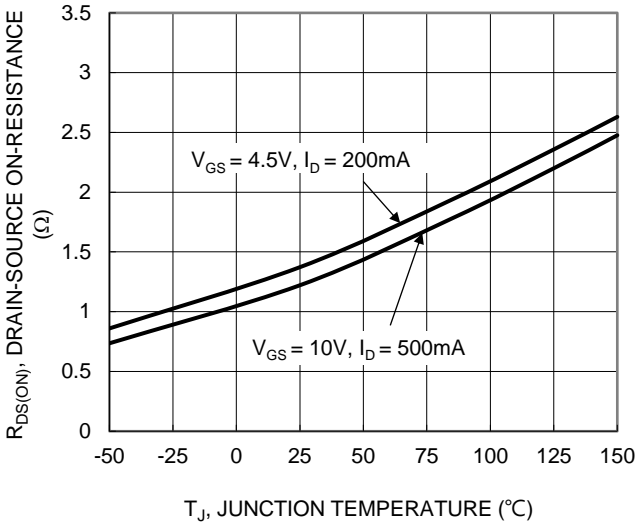


Figure 7. On-Resistance Variation with Temperature

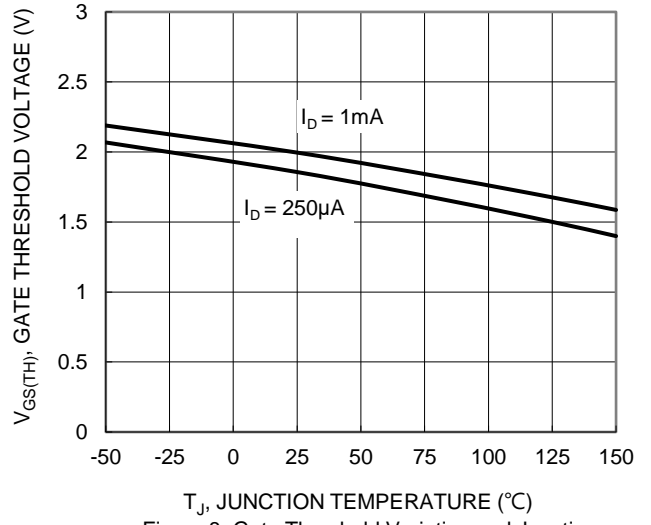


Figure 8. Gate Threshold Variation and Junction Temperature

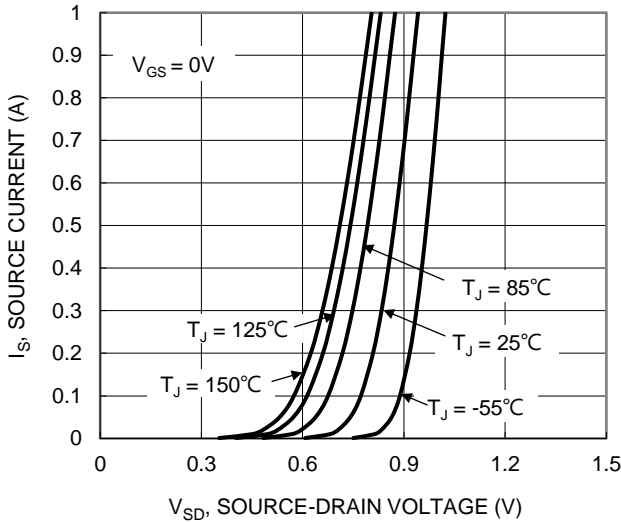


Figure 9. Diode Forward Voltage vs. Current

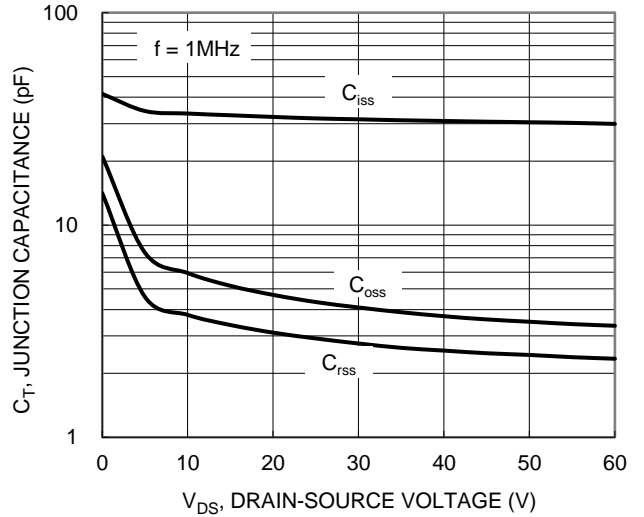


Figure 10. Typical Junction Capacitance

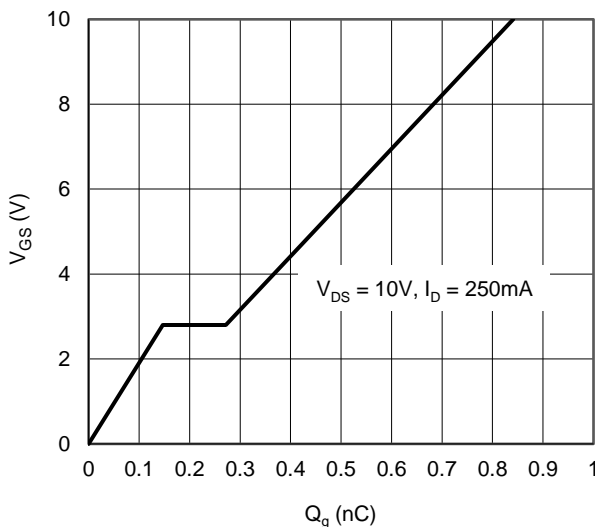


Figure 11. Gate Charge

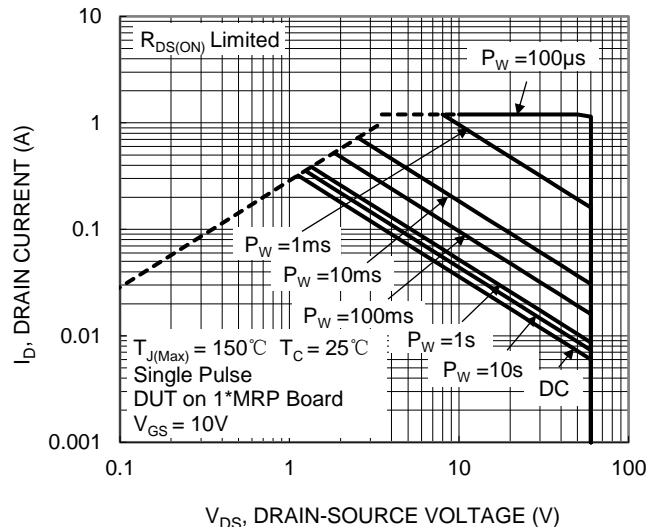


Figure 12. SOA, Safe Operation Area

Typical Characteristics – P-CHANNEL

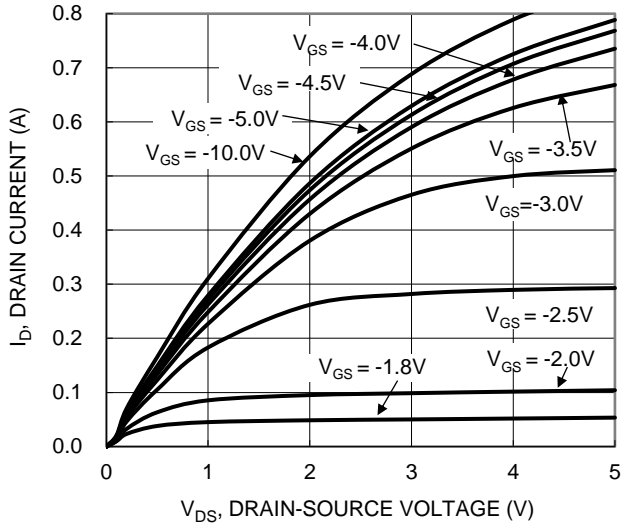


Figure 13. Typical Output Characteristic

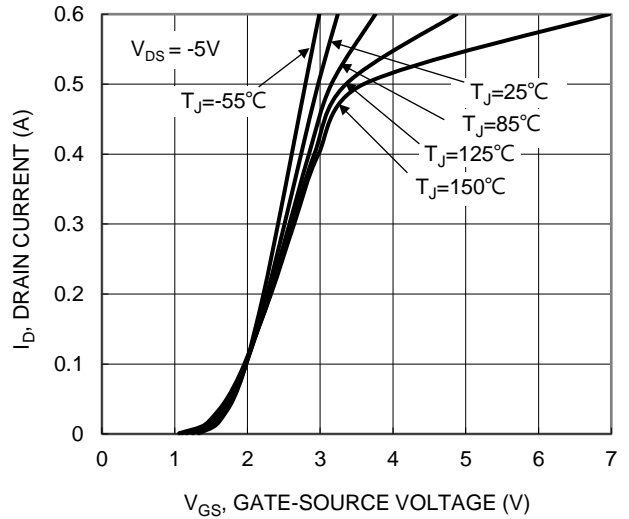


Figure 14. Typical Transfer Characteristic

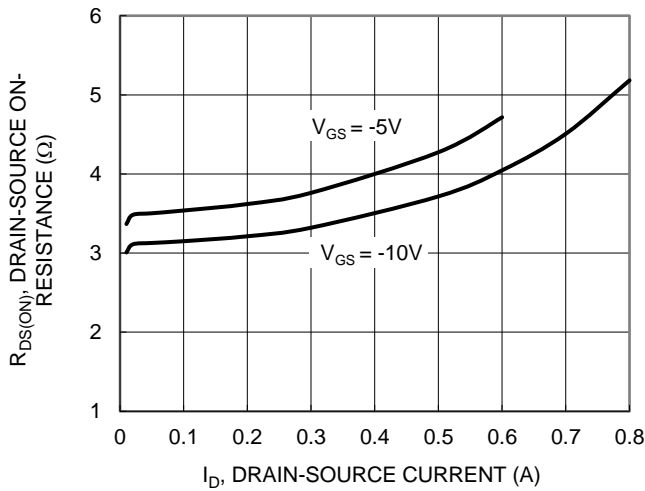


Figure 15. Typical On-Resistance vs. Drain Current and Gate Voltage

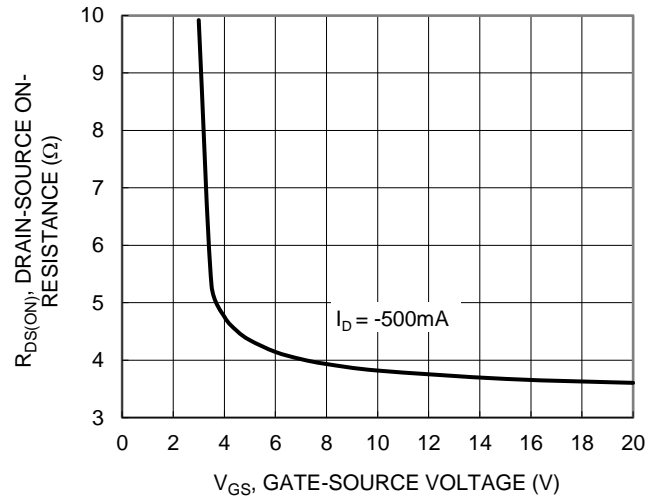


Figure 16. Typical Transfer Characteristic

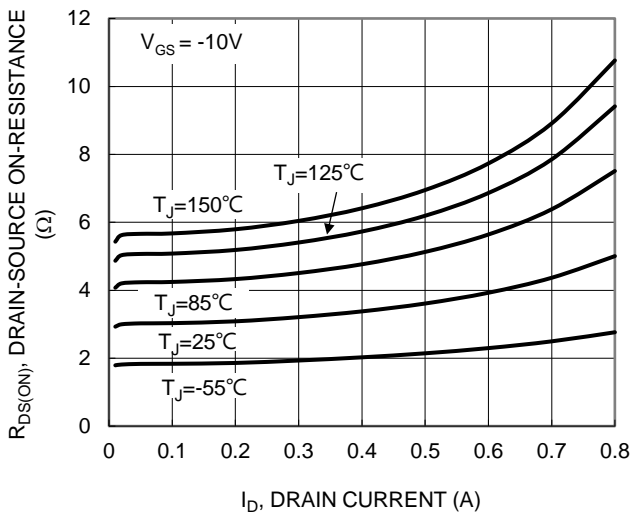


Figure 17. Typical On-Resistance vs. Drain Current and Temperature

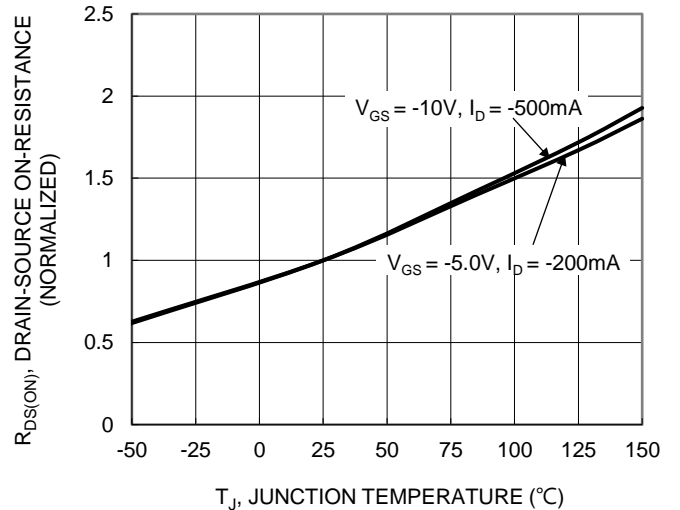


Figure 18. On-Resistance Variation with Temperature

Typical Characteristics – P-CHANNEL (continued)

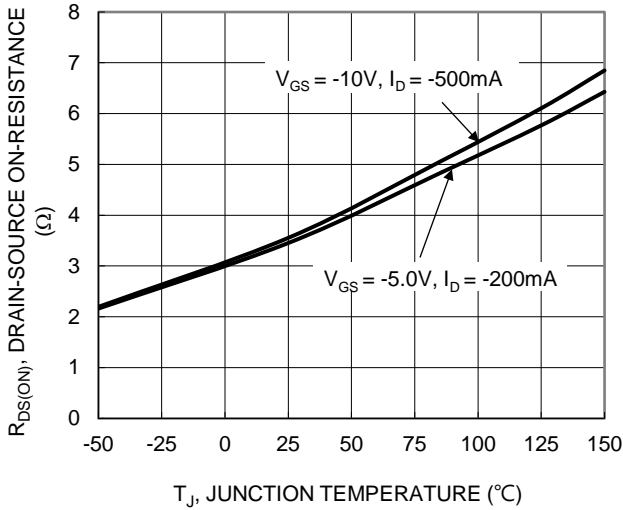


Figure 19. On-Resistance Variation with Temperature

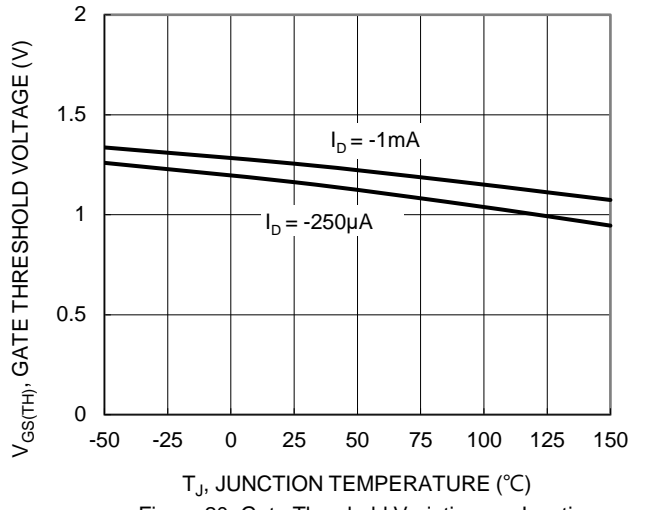


Figure 20. Gate Threshold Variation vs. Junction Temperature

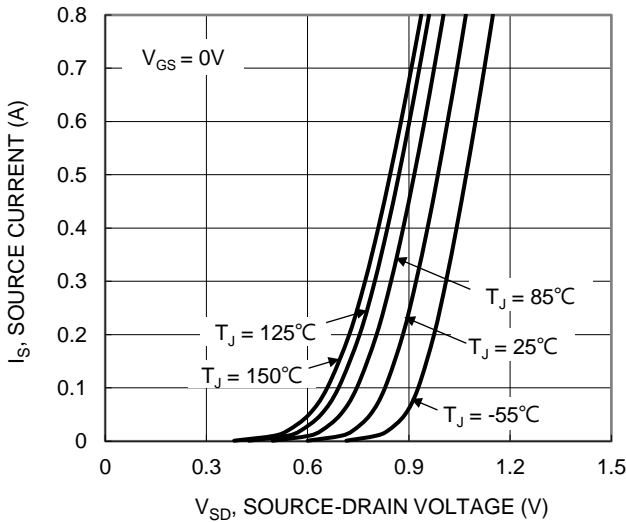


Figure 21. Diode Forward Voltage vs. Current

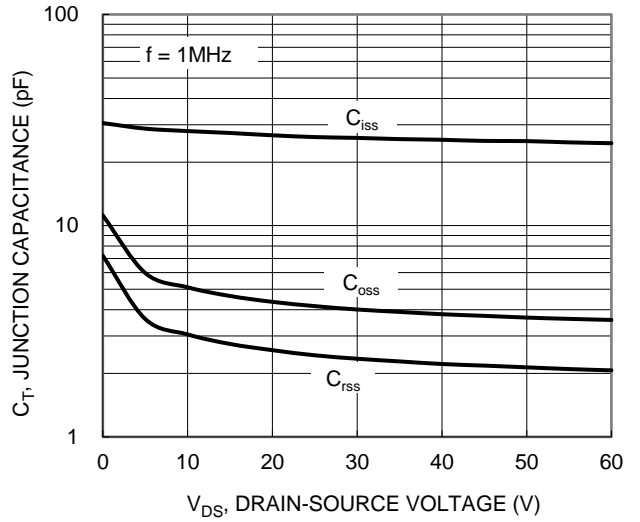


Figure 22. Typical Junction Capacitance

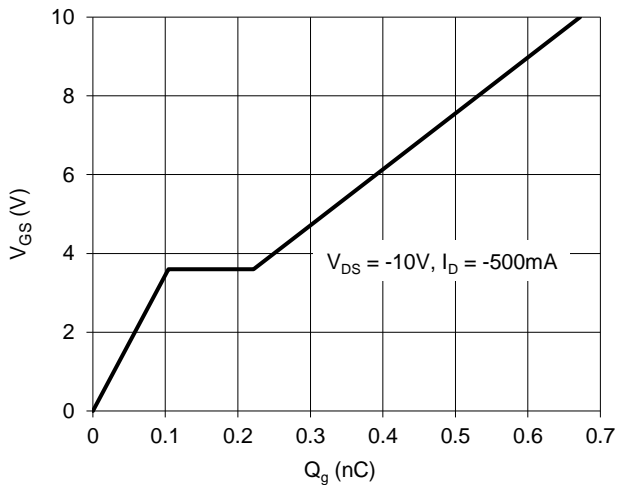


Figure 23. Gate Charge

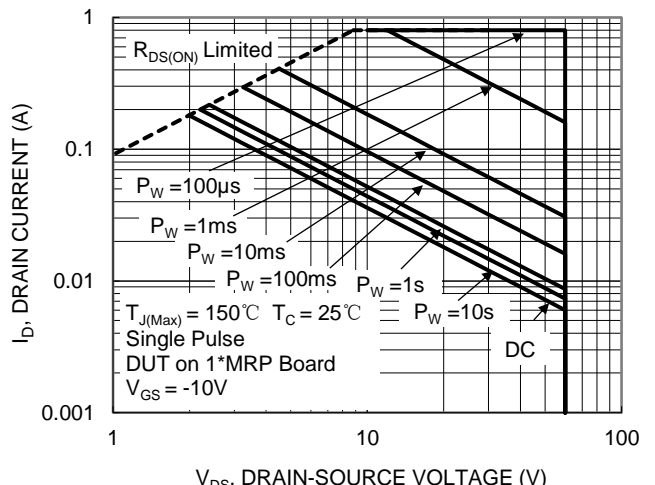


Figure 24. SOA, Safe Operation Area

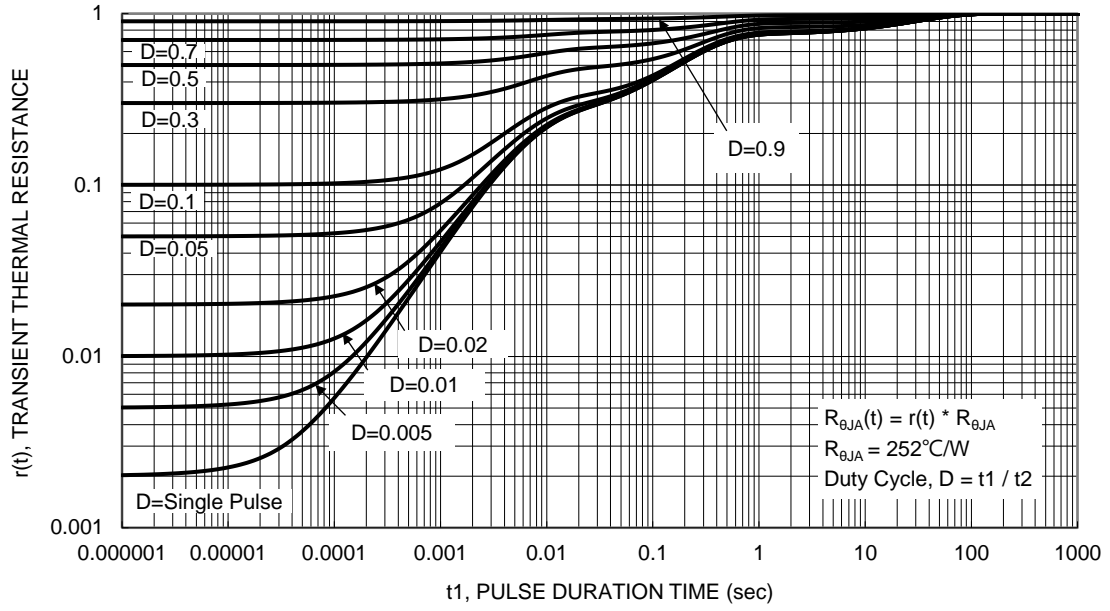
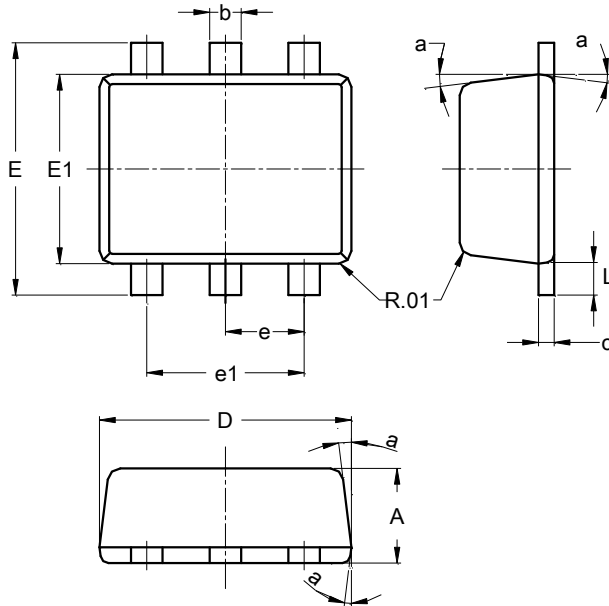


Figure 25. Transient Thermal Resistance

Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

SOT563

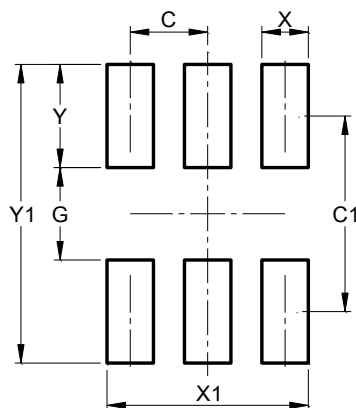


SOT563			
Dim	Min	Max	Typ
A	0.55	0.60	--
b	0.15	0.30	0.20
c	0.10	0.18	0.11
D	1.50	1.70	1.60
E	1.55	1.70	1.60
E1	1.10	1.25	1.20
e	--	--	0.50
e1	0.90	1.10	1.00
L	0.10	0.30	0.20
a	8°	9°	7°
All Dimensions in mm			

Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

SOT563



Dimensions	Value (in mm)
C	0.500
C1	1.270
G	0.600
X	0.300
X1	1.300
Y	0.670
Y1	1.940

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