



N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{DSS}	R _{DS(ON)} Max	I _D Max T _A = +25°C
	$25m\Omega$ @ V _{GS} = 4.5V	4.2A
20V	$29m\Omega$ @ V _{GS} = 2.5V	4.0A
	37mΩ @ V _{GS} = 1.8V	3.4A

Description

This MOSFET is designed to minimize the on-state resistance (R_{DS(ON)}) yet maintain superior switching performance, making it ideal for high-efficiency power-management applications.

Applications

- · Power-management functions
- DC-DC converters

Features

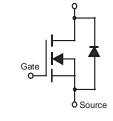
- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- The DMG3414UQ is suitable for automotive applications requiring specific change control; this part is AEC-Q101 qualified, PPAP capable, and manufactured in IATF 16949 certified facilities.

https://www.diodes.com/quality/product-definitions/

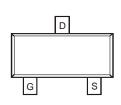
Mechanical Data

- Package: SOT23
- Package Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Annealed over Copper Leadframe.
 Solderable per MIL-STD-202, Method 208 (3)
- Terminals Connections: See Diagram Below
- Weight: 0.008 grams (Approximate)





Drain



Top View

Internal Schematic

Top View

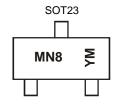
Ordering Information (Note 4)

Orderable Part Number	Pookogo	Paci	king
Orderable Part Number	Package	Qty.	Carrier
DMG3414UQ-7	SOT23	3,000	Tape & Reel
DMG3414UQ-13	SOT23	10,000	Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/

Marking Information



 $\begin{array}{l} \text{MN8} = \text{Product Type Marking Code} \\ \text{YM or } \overline{\text{Y}}\text{M} = \text{Date Code Marking} \\ \text{Y or } \overline{\text{Y}} = \text{Year (ex: L} = 2024) \\ \text{M} = \text{Month (ex: 9} = \text{September)} \end{array}$

Date Code Key

Year	2015	•	2024	2025	2026	2027	2028	2029	2030	2031	2032	2033
Code	С	-	L	М	N	Р	R	S	T	U	V	W
	•		1	•	•						1	
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec



Maximum Ratings (@ $T_A = +25$ °C, unless otherwise specified.)

Characte	eristic		Symbol	Value	Unit
Drain-Source Voltage			VDSS	20	V
Gate-Source Voltage		Vgss	±8	V	
Continuous Drain Current (Note 5) Steady $T_A = +25$ °C $T_A = +70$ °C			lo	4.2 3.2	Α
Pulsed Drain Current (Note 6)			I _{DM}	30	А

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 5)	P _D	0.78	W
Thermal Resistance, Junction to Ambient @T _A = +25°C	R _{0JA}	162	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

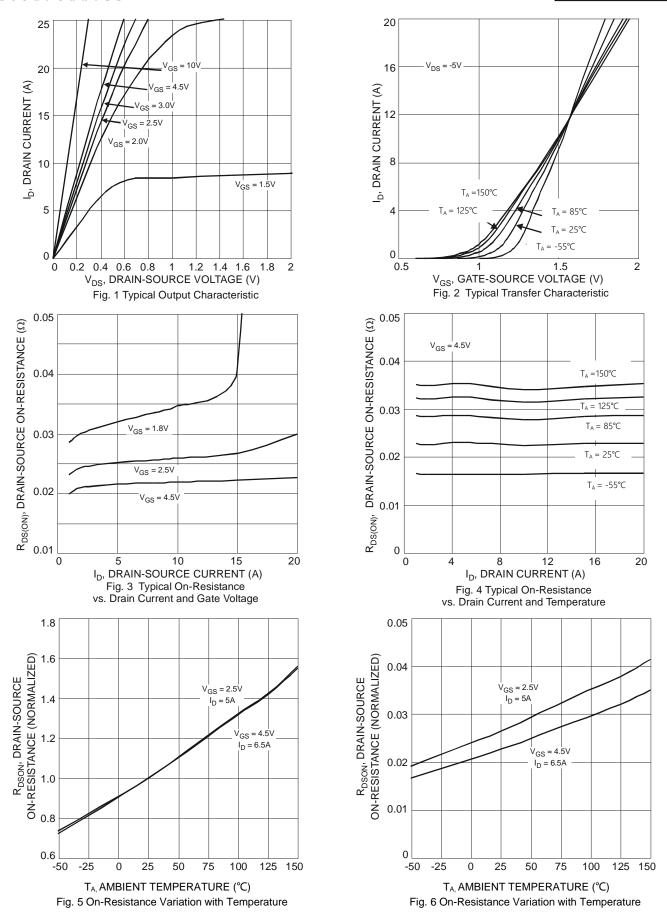
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV _{DSS}	20	_	_	V	V _G S = 0V, I _D = 250µA
Zero Gate Voltage Drain Current $T_J = +25^{\circ}C$	I _{DSS}	_	_	1.0	μA	V _{DS} = 20V, V _{GS} = 0V
Gate-Source Leakage	Igss	_	_	±100	nA	$V_{GS} = \pm 8V$, $V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	VGS(TH)	0.5	_	0.9	V	$V_{DS} = V_{GS}$, $I_D = 250\mu A$
			19	25		$V_{GS} = 4.5V, I_{D} = 8.2A$
Static Drain-Source On-Resistance	RDS(ON)	_	22	29	mΩ	$V_{GS} = 2.5V, I_{D} = 3.3A$
			28	37		V _G S = 1.8V, I _D = 2.0A
Forward Transfer Admittance	Y _{fs}		7	_	S	V _{DS} = 10V, I _D = 4A
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	Ciss	_	829.9	_	pF	
Output Capacitance		_	85.3	_	pF	V _{DS} = 10V, V _{GS} = 0V - f = 1.0MHz
Reverse Transfer Capacitance	C _{rss}		81.2	_	pF	1 - 1.600112
Total Gate Charge	Qg		9.6	_	nC	
Gate-Source Charge	Qgs		1.5	_	nC	V _G S = 4.5V, V _D S = 10V, I _D = 8.2A
Gate-Drain Charge	Q_{gd}		3.5	_	nC	
Turn-On Delay Time	tD(ON)		8.1	_	ns	
Turn-On Rise Time	t _R		8.3	_	ns	$V_{DD} = 10V, V_{GS} = 4.5V,$
Turn-Off Delay Time	tD(OFF)		40.1	_	ns	$R_L = 10\Omega$, $R_G = 6\Omega$, $I_D = 1A$
Turn-Off Fall Time	tF	_	9.6	_	ns	

Notes:

- 5. Device mounted on FR-4 PCB with 2oz. copper and test pulse width t ≤ 10s.
- 6. Repetitive rating, pulse width limited by junction temperature.
- 7. Short duration pulse test used to minimize self-heating effect.
- 8. Guaranteed by design. Not subject to production testing.







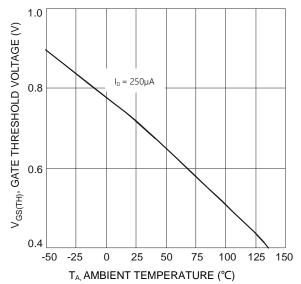
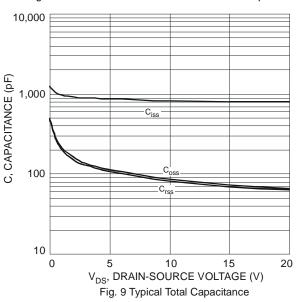
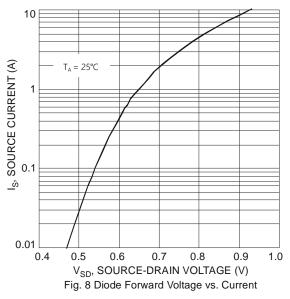


Fig. 7 Gate Threshold Variation vs. Ambient Temperature





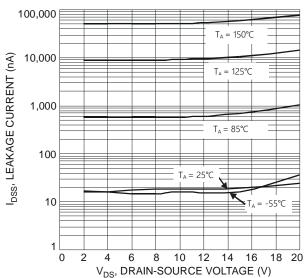


Fig. 10 Typical Leakage Current vs. Drain-Source Voltage

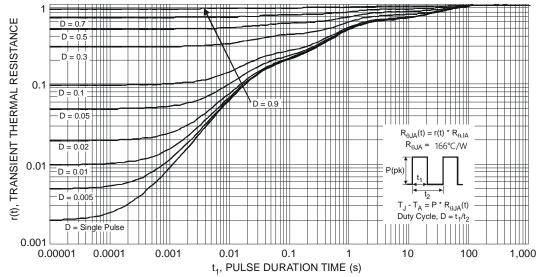


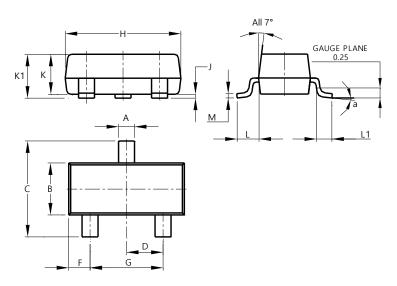
Fig. 11 Transient Thermal Response



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT23

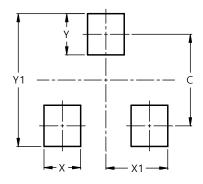


SOT23						
Dim	Min	Max	Тур			
Α	0.37	0.51	0.40			
В	1.20	1.40	1.30			
C	2.30	2.50	2.40			
D	0.89	1.03	0.915			
F	0.45	0.60	0.535			
G	1.78	2.05	1.83			
Н	2.80	3.00	2.90			
7	0.013	0.10	0.05			
K	0.890	1.00	0.975			
K1	0.903	1.10	1.025			
L	0.45	0.61	0.55			
L1	0.25	0.55	0.40			
М	0.085	0.150	0.110			
а	0°	8°				
All	All Dimensions in mm					

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT23



Dimensions	Value (in mm)				
С	2.0				
Х	0.8				
X1	1.35				
Y	0.9				
V1	2.0				



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