





#### N-CHANNEL ENHANCEMENT MODE MOSFET

## **Product Summary**

BV <sub>DSS</sub> (@ T <sub>J</sub> Max)	Rds(on)	I <sub>D</sub> Tc = +25°C
650V	$2.5\Omega@V_{GS} = 10V$	4.5A

### **Description**

This new generation MOSFET features low on-resistance and fast switching, making it ideal for high efficiency power management applications.

### **Applications**

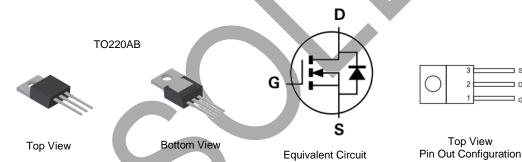
- Motor controls
- Backlighting
- DC-DC converters
- Power management functions

#### **Features**

- Low Input Capacitance
- High BV<sub>DSS</sub> Rating for Power Application
- Low Input/Output Leakage
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please contact us or your local Diodes representative. https://www.diodes.com/quality/product-definitions/

#### **Mechanical Data**

- Package: TO220AB
- Package Material: Molded Plastic, "Green" Molding Compound, UL Flammability Classification Rating 94V-0
- Terminals: Matte Tin Finish Annealed over Copper Leadframe.
   Solderable per MIL-STD-202, Method 208 <sup>®</sup>
- Terminal Connections: See Diagram Below
- Weight: TO220AB 1.85 grams (Approximate)



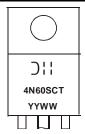
# Ordering Information (Note 4)

Part Number	Daskage	Packing		
Part Number	Package	Qty.	Carrier	
DMG4N60SCT	TO220AB	50 pieces	tube	

Notes:

- 1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

## **Marking Information**



☐ ☐ Manufacturer's Marking

4N60SCT = Product Type Marking Code

YYWW = Date Code Marking

YY or <u>YY</u> = Last Two Digits of Year (ex: 22 = 2022)

WW or <u>WW</u> = Week Code (01 to 53)



# **Maximum Ratings** (@ $T_A = +25$ °C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage		VDSS	600	V	
Gate-Source Voltage			V <sub>GSS</sub>	±30	V
Continuous Drain Current (Note 5) V <sub>GS</sub> = 10V	Steady State	T <sub>C</sub> = +25°C T <sub>C</sub> = +100°C	I <sub>D</sub>	4.5 3	А
Maximum Body Diode Forward Current (Note 5)		Is	6	Α	
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)			I <sub>DM</sub>	6	Α
Avalanche Current, L = 60mH (Note 6)			las	1.7	Α
Avalanche Energy, L = 60mH (Note 6)			Eas	90	mJ

# **Thermal Characteristics**

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)	$T_{C} = +25^{\circ}C$ $T_{C} = +100^{\circ}C$	PD	113 45	W
Thermal Resistance, Junction to Ambient (Note 5)		Rеја	58	°C/W
Thermal Resistance, Junction to Case (Note 5)		R <sub>θ</sub> JC	1.1	C/VV
Operating and Storage Temperature Range		T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

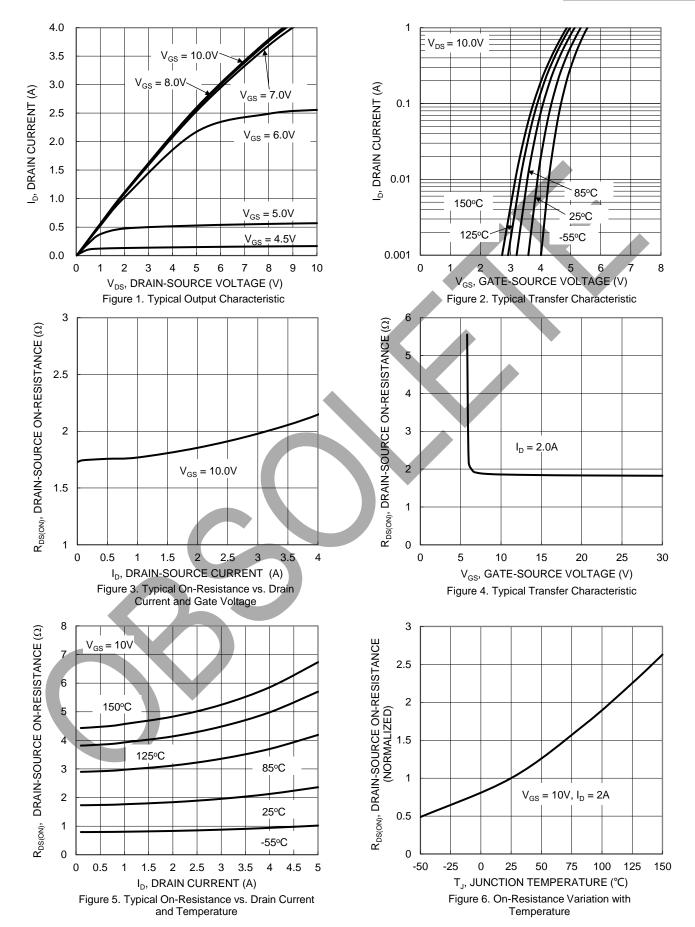
# Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BVDSS	600	_	_	V	$V_{GS} = 0V, I_{D} = 250\mu A$	
Zero Gate Voltage Drain Current	IDSS	1	_	1	μA	V <sub>DS</sub> = 600V, V <sub>GS</sub> = 0V	
Gate-Source Leakage	IGSS	1	7	100	nA	$V_{GS} = \pm 30V$ , $V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	V <sub>GS(TH)</sub>	2.5		4.5	V	$V_{DS} = V_{GS}$ , $I_D = 250\mu A$	
Static Drain-Source On-Resistance	RDS(ON)	_ '	2.0	2.5	Ω	Vgs = 10V, ID = 2A	
Diode Forward Voltage	V <sub>SD</sub>	_		1.4	V	$V_{GS} = 0V$ , $I_S = 1A$	
DYNAMIC CHARACTERISTICS (Note 6)						•	
Input Capacitance	Ciss		532	_		V <sub>DS</sub> = 25V, f = 1.0MHz, V <sub>GS</sub> = 0	
Output Capacitance	Coss		47	_	pF		
Reverse Transfer Capacitance	Crss	_	4	_			
Gate Resistance	Rg	_	3.3	_	Ω	$V_{DS} = 0V$ , $V_{GS} = 0V$ , $f = 1.0MHz$	
Total Gate Charge (V <sub>GS</sub> = 10V)	Qg	_	14.3	_		1001/ 1	
Gate-Source Charge	Qgs	_	3.3	_	nC	$V_{DD} = 480V, I_{D} = 4A,$ $V_{GS} = 10V$	
Gate-Drain Charge	Qgd	_	6.9	_			
Turn-On Delay Time	tD(ON)	_	14	_			
Turn-On Rise Time	t <sub>R</sub>	_	34	_		$V_{DD} = 300V$ , $R_G = 25\Omega$ , $I_D = 4A$ ,	
Turn-Off Delay Time	tD(OFF)	_	32	_	ns	V <sub>G</sub> S = 10V	
Turn-Off Fall Time	tF	_	25	_			
Body Diode Reverse Recovery Time	t <sub>RR</sub>	_	229	_	ns	$dI/dt = 100A/\mu s$ , $V_{DS} = 100V$ ,	
Body Diode Reverse Recovery Charge	QRR	_	1564	_	nC	IF = 4A	

Notes:

5. Device mounted on an infinite heatsink.
6. Guaranteed by design. Not subject to production testing.
7. Short duration pulse test used to minimize self-heating effect.







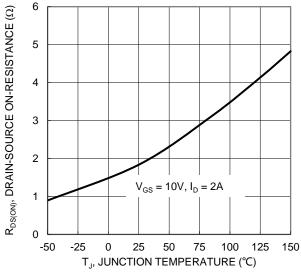


Figure 7. On-Resistance Variation with Temperature

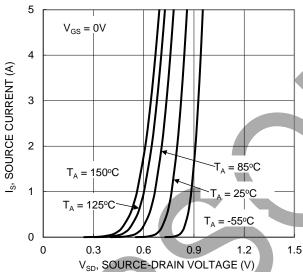
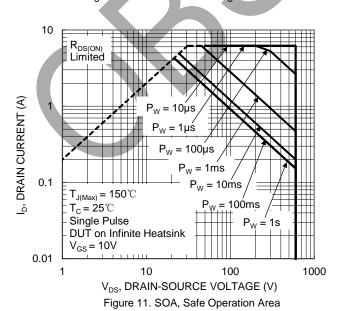


Figure 9. Diode Forward Voltage vs Current



4.4  $V_{GS(TH)},\; GATE\; THRESHOLD\; VOLTAGE\; (V)$ 4.2 4 3.8 3.6  $I_D = 1mA$ 3.4 3.2  $I_D = 250 \mu A$ 3 2.8 2.6 2.4 2.2 2 -50 -25 25 50 75 100 125 150 T<sub>J</sub>, JUNCTION TEMPERATURE (°C)

Figure 8. Gate Threshold Variation vs. Junction Temperature

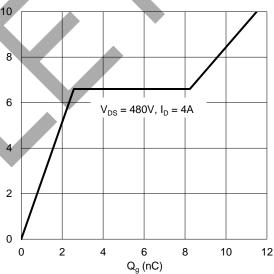


Figure 10. Gate Charge



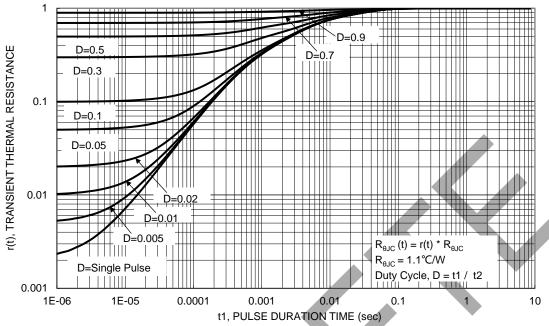
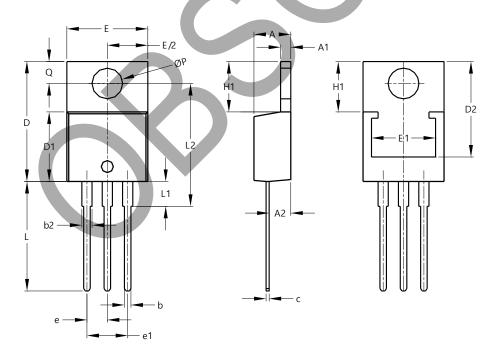


Figure 12. Transient Thermal Resistance

# **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.





TO220AB						
Dim	Min	Max	Тур			
Α	3.56	4.82				
A1	0.51	1.39	_			
A2	2.04	2.92	_			
b	0.39	1.01	0.81			
b2	1.15	1.77	1.24			
C	0.356	0.61	_			
D	14.22	16.51	_			
D1	8.39	9.01	_			
D2	11.45	12.87	_			
е	_		2.54			
e1	_	_	5.08			
Е	9.66	10.66	_			
E1	6.86	8.89	_			
H1	5.85	6.85	_			
L	12.70	14.73	_			
L1		4.42	_			
L2	15.80	17.51	16.00			
Р	3.54	4.08	_			
ø	2.54	3.42	_			
All Dimensions in mm						



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