



### N-CHANNEL ENHANCEMENT MODE MOSFET

### **Product Summary**

BV <sub>DSS</sub>	R <sub>DS(ON)</sub> max	Package	I <sub>D MAX</sub> T <sub>C</sub> = +25°С	
650V	3.0Ω@V <sub>GS</sub> = 10V	ITO220AB	4.0A	

### Description

This new generation complementary MOSFET features low onresistance and fast switching, making it ideal for high efficiency power management applications.

## Applications

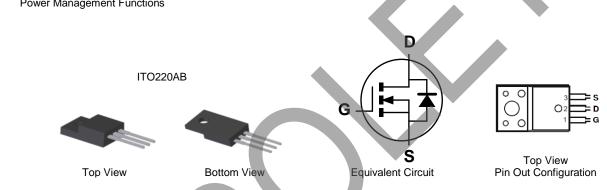
- Motor Control
- Backlighting
- **DC-DC Converters**
- **Power Management Functions**

### **Features**

- Low Input Capacitance
- High BV<sub>DSS</sub> Rating for Power Application
- Low Input/Output Leakage
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

### **Mechanical Data**

- Case: ITO220AB
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish-Matte Tin Annealed over Copper Leadframe Solderable per MIL-STD-202, Method 208 (e3)
- Terminal Connections: See Diagram Below
- Weight: 0.008 grams (Approximate)



# Ordering Information (Note 4)

Part Number	Case	Packaging
DMG4N65CTI	ITO220AB	50 pieces/Tube

1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied. Notes: 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and

l ead-free

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

# Marking Information



⊃ : : = Manufacturer's Marking 4N65CTI = Product Type Marking Code YYWW = Date Code Marking YY = Last Two Digits of Year (ex: 19 = 2019) WW = Week (01 to 53)



### Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit	
Drain-Source Voltage			V <sub>DSS</sub>	650	V	
Gate-Source Voltage			V <sub>GSS</sub>	±30	V	
Continuous Drain Current (Notes 5 and 6) $V_{GS}$ = 10V	Steady State	$T_C = +25^{\circ}C$		4.0	•	
		T <sub>C</sub> = +70°C	ID	3.0	А	
Pulsed Drain Current (Note 7)			I <sub>DM</sub>	6	А	
Avalanche Current (Note 8 ) $V_{DD}$ = 100V, $V_{GS}$ = 10V, L = 60mH			I <sub>AS</sub>	3.9	A	
Repetitive avalanche energy (Note 7)			E <sub>AS</sub>	456	mJ	

# **Thermal Characteristics**

Characteristic	Symbol	Max	Unit
Power Dissipation (Note 5)	PD	8.35	W
Thermal Resistance, Junction to Ambient $@T_A = +25$ °C (Note 5)	R <sub>0JA</sub>	12.36	°C/W
Thermal Resistance, Junction to Case $@T_A = +25^{\circ}C$ (Note 5)	R <sub>θJC</sub>	10.69	°C/W
Operating and Storage Temperature Range	TJ, TSTG	-55 to +150	С°

# Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 9)	- Cynison		• • •	inax	0		
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	650	-	_	V	$V_{GS} = 0V, I_D = 250 \mu A$	
Zero Gate Voltage Drain Current $T_J = +25^{\circ}C$	I <sub>DSS</sub>	—		1.0	μA	$V_{DS} = 650 V, V_{GS} = 0 V$	
Gate-Source Leakage	IGSS		~	±100	nA	$V_{GS} = \pm 30V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 9)							
Gate Threshold Voltage	V <sub>GS(TH)</sub>	3		5	V	$V_{DS} = V_{GS}, I_D = 250 \mu A$	
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>	-	2.1	3.0	Ω	$V_{GS} = 10V, I_D = 2A$	
Forward Transfer Admittance	Y <sub>fs</sub>	_	3.7	_	S	$V_{DS} = 40V, I_{D} = 2A$	
Diode Forward Voltage	V <sub>SD</sub>	_	0.7	1.0	V	$V_{GS} = 0V, I_{S} = 1A$	
DYNAMIC CHARACTERISTICS (Note 10)							
Input Capacitance	Ciss	—	900	_			
Output Capacitance	Coss	_	50	_	pF	$V_{DS} = 25V, V_{GS} = 0V,$ f = 1.0MHz	
Reverse Transfer Capacitance	Crss	_	1.1	_		T = T.000112	
Gate Resistance	Rg	_	2.4	_	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1MHz$	
Total Gate Charge V <sub>GS</sub> = 10V	Qg	_	13.5				
Gate-Source Charge	Q <sub>gs</sub>		2.7		nC	$V_{GS} = 10V, V_{DS} = 520V,$ In = 4A	
Gate-Drain Charge	Q <sub>gd</sub>	_	3.8			ID = 4A	
Turn-On Delay Time	t <sub>D(on)</sub>	_	15.1		ns		
Turn-On Rise Time	tr	—	13.8	_	ns		
Turn-Off Delay Time	t <sub>D(off)</sub>	_	40	_	ns	$R_g = 25\Omega, I_D = 4A$	
Turn-Off Fall Time	t <sub>f</sub>	_	16	_	ns		
Body Diode Reverse Recovery Time	t <sub>rr</sub>	_	515	_	ns	dl/dt = 100A/µs, V <sub>DS</sub> = 100V,	
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>		2330		nC	$I_F = 4A$	

Notes: 5. Device mounted on an infinite heatsink

6. Drain current limited by maximum junction temperature.

7. Repetitive rating, pulse width limited by junction temperature.

8.  $I_{AS}$  and  $E_{AS}$  ratings are based on low frequency and duty cycles to keep  $T_J = +25^{\circ}C$ .

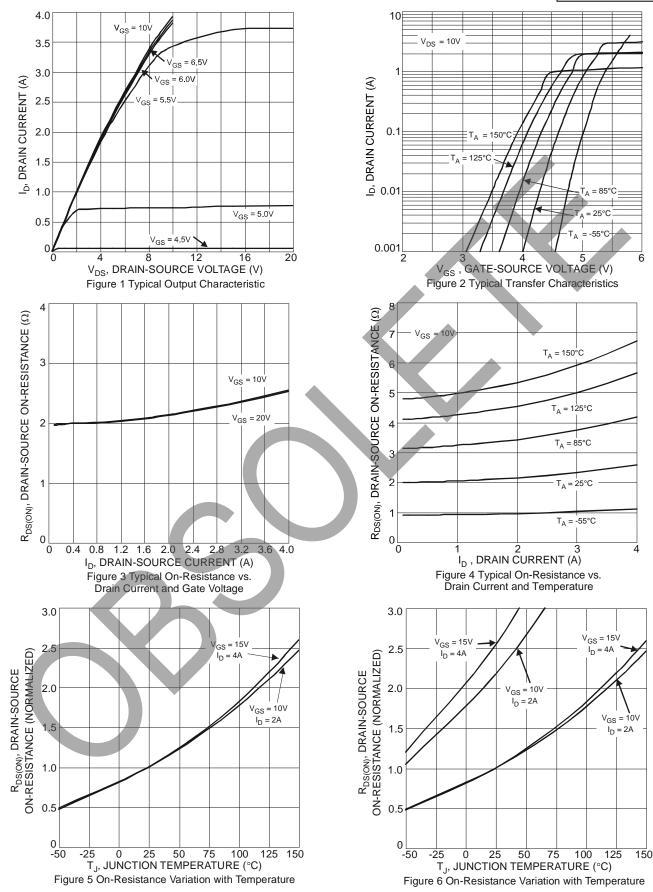
9. Short duration pulse test used to minimize self-heating effect.

10. Guaranteed by design. Not subject to production testing.



## PART OBSOLETE - NO ALTERNATE PART

DMG4N65CTI

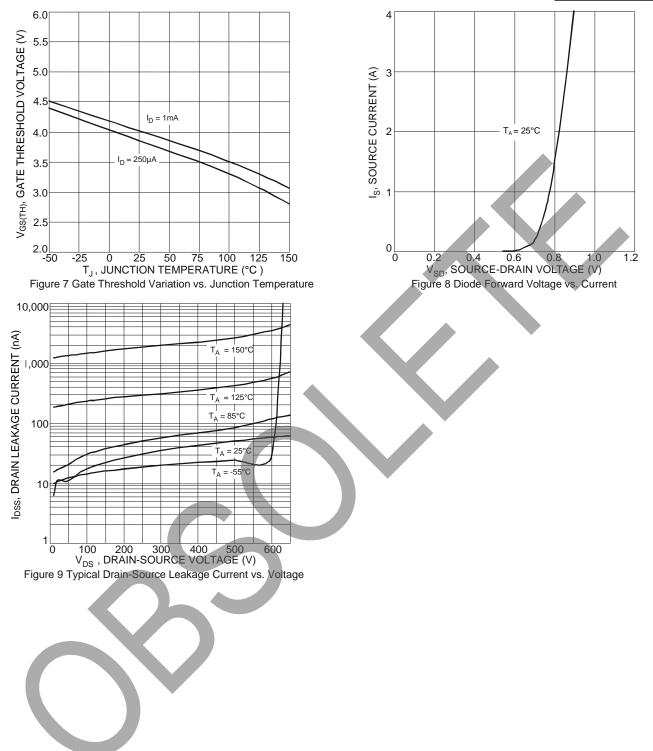


DMG4N65CTI Document number: DS36122 Rev. 2 - 4



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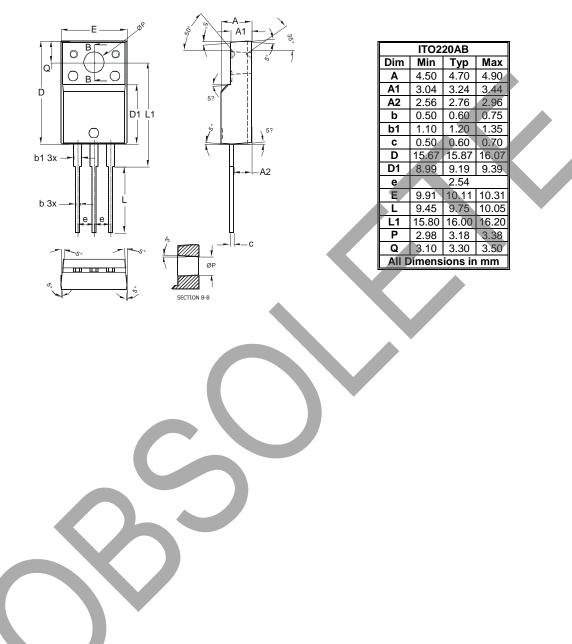


DMG4N65CTI

## **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

ITO220AB





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