## N-CHANNEL ENHANCEMENT MODE MOSFET

#### **Product Summary**

BV <sub>DSS</sub>	RDS(ON) Max	I <sub>D</sub> T <sub>C</sub> = +25°C	
650V	1.4Ω @ V <sub>G</sub> S = 10V	5.5A	

### **Description and Applications**

This MOSFET is designed to minimize the on-state resistance (RDS(ON)) yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

- Motor controls
- Backlighting
- DC-DC converters
- Power management functions

#### **Features and Benefits**

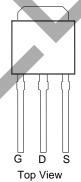
- Low On-Resistance
- High BV<sub>DSS</sub> Rating for Power Application
- Low Input Capacitance
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please contact us or your local Diodes representative. https://www.diodes.com/quality/product-definitions/

#### **Mechanical Data**

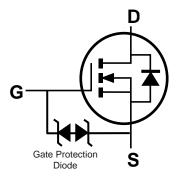
- Package: TO251
- Package Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Terminals: Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 @3
- Weight: 0.33 grams (Approximate)







Pin Configuration



Internal Schematic

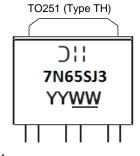
### Ordering Information (Note 4)

Part Number	Package	Packing		
Fait Number	Fackage	Qty.	Carrier	
DMG7N65SJ3	TO251 (Type TH)	75 Pieces	Tube	

Notes:

- 1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
- See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + CI) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

## **Marking Information**



D∷ = Manufacturer's Marking 7N65SJ3 = Product Type Marking Code YYWW = Date Code Marking YY = Last Two Digits of Year (ex: 22 = 2022) WW = Week Code (01 to 53)

DMG7N65SJ3 Document number: DS38155 Rev. 5 - 4

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## **Maximum Ratings** (@ $T_A = +25^{\circ}C$ , unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Drain-Source Voltage	VDSS	650	V	
Gate-Source Voltage		Vgss	±30	V
Continuous Drain Current (Note 5) V <sub>GS</sub> = 10V	$T_{C} = +25^{\circ}C$ $T_{C} = +100^{\circ}C$	ΙD	5.5 3.7	А
Continuous Drain Current (Note 5) V <sub>GS</sub> = 10V	T <sub>A</sub> = +25°C	ΙD	0.7	Α
Maximum Body Diode Forward Current (Note 5)		Is	5.5	Α
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)		lом	10	Α
Avalanche Current (Note 6)	L = 60mH	I <sub>AS</sub>	1.7	Α
Avalanche Energy (Note 6)	L = 60mH	Eas	87	mJ
Peak Diode Recovery dV/dt (Note 6)		dV/dt	3	V/ns

### Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

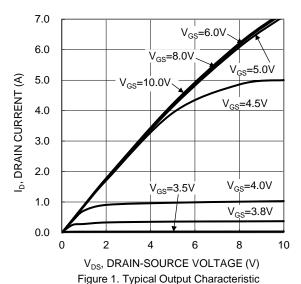
Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)	$T_{C} = +25^{\circ}C$ $T_{C} = +100^{\circ}C$	P <sub>D</sub>	125 50	W
Total Power Dissipation (Note 5)	T <sub>A</sub> = +25°C	PD	1.8	W
Thermal Resistance, Junction to Ambient (Note 5)		Reja	70	°C/W
Thermal Resistance, Junction to Case (Note 5)		Rejc	1	C/VV
Operating and Storage Temperature Range		TJ, T <sub>STG</sub>	-55 to +150	°C

## Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BVDSS	650	_	_	V	$V_{GS} = 0V, I_{D} = 250\mu A$	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>		_	1	μA	$V_{DS} = 650V, V_{GS} = 0V$	
Gate-Source Leakage	Igss		_	10	μA	$V_{GS} = \pm 24V$ , $V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	V <sub>GS(TH)</sub>	2	3	4	V	$V_{DS} = V_{GS}$ , $I_D = 250\mu A$	
Static Drain-Source On-Resistance	RDS(ON)	_	1.1	1.4	Ω	$V_{GS} = 10V, I_D = 2.5A$	
Diode Forward Voltage	VsD	_	0.84	1.5	V	VGS = 0V, IS = 5A	
DYNAMIC CHARACTERISTICS (Note 6)	DYNAMIC CHARACTERISTICS (Note 6)						
Input Capacitance	Ciss	_	886	_		V <sub>DS</sub> = 50V, f = 1MHz V <sub>GS</sub> = 0V	
Output Capacitance	Coss	_	62	_	pF		
Reverse Transfer Capacitance	Crss	-	8.8	-			
Gate Resistance	$R_{G}$		1.36		Ω	$V_{DS} = 0V$ , $V_{GS} = 0V$ , $f = 1MHz$	
Total Gate Charge	$Q_g$	_	25	_		1001/ 1 54	
Gate-Source Charge	Qgs	_	3.5	_	nC	$V_{DS} = 480V, I_{D} = 5A$ $V_{GS} = 10V$	
Gate-Drain Charge	$Q_{gd}$	-	12.4	-			
Turn-On Delay Time	t <sub>D(ON)</sub>		10				
Turn-On Rise Time	t <sub>R</sub>	_	11	_		$V_{DS} = 300V, V_{GS} = 10V$ $R_{G} = 4.7\Omega, I_{D} = 2.5A$	
Turn-Off Delay Time	tD(OFF)		36	-	ns		
Turn-Off Fall Time	tF		15				
Body Diode Reverse Recovery Time	t <sub>RR</sub>		245		ns	V <sub>DS</sub> = 100V, I <sub>F</sub> = 5A	
Body Diode Reverse Recovery Charge	Qrr	_	1.89	_	μC	dl/dt = 100A/µs	

5. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.6. Guaranteed by design. Not subject to production testing.7. Short duration pulse test used to minimize self-heating effect. Notes:





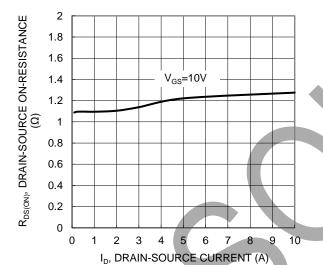


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

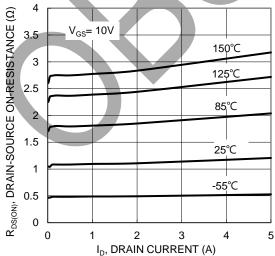


Figure 5. Typical On-Resistance vs. Drain Current and Junction Temperature

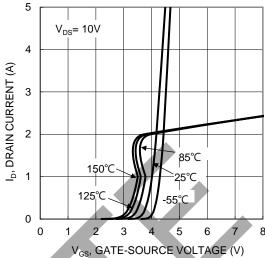


Figure 2. Typical Transfer Characteristic

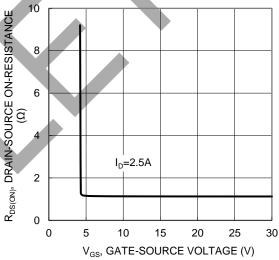


Figure 4. Typical Transfer Characteristic

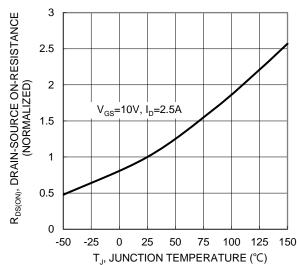


Figure 6. On-Resistance Variation with Junction Temperature



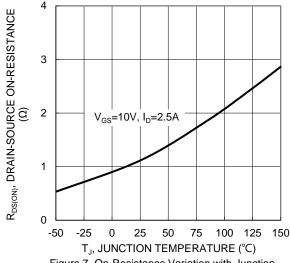
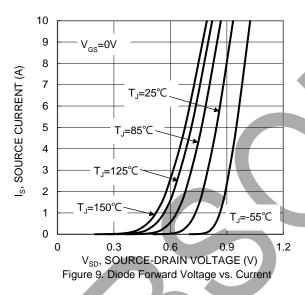


Figure 7. On-Resistance Variation with Junction Temperature



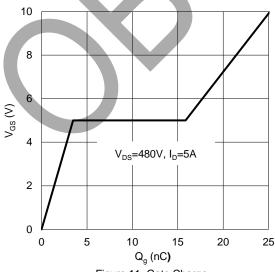


Figure 11. Gate Charge

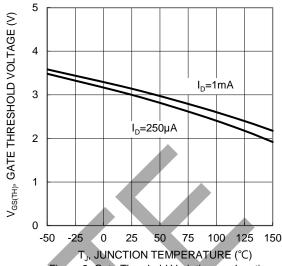
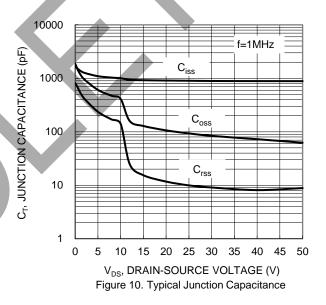


Figure 8. Gate Threshold Variation vs. Junction Temperature



100 \_\_\_\_  $R_{DS(ON)}$  Limited 10µs 10 ID, DRAIN CURRENT (A) =100µs 1 P<sub>W</sub>=10ms  $P_W$ =100ms T<sub>J(MAX)</sub>=150°C 0.1 T<sub>C</sub>=25°C Single Pulse DUT on infinite heatsink  $V_{GS}=10V$ 0.01 100 1000 V<sub>DS</sub>, DRAIN-SOURCE VOLTAGE (V) Figure 12. SOA, Safe Operation Area



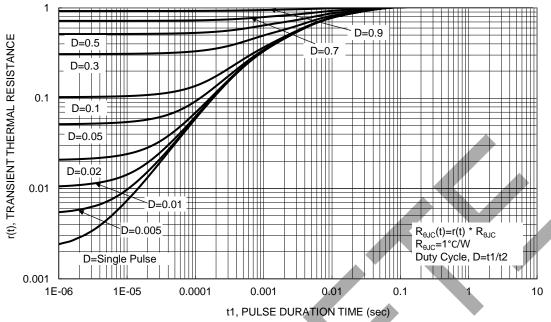


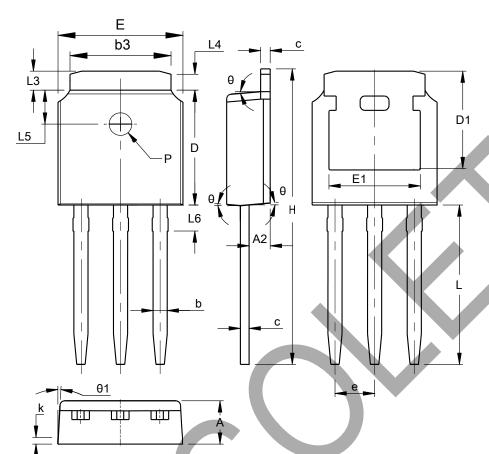
Figure 13. Transient Thermal Resistance



# **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

#### TO251 (Type TH)



TO251 (Type TH)					
Dim	Min	Max	Тур		
Α	2.20	2.40	2.30		
A2	0.97	1.17	1.07		
b	0.68	0.90	0.78		
b3	5.20	5.50	5.33		
С	0.43	0.63	0.53		
D	5.98	6.22	6.10		
D1	5	.30 RE	F		
е	2.	286 BS	C		
E	6.40	6.80	6.60		
E1	4.63	5.03	4.83		
H	16.22	16.82	16.52		
k	0.40REF				
L	9.15	9.65	9.40		
L3	0.88	1.28	1.02		
L4	0.75 REF				
L5	1.65	1.95	1.80		
L6	0.85	1.25	1.05		
ΡØ	1.20				
θ	5°	9°	7°		
θ1	5°	9°	7°		
All Dimensions in mm					



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