





N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{DSS}	Rds(on) max	ID MAX Tc = +25°C	
700V	1.3Ω @ V _{GS} = 10V	4.6A	

Description and Applications

This MOSFET is designed to minimize the on-state resistance (R_{DS(ON)}) yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

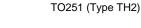
- Motor controls
- Backlighting
- AC-DC converters

Features and Benefits

- Low On-Resistance
- High BV_{DSS} Rating for Power Application
- Low Input Capacitance
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please contact us or your local Diodes representative. https://www.diodes.com/quality/product-definitions/

Mechanical Data

- Package: TO251
- Package Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Terminals: Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 @3
- Weight: 0.33 grams (Approximate)

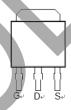




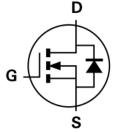
Top View



Bottom View



Top View Pin Configuration



Internal Schematic

Ordering Information (Note 4)

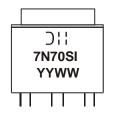
Part Number	Pagkaga Pagkaga		king
Part Number	Package	Qty.	Carrier
DMJ70H1D3SI3	TO251 (Type TH2)	75 Pieces	Tube

Notes:

- 1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and
- 3, Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.</p>
 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information

TO251 (Type TH2)



Dil = Manufacturer's Marking 7N70SI = Product Type Marking Code YYWW = Date Code Marking YY or YY = Last Two Digits of Year (ex. 22 = 2022) WW or WW = Week Code (01 to 53)

DMJ70H1D3SI3 Document number: DS38121 Rev. 3 - 4

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Maximum Ratings (@ $T_A = +25^{\circ}C$, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Drain-Source Voltage	V _{DSS}	700	V	
Gate-Source Voltage		Vgss	±30	V
Continuous Drain Current (Note 5) V _{GS} = 10V	$T_{C} = +25^{\circ}C$ $T_{C} = +100^{\circ}C$	ΙD	4.6 2.9	Α
Maximum Body Diode Forward Current (Note 6)		Is	3.0	А
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)		Ірм	5.4	Α
Avalanche Current (Note 7)	L = 60mH	las	1.1	Α
Avalanche Energy (Note 7)	L = 60mH	Eas	40	mJ
Peak Diode Recovery dV/dt (Note 7)		dV/dt	5	V/ns

Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit	
Total Power Dissipation (Note 5)	Tc = +25°C	Do.	41	W	
Total Power Dissipation (Note 5)	Tc = +100°C	PD	16	VV	
Thermal Resistance, Junction to Ambient (Note 6)		Reja	79	°C/W	
Thermal Resistance, Junction to Case (Note 5)	Rejc	3.0	C/VV		
Operating and Storage Temperature Range		TJ, TSTG	-55 to +150	°C	

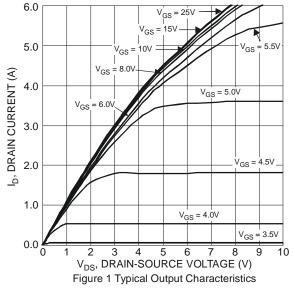
Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

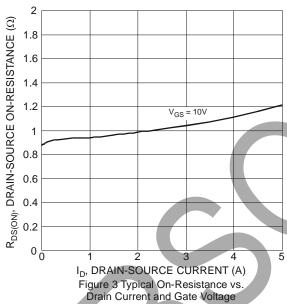
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 8)							
Drain-Source Breakdown Voltage	BVDSS	700		_	V	Vgs = 0V, ID = 250µA	
Zero Gate Voltage Drain Current	IDSS	_	_	1	μΑ	V _{DS} = 700V, V _{GS} = 0V	
Gate-Source Leakage	Igss		_	100	nA	$V_{GS} = \pm 30V$, $V_{DS} = 0V$	
ON CHARACTERISTICS (Note 8)							
Gate Threshold Voltage	VGS(TH)	2	2.9	4	V	$V_{DS} = V_{GS}$, $I_D = 250\mu A$	
Static Drain-Source On-Resistance	RDS(ON)	1	1.0	1.3	Ω	VGS = 10V, ID = 2.5A	
Diode Forward Voltage	V _{SD}	-	0.9	1.3	٧	$V_{GS} = 0V, I_{S} = 5A$	
DYNAMIC CHARACTERISTICS (Note 7)							
Input Capacitance	Ciss	-	351	_		V 50V 5 4MH-	
Output Capacitance	Coss	_	66	_	pF	V _{DS} = 50V, f = 1MHz, V _{GS} = 0V	
Reverse Transfer Capacitance	C _{rss}		1.1	_		VGS - 0V	
Gate Resistance	Rg	_	3.5	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$	
Total Gate Charge	Qg	_	13.9	_		V F60V I- FA	
Gate-Source Charge	Q_{gs}	_	1.9	_	nC	$V_{DD} = 560V, I_{D} = 5A$ $V_{GS} = 10V$	
Gate-Drain Charge	Q_{gd}	_	8.5	_		VGS = 10V	
Turn-On Delay Time	t _{D(on)}	1	8.5	_		$V_{DD} = 350V, V_{GS} = 10V$ $R_{G} = 4.7\Omega, I_{D} = 2.5A$	
Turn-On Rise Time	tr	1	11.6	_	ns		
Turn-Off Delay Time	t _{D(off)}	_	24.5	_	115		
Turn-Off Fall Time	tf	_	10	_			
Body Diode Reverse Recovery Time	t _{rr}	-	212	_	ns		
Body Diode Reverse Recovery Time (T _J = +150°C)	t _{rr}	_	251	_	ns	la - 50 dl/dt - 1000/ug	
Body Diode Reverse Recovery Charge	Qrr	_	1.8	_	μC	I _S = 5A, dI/dt = 100A/μs	
Body Diode Reverse Recovery Charge (T _J = +150°C)	Qrr	_	2.3	_	μC		

Notes:

- 5. Device mounted on infinite heatsink.
 6. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
 7. Guaranteed by design. Not subject to production testing.
 8. Short duration pulse test used to minimize self-heating effect.







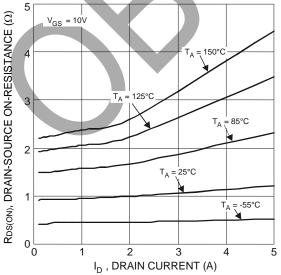
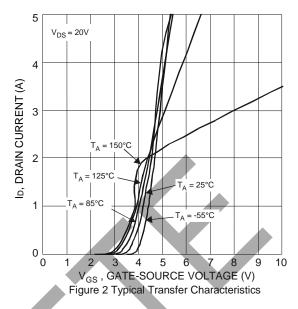
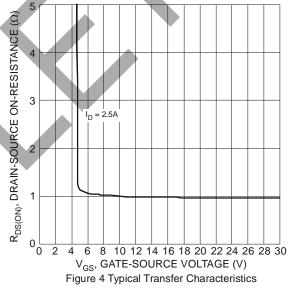


Figure 5 Typical On-Resistance vs. Drain Current and Temperature





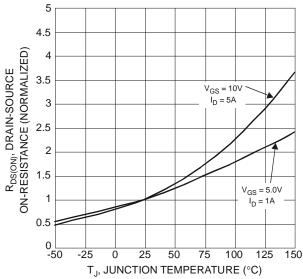
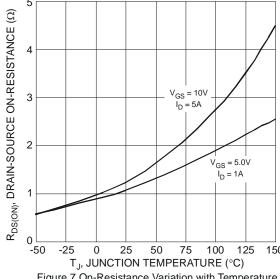
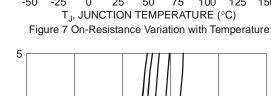
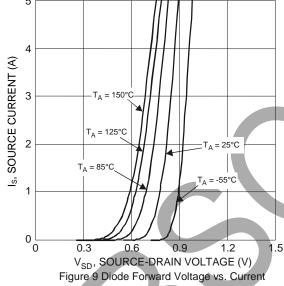


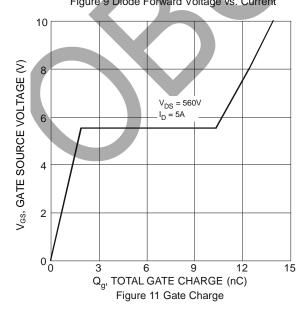
Figure 6 On-Resistance Variation with Temperature











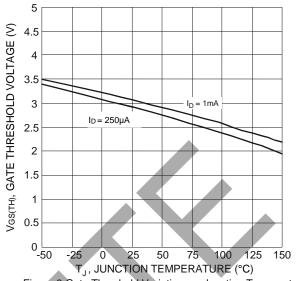
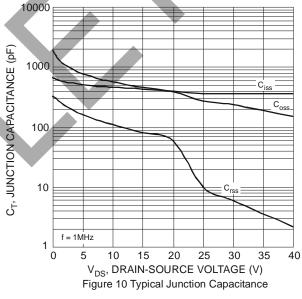
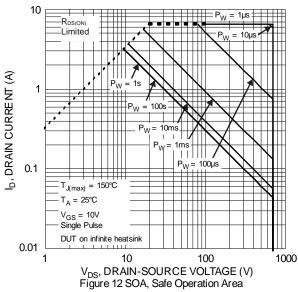
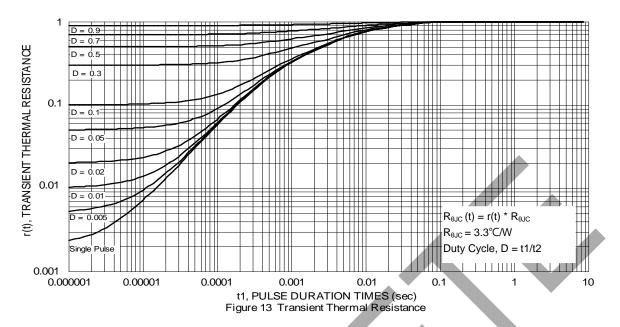


Figure 8 Gate Threshold Variation vs. Junction Temperature







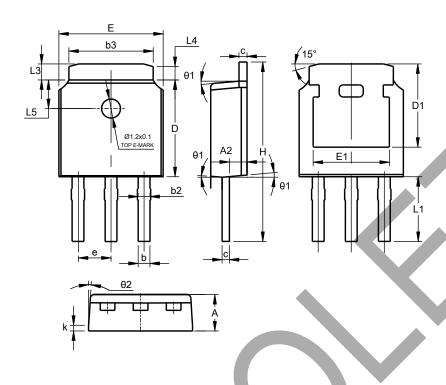




Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

TO251 (Type TH2)



TO251 (Type TH2)						
Dim	Min	Max	Тур			
Α	2.20	2.40	2.30			
A2	0.97	1.17	1.07			
b	0.68	0.90	0.78			
b2	0.76	0.95	0.84			
b3	5.20	5.50	5.33			
С	0.43	0.63	0.53			
D	5.98	6.22	6.10			
D1	5	.30 RE	F			
e	2.	286 BS	C			
E	6.40	6.80	6.60			
E1	4.63	5.03	4.83			
Н	10.42	10.82	10.62			
k		0.40REF				
L1	3.30	3.70	3.50			
_ L3	0.88	1.28	1.02			
L4	0.75 REF					
L5	1.65	1.95	1.80			
θ1	5°	9°	7°			
θ2	5°	9°	7°			
All Dimensions in mm						



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