

NOT RECOMMENDED FOR NEW DESIGN **CONTACT US**



DMN2114SN

N-CHANNEL ENHANCEMENT MODE FIELD EFFECT TRANSISTOR

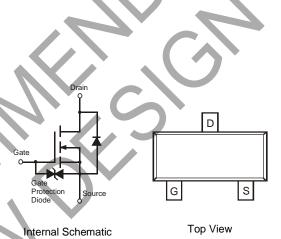
Features

- Low On-Resistance
- Ideal for Notebook Computer, Portable Phone, PCMCIA Cards, and Battery Power Circuits
- **ESD Protected Gate**
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e.: parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please refer to the related automotive grade (Qsuffix) part. A listing can be found at https://www.diodes.com/products/automotive/automotiveproducts/.
- This part is qualified to JEDEC standards (as references in AEC-Q) for High Reliability.

https://www.diodes.com/quality/product-definitions/

Mechanical Data

- Package: SC59
- Package Material Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Annealed over Copper Leadframe. Solderable Per MIL-STD-202, Method 208 @3
- Terminal Connections: See Diagram
- Weight: 0.014 grams (Approximate)



ESD protected



SC59

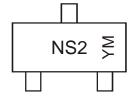
Ordering Information (Note 4)

Part Number	< /	Package	Pac	king
Part Number		Package	Qty.	Carrier
DMN2114SN-7		SC59	3000	Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



NS2 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: K = 2023)M = Month (ex: 9 = September)

Date Code Key

Year	2006		2023	2024	2025	2026	2027	2028	2029	2030	2031	2032
Code	Т		K	L	М	N	0	Р	R	S	Т	U
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	g	0	N	D



Maximum Ratings (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Drain-Source Voltage	V _{DSS}	20	V
Gate-Source Voltage Continuous	Vgss	±12	V
Drain Current Continuous Pulsed	lo	1.2 4.0	A

Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

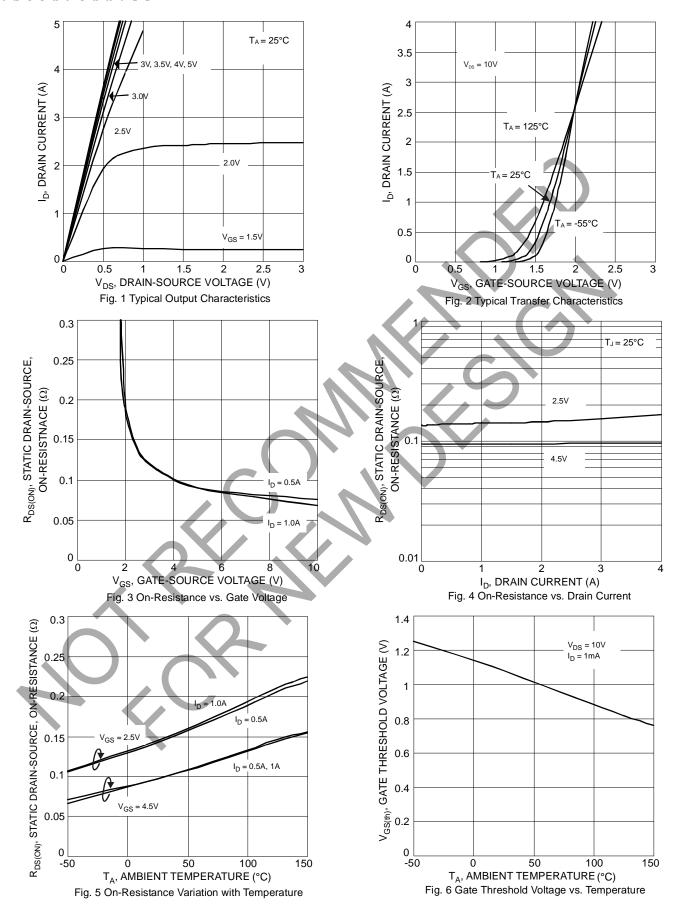
Characteristic	Symbol	Value	Unit
Total Power Dissipation	Pd	500	mW
Thermal Resistance, Junction to Ambient	$R_{ heta JA}$	250	°C W
Operating and Storage Temperature Range	TJ, TSTG	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

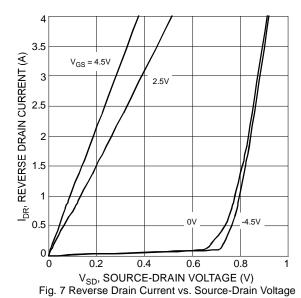
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 5)							
Drain-Source Breakdown Voltage	BV _{DSS}	20		_	V	$V_{GS} = 0V, I_{D} = 250\mu A$	
Zero Gate Voltage Drain Current @ T _J = +25	5°C IDSS	1		10	μΑ	V _{DS} = 24V, V _{GS} = 0V	
Gate-Body Leakage	I _{GSS}		_	±10	μΑ	$V_{GS} = \pm 12V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 5)			4				
Gate Threshold Voltage	VGS(th)	0.7		1.40	V	$V_{DS} = 10V, I_{D} = 1.0mA$	
Static Drain-Source On-Resistance	RDS (ON)			0.100 0.160	Ω	$V_{GS} = 4.5V, I_D = 0.5A$	
Static Brain Godice On Resistance	TVDS (ON)				52	$V_{GS} = 2.5V, I_{D} = 0.5A$	
Forward Transfer Admittance	Y _{fs}		3.3	_	S	$V_{DS} = 10V, I_{D} = 0.5A$	
Diode Forward Voltage	V _{SD}		0.8	1.1	V	$V_{GS} = 0V$, $I_{S} = 1.0A$	
DYNAMIC CHARACTERISTICS							
Input Capacitance	C _{iss}		180	_	pF	101/1/	
Output Capacitance	Coss		120	_	pF	V _{DS} = 10V, V _{GS} = 0V, f = 1.0MHz	
Reverse Transfer Capacitance	Crss	4	45	_	pF	T = 1.0WHZ	
SWITCHING CHARACTERISTICS							
Turn-On Delay Time	tD(ON)		10	_	ns		
Turn-Off Delay Time	t _{D(OFF)}		50	_	ns	$V_{DD} = 10V, I_D = 0.5A,$	
Turn-On Rise Time	tr	_	15	_	ns	$V_{GS} = 5.0V$, $R_{GEN} = 50\Omega$	
Turn-Off Fall Time	tf		45	_	ns		

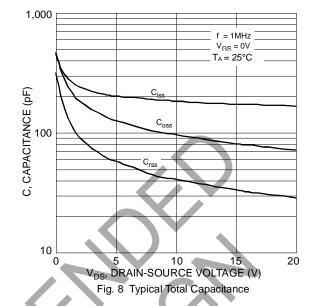
Note: 5. Pulse width $\leq 300 \mu S$, duty cycle $\leq 2\%$.









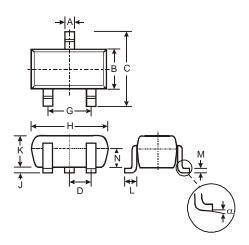




Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SC59

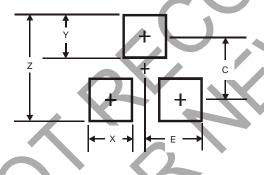


SC59						
Dim	Min	Max	Тур			
Α	0.35	0.50	0.38			
В	1.50	1.70	1.60			
C	2.70	3.00	2.80			
D	-	-	0.95			
G	-	-	1.90			
Н	2.90	3.10	3.00			
J	0.013	0.10	0.05			
K	1.00	1.30	1.10			
L	0.35	0.55	0.40			
M	0.10	0.20	0.15			
Z	0.70	0.80	0.75			
α	0°	8°	-			
All Dimensions in mm						

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.





Dimensions	Value (in mm)
Z	3.4
Х	0.8
Υ	1.0
С	2.4
E	1.35



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