



30V N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{DSS}	Rds(on) Max	I _D Max T _A = +25°C	
30V	$7m\Omega$ @ V _{GS} = 10V	16A	
307	10mΩ @ V _{GS} = 4.5V	13.5A	

Description and Applications

This MOSFET has been designed to minimize the on-state resistance (RDS(ON)) yet maintain superior switching performance, making it ideal for high-efficiency power-management applications.

- Backlighting
- Power-management functions
- DC-DC converters

Features and Benefits

- Low On-Resistance
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- The DMN3007LSSQ is suitable for automotive applications requiring specific change control; this part is AEC-Q101 qualified, PPAP capable, and manufactured in IATF 16949 certified facilities.

https://www.diodes.com/quality/product-definitions/

Mechanical Data

Package: SO-8

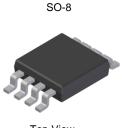
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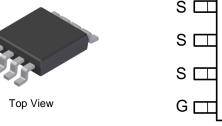
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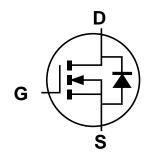
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- Package Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections Indicator: See diagram
- Terminals: Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.074 grams (Approximate)







Equivalent Circuit

Ordering Information (Note 4)

Part Number	Paakaga	Packing		
Part Number	Package	Qty.	Carrier	
DMN3007LSSQ-13	SO-8	2,500	Tape & Reel	

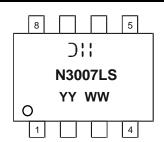
Top View

Internal Schematic

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + CI) and
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/

Marking Information



⊃¦¦ = Manufacturer's Marking N3007LS = Product Type Marking Code YYWW = Date Code Marking YY = Year (ex: 24 = 2024)WW = Week (01 to 53)



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V _{DSS}	30	V
Gate-Source Voltage			Vgss	±20	V
Drain Current (Note 5)	Steady State	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	I _D	16 13	А
Pulsed Drain Current (Note 6)			IDM	64	Α

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 5)	P _D	2.5	W
Thermal Resistance, Junction to Ambient	RθJA	50	°C/W
Operating and Storage Temperature Range	TJ, TSTG	-55 to +150	°C

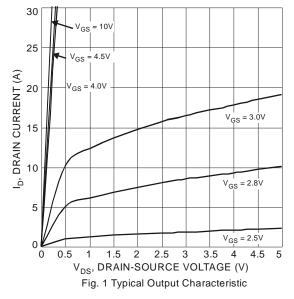
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

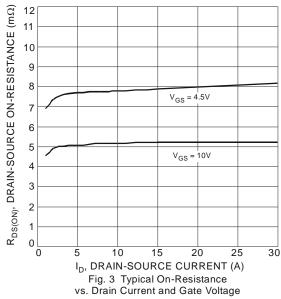
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BVDSS	30	_	_	V	$V_{GS} = 0V, I_{D} = 250\mu A$
Zero Gate Voltage Drain Current	IDSS	_	_	1	μΑ	V _{DS} = 30V, V _{GS} = 0V
Gate-Source Leakage	Igss	_	_	±100	nA	$V_{GS} = \pm 20V$, $V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V _{GS(TH)}	1.3	_	2.1	V	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$
Static Drain-Source On-Resistance	RDS (ON)	_	5 7.9	7 10	mΩ	V _G S = 10V, I _D = 15A V _G S = 4.5V, I _D = 13A
Forward Transconductance	G fs	_	16.4	_	S	V _{DS} = 10V, I _D = 15A
Diode Forward Voltage	VsD	_	0.67	1.2	V	V _G S = 0V, I _S = 2.3A
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	Ciss	_	2714	_	pF	451/1/ 01/
Output Capacitance	Coss	_	436	_	pF	$V_{DS} = 15V, V_{GS} = 0V$ f = 1.0MHz
Reverse Transfer Capacitance	Crss	_	380	_	pF	1 = 1.0WH 12
Gate Resistance	Rg	_	0.7	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1.0MHz$
SWITCHING CHARACTERISTICS (Note 8)						
Total Gate Charge	Qg	_	31.2 64.2	_	nC	$V_{DS} = 15V$, $V_{GS} = 4.5V$, $I_{D} = 16A$ $V_{DS} = 15V$, $V_{GS} = 10V$, $I_{D} = 16A$
Gate-Source Charge	Qgs	_	7.1	_	nC	$V_{DS} = 15V, V_{GS} = 10V, I_D = 16A$
Gate-Drain Charge	Q _{gd}	_	17.1	_		V _{DS} = 15V, V _{GS} = 10V, I _D = 16A
Turn-On Delay Time	t _{D(ON)}	_	10.3	_		
Rise Time	t _R	_	14.8	_		V _{DS} = 15V, V _{GS} = 10V,
Turn-Off Delay Time	tD(OFF)	_	85.1	_	ns	$I_D = 1A$, $R_G = 6.0\Omega$
Fall Time	tF		43.6	_		

Notes: 5. Device mounted on 2 oz. Copper pads on FR-4 PCB, with $R_{\theta JA}$ = +50°C. 6. Pulse width \leq 10 μ s, Duty Cycle \leq 1%. 7. Short duration pulse test used to minimize self-heating effect.

8. Guaranteed by design. Not subject to product testing.







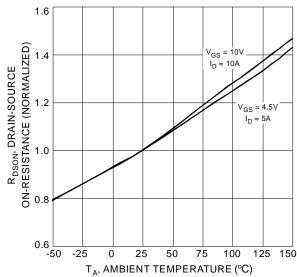
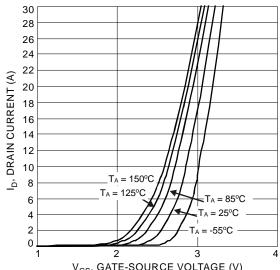


Fig. 5 On-Resistance Variation with Temperature



V_{GS}, GATE-SOURCE VOLTAGE (V) Fig. 2 Typical Transfer Characteristic

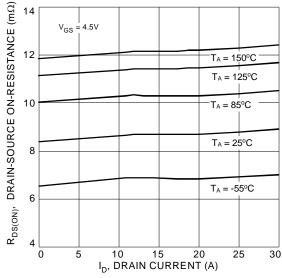


Fig. 4 Typical On-Resistance vs. Drain Current and Temperature

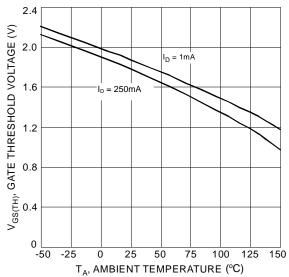
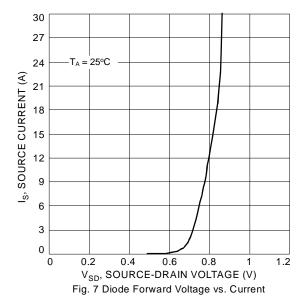
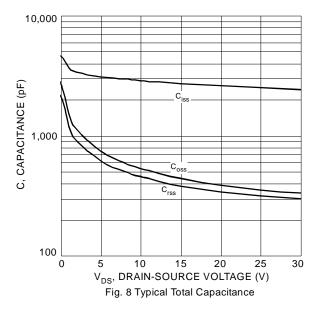
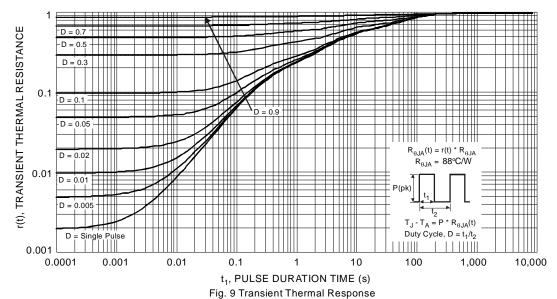


Fig. 6 Gate Threshold Variation vs. Ambient Temperature





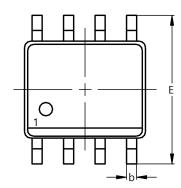


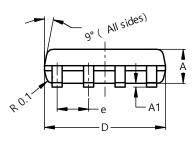


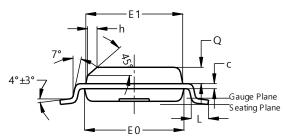


Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.







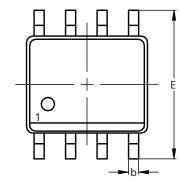
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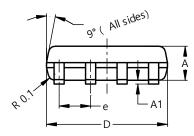
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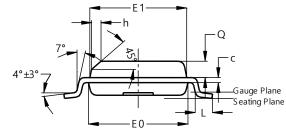
SO-8						
Dim	Min	Max	Тур			
Α	1.40	1.50	1.45			
A1	0.10	0.20	0.15			
b	0.30	0.50	0.40			
C	0.15	0.25	0.20			
D	4.85	4.95	4.90			
Е	5.90	6.10	6.00			
E1	3.80	3.90	3.85			
E0	3.85	3.95	3.90			
е	e 1.27					
h	-		0.35			
L	0.62	0.82	0.72			
ø	0.60	0.70	0.65			
All Dimensions in mm						

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.







SO-8					
Dim	Min	Max	Тур		
Α	1.40	1.50	1.45		
A1	0.10	0.20	0.15		
b	0.30	0.50	0.40		
C	0.15	0.25	0.20		
D	4.85	4.95	4.90		
Е	5.90	6.10	6.00		
E1	3.80	3.90	3.85		
E0	3.85	3.95	3.90		
е			1.27		
h	-		0.35		
١	0.62	0.82	0.72		
Q	0.60	0.70	0.65		
All Dimensions in mm					



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