

NOT RECOMMENDED FOR NEW DESIGN **USE DMN30H4D0L**



DMN30H4D1S

N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BVDSS	Rds(on) Max	I _D Max T _A = +25°C
300V	4.0Ω @ V _{GS} = 10V	0.43A
3007	5.0Ω @ V _{GS} = 4.5V	0.39A

Description

This new generation MOSFET is designed to minimize the on-state resistance (RDS(ON)) yet maintain superior switching performance, which makes the device ideal for high-efficiency powermanagement applications.

Applications

- DC-DC converters
- Power management functions
- Battery-operated systems and solid-state relays
- Drivers: relays, solenoids, lamps, hammers, displays, memories, transistors, and so on

Features and Benefits

- Low Gate-Threshold Voltage
- Low-Input Capacitance
- Fast-Switching Speed
- Small Surface-Mount Package
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please contact us or your local Diodes representative.

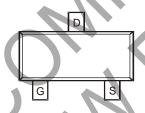
https://www.diodes.com/quality/product-definitions/

Mechanical Data

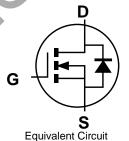
- Package: SOT23
- Package Material: Molded Plastic; UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 3 per J-STD-020
- Terminals: Solderable per MIL-STD-202, Method 208 Lead-Free Plating—Matte Tin Finish Annealed over Alloy 42 Leadframe (e3)
- Terminal Connections—See Diagram
- Weight: 0.008 grams (Approximate)







Top View Pin Configuration



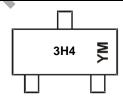
Ordering Information (Note 4)

Part Number	Packago	Pack	king
Part Number	Package	Qty.	Carrier
DMN30H4D1S-7	SOT23	3,000	Tape & Reel
DMN30H4D1S-13	SOT23	10,000	Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.</p>
 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



3H4 = Product Type Marking Code YM = Date Code Marking Y or \overline{Y} = Year (ex: J = 2022) M = Month (ex: 9 = September)

Date Code Key

Year	2018		2022	2023	2024	2025	2026	2027	2028	2029	2030	2031
Code	F		J	K	L	М	N	0	Р	R	S	Т
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec



Maximum Ratings (@ $T_A = +25$ °C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit		
Drain-Source Voltage	VDSS	300	V		
Gate-Source Voltage			Vgss	±20	V
Continuous Drain Current (Note 6) V _{GS} = 10V	I _D	0.43 0.34	А		
Pulsed Drain Current (10µs Pulse, Duty Cycle ≤ 1%)	I _{DM}	2	Α		
Maximum Body Diode Continuous Current (Note 6)	Is	1.3	Α		
Pulsed Source Current (10µs Pulse, Duty Cycle ≤ 1%	%)		Ism	2	A

Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)		PD	0.36	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	RөJA	162	°C/W
Total Power Dissipation (Note 6)		PD	0.43	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	R _Ө ЈА	111	°C/W
Thermal Resistance, Junction to Case (Note 6)		Rejo	31	°C/W
Operating and Storage Temperature Range		TJ, TSTG	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

	- 1					1
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BVDSS	300	_		V	$V_{GS} = 0V, I_{D} = 250\mu A$
Zero Gate Voltage Drain Current	IDSS	-		1.0	μA	V _{DS} = 240V, V _{GS} = 0V
Gate-Body Leakage	Igss	1		±100	nA	$V_{GS} = \pm 20V$, $V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	Vgs(TH)	1		3	V	$V_{DS} = V_{GS}$, $I_D = 250\mu A$
Static Drain-Source On-Resistance	Passan		2.29	4.0	Ω	$V_{GS} = 10V, I_{D} = 0.3A$
Static Dialii-Source Off-Resistance	R _{DS} (ON)	_	2.34	5.0	12	V _G S = 4.5V, I _D = 0.2A
Diode Forward Voltage	V _{SD}	_	0.7	1.2	V	$V_{GS} = 0V, I_{S} = 0.3A$
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C _{iss}	_	174	_		
Output Capacitance	Coss	_	12	_	pF	$V_{DS} = 25V, V_{GS} = 0V,$ f = 1.0MHz
Reverse Transfer Capacitance	Crss	_	7	_		1 = 1.000112
Gate Resistance	Rg	_	2.96	_	Ω	V _{DS} = 0V, V _{GS} = 0V, f = 1.0MHz
Total Gate Charge	Qg	_	4.8	_		
Gate-Source Charge	Qgs	_	0.6	_	nC	V _{DS} = 192V, V _{GS} = 10V, I _D = 0.5A
Gate-Drain Charge	Qgd	_	2.1	_		ID = 0.5A
Turn-On Delay Time	td(ON)	_	6.1	_		
Turn-On Rise Time	t _R	_	3.5	_		$V_{DS} = 60V, R_{L} = 200\Omega$
Turn-Off Delay Time	tD(OFF)	_	20.6	_	ns	$V_{GS} = 10V$, $R_{G} = 25\Omega$
Turn-Off Fall Time	tF	_	13.8	_		
Reverse Recovery Time	trr	_	43	_	ns	I _F =0.5A, di/dt=100A/µs
Reverse Recovery Charge	Qrr	_	51	_	nC	11=0.5A, di/di=100A/µ5

Notes:

^{5.} Device mounted on FR-4 PC board with minimum recommended pad layout, single sided.

^{6.} Device mounted on FR-4 substrate PC board, 2oz copper with 1-inch square copper pad layout.

^{7 .}Short-duration pulse test used to minimize self-heating effect.

^{8.} Guaranteed by design. Not subject to production testing.

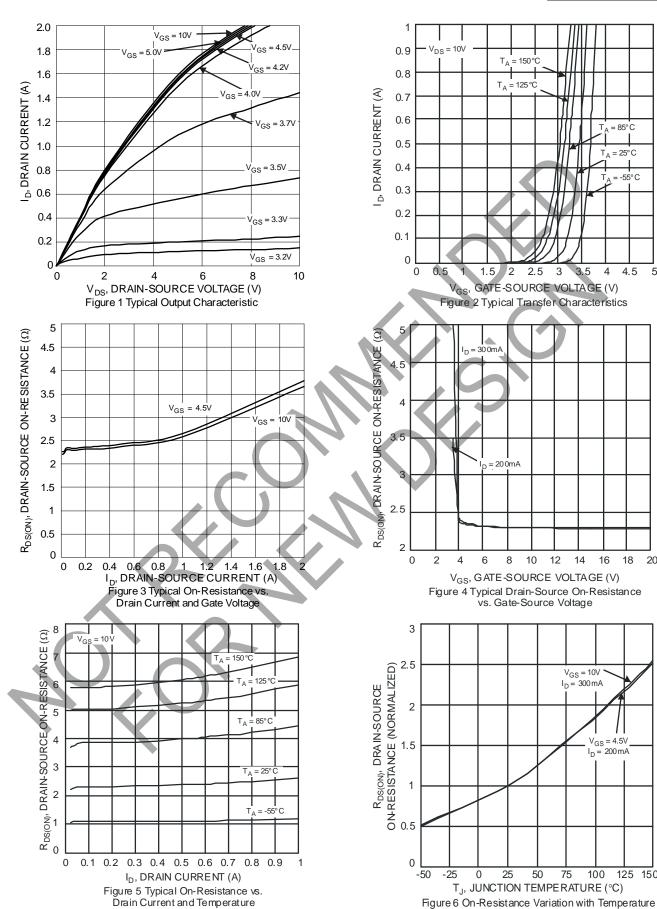


= 25° C

T_A = -55° C

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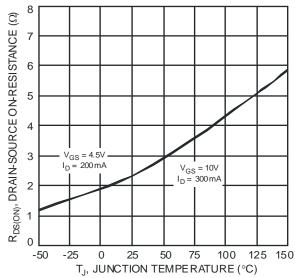
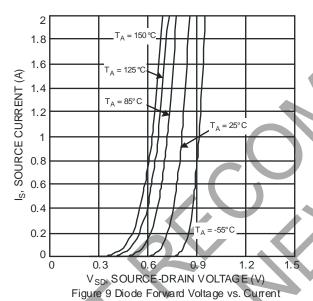
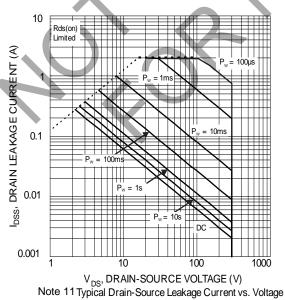
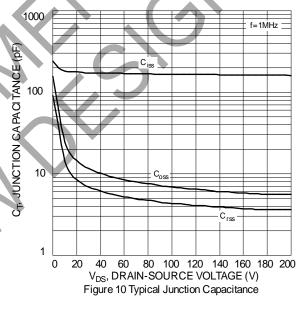


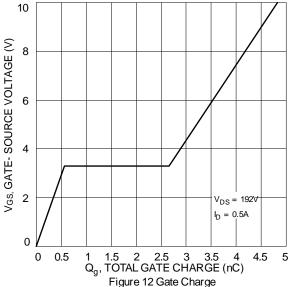
Figure 7 On-Resistance Variation with Temperature



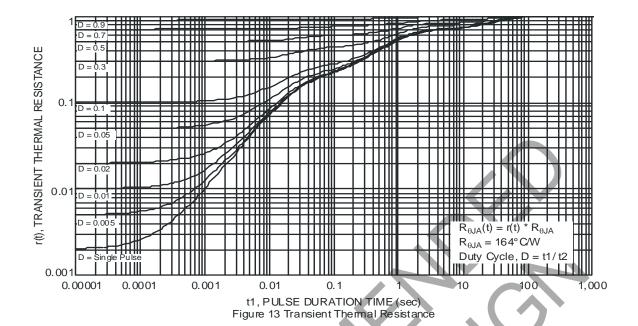


4 VGS(TH), GATE THRESHOLD VOLTAGE (V) 3.5 3 $I_D = 1mA$ 2.5 $I_D = 250 \mu A$ 2 1.5 0.5 0 -50 -25 0 25 50 75 100 125 150
T_J, JUNCTION TEMPERATURE (°C)
Figure 8 Gate Threshold Variation vs. Junction Temperature







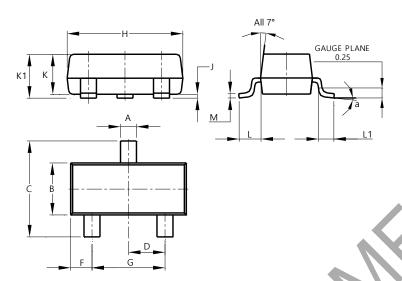




Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT23

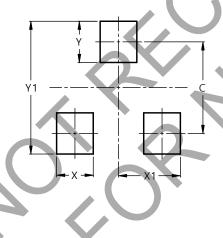


SOT23						
Dim	Min	Max	Тур			
Α	0.37	0.51	0.40			
В	1.20	1.40	1.30			
С	2.30	2.50	2.40			
D	0.89	1.03	0.915			
F	0.45	0.60	0.535			
G	1.78	2.05	1.83			
H	2.80	3.00	2.90			
J	0.013	0.10	0.05			
K	0.890	1.00	0.975			
K1	0.903	1.10	1.025			
, L	0.45	0.61	0.55			
L1	0.25	0.55	0.40			
M	0.085	0.150	0.110			
а	0°	8°				
All Dimensions in mm						

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.





Dimensions	Value (in mm)
С	2.0
Х	0.8
X1	1.35
Υ	0.9
Y1	2.9



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