



### **DUAL N-CHANNEL ENHANCEMENT MODE MOSFET**

## **Product Summary**

BVDSS	R <sub>DS(ON)</sub> Max	I <sub>D</sub> Max T <sub>A</sub> = +25°C
001/	0.4Ω @ V <sub>GS</sub> = 10V	0.89A
30V	0.7Ω @ V <sub>GS</sub> = 4.5V	0.67A

## **Features and Benefits**

- **Dual N-Channel MOSFET**
- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- **ESD Protected Gate**
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities). please contact us or your local Diodes representative. https://www.diodes.com/quality/product-definitions/

## **Description and Applications**

This MOSFET is designed to minimize the on-state resistance (RDS(ON)) yet maintain superior switching performance, making it ideal for high-efficiency power-management applications.

- Motor controls
- Power-management functions
- DC-DC converters

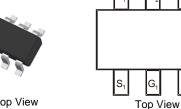
### **Mechanical Data**

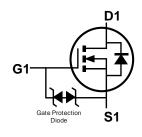
- Package: SOT363
- Package Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections Indicator: See Diagram
- Terminals: Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 @3
- Weight: 0.027 grams (Approximate)

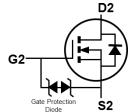




**SOT363** 







Top View

Q1 N-Channel

Q2 N-Channel

## Ordering Information (Note 4)

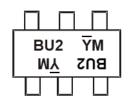
Part Number	Pankaga	Packing		
Fait Number	Package	Qty.	Carrier	
DMN3350LDW-7	SOT363	3000	Tape & Reel	
DMN3350LDW-13	SOT363	10000	Tape & Reel	

Pin Out

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/

# **Marking Information**



BU2 = Product Type Marking Code YM = Date Code Marking  $\overline{Y}$  = Year (ex: L = 2024) M = Month (ex: 9 = September)

Date Code Key

Date Code Ney												
Year	2023	2024	2025	2026	2027	2028	2029	2030	2031	2032	2033	2034
Code	K	L	М	N	Р	R	S	T	U	V	W	Х
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



## **Maximum Ratings** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Drain-Source Voltage	VDSS	V <sub>DSS</sub> 30		
Gate-Source Voltage	$V_{GSS}$	±20	V	
Continuous Drain Current (Note 6) V <sub>GS</sub> = 10V	lo	0.89 0.7	А	
Maximum Continuous Body Diode Forward Current	ls	0.48	Α	
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%	IDM	3.2	А	

# Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

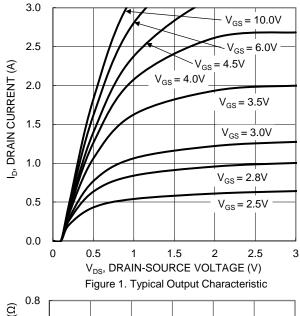
Characteristic	Symbol	Value	Unit	
Total Power Dissipation (Note 5)		PD	0.34	W
Thermal Resistance, Junction to Ambient (Note 5)	Reja	364	°C/W	
Total Power Dissipation (Note 6)		PD	0.48	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	R <sub>0JA</sub>	260	°C/W
Operating and Storage Temperature Range		TJ, TSTG	-55 to +150	°C

# Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
DFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	30	_	_	V	$V_{GS} = 0V, I_{D} = 250\mu A$
Zero Gate Voltage Drain Current	IDSS	_	_	1.0	μΑ	$V_{DS} = 30V$ , $V_{GS} = 0V$
Gate-Source Leakage	Igss	_	_	±10	μΑ	Vgs = ±20V, Vps = 0V
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	Vgs(TH)	0.8	_	1.6	V	$V_{DS} = V_{GS}$ , $I_D = 250\mu A$
Static Drain-Source On-Resistance	0	_	0.24	0.4	Ω	$V_{GS} = 10V, I_D = 0.59A$
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>	_	0.3	0.7	Ω	V <sub>G</sub> S = 4.5V, I <sub>D</sub> = 0.2A
Diode Forward Voltage	VsD	_	0.6	1.2	V	$V_{GS} = 0V$ , $I_{S} = 10mA$
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	Ciss	_	38.4	_	pF	\\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
Output Capacitance	Coss	_	10.5	_	pF	$V_{DS} = 15V, V_{GS} = 0V,$ -f = 1.0MHz
Reverse Transfer Capacitance	Crss	_	6.4	_	pF	1 = 1.000112
Total Gate Charge (V <sub>GS</sub> = 4.5V)	Qg	_	0.5	_	nC	
Total Gate Charge (V <sub>GS</sub> = 10V)	Qg	_	1.1	_	nC	$V_{DS} = 10V, V_{GS} = 10V$
Gate-Source Charge	Qgs	_	0.2	_	nC	I <sub>D</sub> = 250mA
Gate-Drain Charge	Q <sub>gd</sub>	_	0.1	_	nC	
Turn-On Delay Time	t <sub>D(ON)</sub>	_	3.2	_	ns	
Turn-On Rise Time	t <sub>R</sub>	_	12	_	ns	V <sub>G</sub> S = 10V, V <sub>D</sub> S = 30V,
Turn-Off Delay Time		_	82	_	ns	$I_D = 100 \text{mA}, R_G = 25 \Omega$
Turn-Off Fall Time	tF	_	51	_	ns	

 Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
 Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
 Short duration pulse test used to minimize self-heating effect.
 Guaranteed by design. Not subject to product testing. Notes:





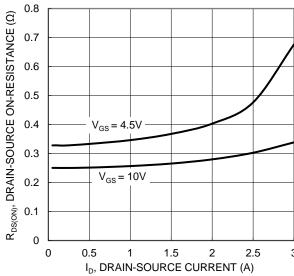


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

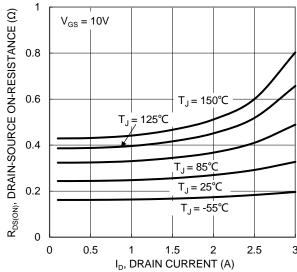
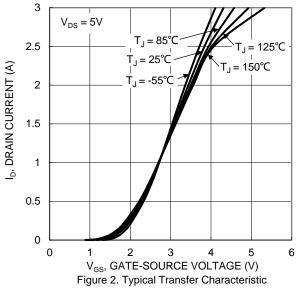
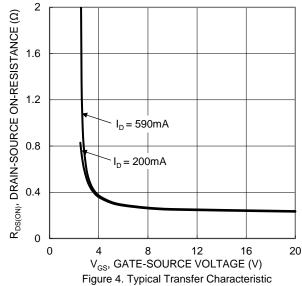


Figure 5. Typical On-Resistance vs. Drain Current and Junction Temperature





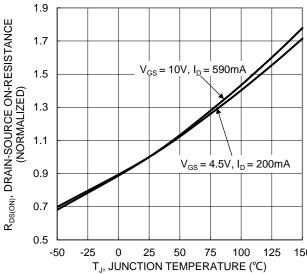


Figure 6. On-Resistance Variation with Junction Temperature



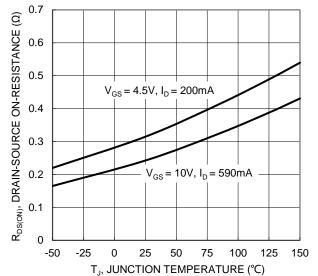


Figure 7. On-Resistance Variation with Junction Temperature

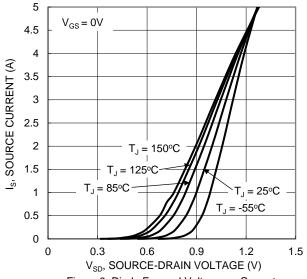


Figure 9. Diode Forward Voltage vs. Current

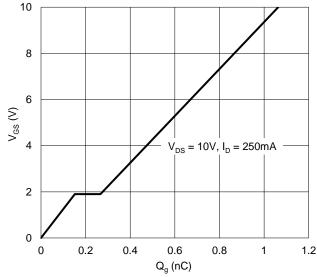


Figure 11. Gate Charge

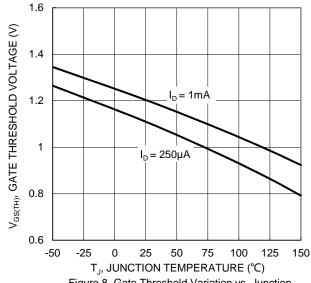
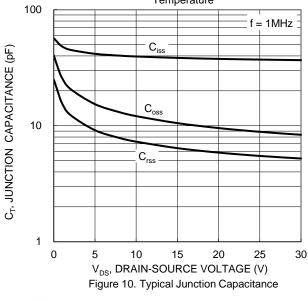
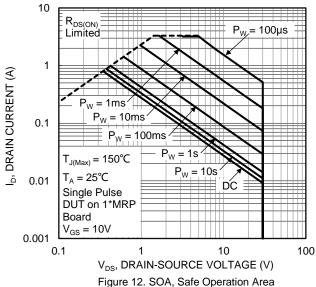


Figure 8. Gate Threshold Variation vs. Junction Temperature







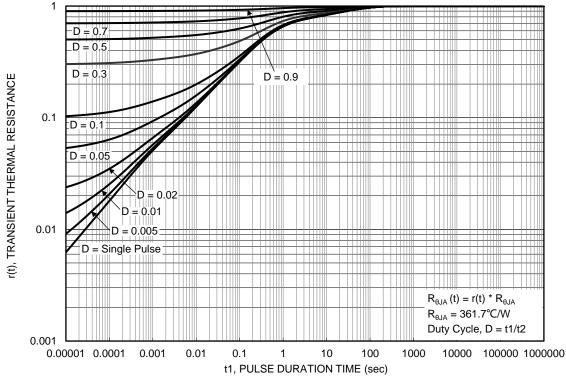


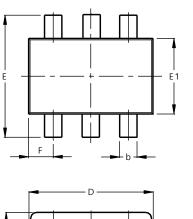
Figure 13. Transient Thermal Resistance

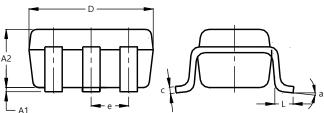


# **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

### **SOT363**



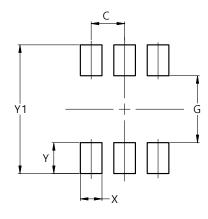


SOT363						
Dim	Min	Max	Тур			
A1	0.00	0.10	0.05			
A2	0.90	1.00	0.95			
b	0.10	0.30	0.25			
C	0.10	0.22	0.11			
D	1.80	2.20	2.15			
Е	2.00	2.20	2.10			
E1	1.15	1.35	1.30			
е	C	.650 E	SC			
F	0.40	0.45	0.425			
L	0.25	0.40	0.30			
а	0°	8°				
All Dimensions in mm						

# **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.

### **SOT363**



Dimensions	Value (in mm)
C	0.650
G	1.300
Х	0.420
Y	0.600
Y1	2.500



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