



N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{DSS}	Rds(on)	I _D T _A = +25°C
25V	4Ω @ V _{GS} = 4.5V	0.3A
237	5Ω @ V _{GS} = 2.7V	0.27A

Description

This new generation MOSFET is designed to minimize the on-state resistance (R_{DS(ON)}) yet maintain superior switching performance, making it ideal for high-efficiency power-management applications.

Applications

- DC-DC converters
- Power-management functions
- Battery operated systems and solid-state relays
- Drivers: relays, solenoids, lamps, hammers, displays, memories, transistors, etc.

Features

- Low On-Resistance
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Small Surface-Mount Package
- · ESD Protected Gate
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please contact us or your local Diodes representative.

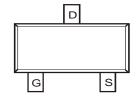
https://www.diodes.com/quality/product-definitions/

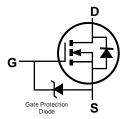
Mechanical Data

- Package: SOT23
- Package Material: Molded Plastic. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Solderable per MIL-STD-202, Method 208@3
- Terminal Connections: See Diagram
- Weight: 0.008 grams (Approximate)









Top View

Top View Pin Configuration

Equivalent Circuit

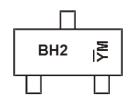
Ordering Information (Note 4)

Part Number	Daakana	Packing		
Part Number	Package	Qty.	Carrier	
DMN34D0U-7	SOT23	3,000	Tape & Reel	
DMN34D0U-13	SOT23	10,000	Tape & Reel	

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.

- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



BH2 = Product Type Marking Code \overline{Y} M = Date Code Marking \overline{Y} = Year (ex: K = 2023)

M = Month (ex: 9 = September)

Date Code Key

Year	2023	2024	2025	2026	2027	2028	2029	2030	2031	2032	2033	2034
Code	K	L	М	N	Р	R	S	Т	U	V	W	Χ
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec



Maximum Ratings (@ $T_A = +25$ °C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit		
Drain-Source Voltage	VDSS	25	V		
Gate-Source Voltage			Vgss	8	V
Continuous Drain Current (Note 6) V _{GS} = 4.5V	lD	0.3 0.24	А		
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)	I _{DM}	1.8	Α		
Maximum Body Diode Continuous Current (Note 6)			Is	0.56	Α

Thermal Characteristics

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)		PD	0.44	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	RθJA	283	°C/W
Total Power Dissipation (Note 6)		P _D	0.56	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	RθJA	222	°C/W
Operating and Storage Temperature Range		TJ, TSTG	-55 to +150	°C

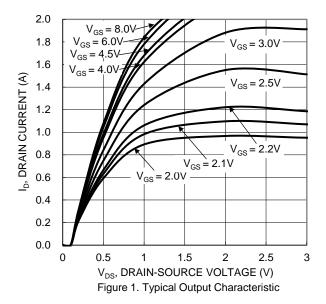
Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

					1	1
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV _{DSS}	25	_	_	V	$V_{GS} = 0V, I_D = 250\mu A$
Zero Gate Voltage Drain Current	IDSS		_	1.0	μA	V _{DS} = 20V, V _{GS} = 0V
Gate-Body Leakage	Igss	_	_	100	nA	$V_{GS} = 8V$, $V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	Vgs(TH)	0.7	_	1.1	V	$V_{DS} = V_{GS}$, $I_D = 250\mu A$
Static Drain-Source On-Resistance	Descara	_	_	4	Ω	V _G S = 4.5V, I _D = 0.4A
Static Dialii-Source Oil-Resistance	RDS(ON)		_	5	Ω	$V_{GS} = 2.7V, I_{D} = 0.2A$
Diode Forward Voltage	VsD	_	0.7	1.2	V	V _G S = 0V, I _S = 0.29A
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	Ciss	_	24	_		
Output Capacitance	Coss	_	8.2	_	n- I	$V_{DS} = 10V, V_{GS} = 0V,$ f = 1.0MHz
Reverse Transfer Capacitance	C _{rss}	_	3.8	_		1 = 1.0WH 12
Gate Resistance	Rg		377	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$
Total Gate Charge	Qg	_	0.4	_		45777
Gate-Source Charge	Q _{gs}	_	0.1	_	nC	$V_{GS} = 4.5V, V_{DS} = 5V,$ $I_{D} = 0.2A$
Gate-Drain Charge	Qgd	_	0.1	_		ID = 0.2A
Turn-On Delay Time	t _D (ON)		3.3	_		
Turn-On Rise Time	t _R	_	16	_	no	$V_{GS} = 4.5V, V_{DS} = 6V$
Turn-Off Delay Time	tD(OFF)	_	24		ns	$I_D = 0.5A$, $R_G = 50\Omega$
Turn-Off Fall Time	tF	_	14	_		

Notes:

- 5. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
- 6. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
- 7. Short duration pulse test used to minimize self-heating effect.
- 8. Guaranteed by design. Not subject to production testing.





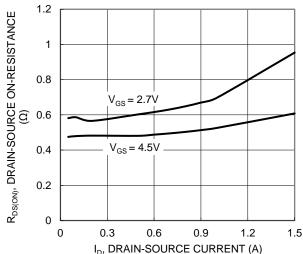


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

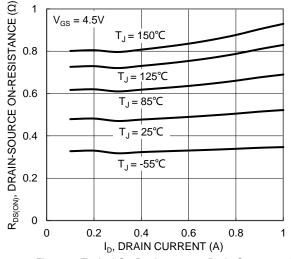


Figure 5. Typical On-Resistance vs. Drain Current and Junction Temperature

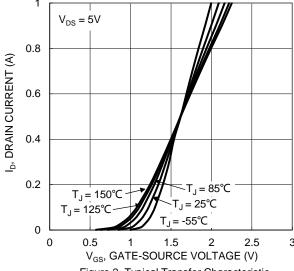
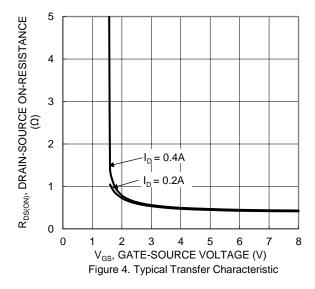


Figure 2. Typical Transfer Characteristic



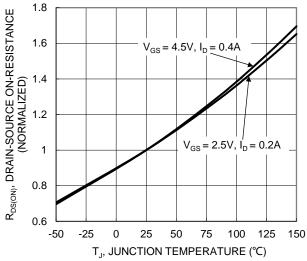


Figure 6. On-Resistance Variation with Junction Temperature



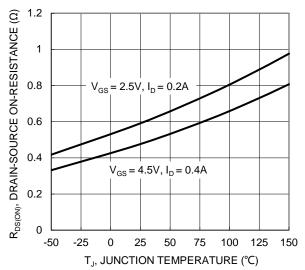


Figure 7. On-Resistance Variation with Junction Temperature

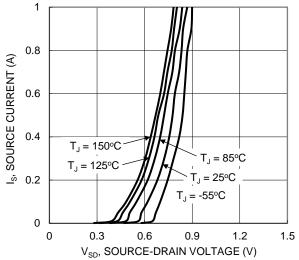


Figure 9. Diode Forward Voltage vs. Current

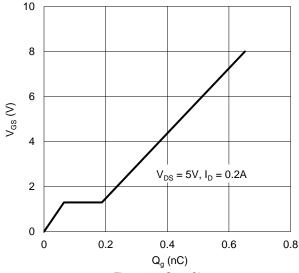


Figure 11. Gate Charge

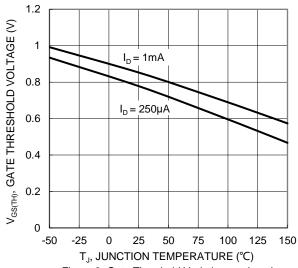
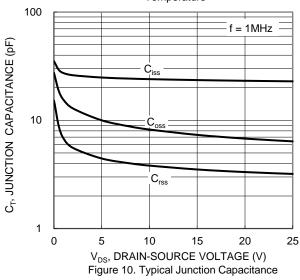
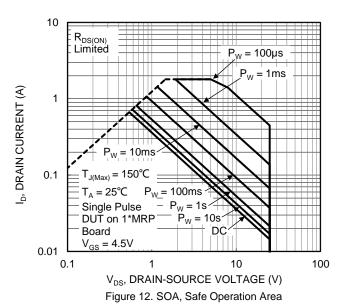


Figure 8. Gate Threshold Variation vs. Junction Temperature







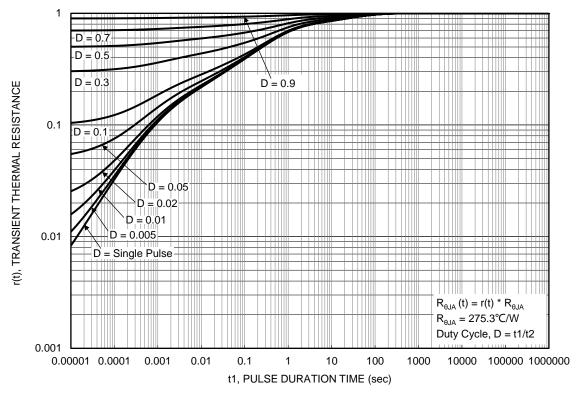


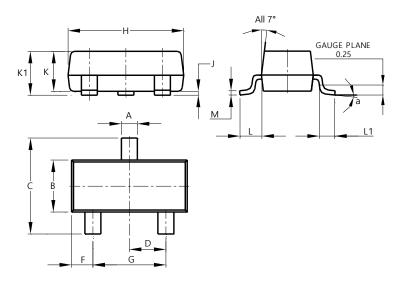
Figure 13. Transient Thermal Resistance



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT23

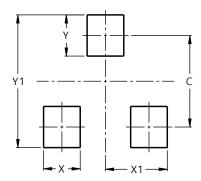


SOT23						
Dim	Min	Max	Тур			
Α	0.37	0.51	0.40			
В	1.20	1.40	1.30			
C	2.30	2.50	2.40			
D	0.89	1.03	0.915			
F	0.45	0.60	0.535			
G	1.78	2.05	1.83			
Н	2.80	3.00	2.90			
J	0.013	0.10	0.05			
K	0.890	1.00	0.975			
K 1	0.903	1.10	1.025			
٦	0.45	0.61	0.55			
L1	0.25	0.55	0.40			
М	0.085	0.150	0.110			
а	0°	8°				
All	Dimens	ions in	mm			

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT23



Dimensions	Value (in mm)
С	2.0
Х	0.8
X1	1.35
Υ	0.9
Y1	2.9



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