



Product Summary

BV _{DSS}	Rds(on) max	ID TA = +25°C		
401/	24mΩ @V _{GS} = 10V	9.0A		
40V	32mΩ @V _{GS} = 4.5V	7.8A		

Description and Applications

This MOSFET is designed to meet the stringent requirements of automotive applications. It is qualified to AEC-Q101, supported by a PPAP and is ideal for use in:

- Motor controls
- Backlighting
- Power-management functions
- DC-DC converters

40V DUAL N-CHANNEL ENHANCEMENT MODE MOSFET

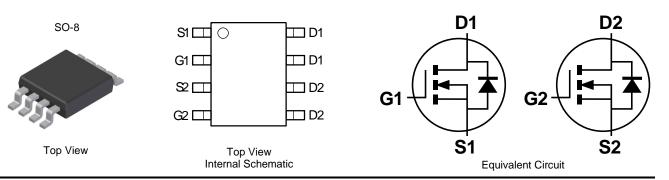
Features and Benefits

- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- The DMN4026SSDQ is suitable for automotive applications requiring specific change control; this part is AEC-Q101 qualified, PPAP capable, and manufactured in IATF 16949 certified facilities.

https://www.diodes.com/quality/product-definitions/

Mechanical Data

- Package: SO-8
- Package Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections Indicator: See diagram below
- Terminals: Finish Matte Tin Annealed over Copper Lead Frame. Solderable per MIL-STD-202, Method 208 3
- Weight: 0.074 grams (Approximate)



Ordering Information (Note 4)

Part Number	Package	Packing	
DMN4026SSDQ-13	SO 9	Qty.	Carrier
	50-8	2,500	Reel

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.

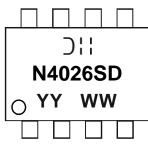
2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information

Notes:



)'' = Manufacturer's Marking
N4026SD = Product Type Marking Code
YYWW = Date Code Marking
YY = Year (ex: 23 = 2023)
WW = Week (01 to 53)



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit		
Drain-Source Voltage	Vdss	40	V		
Gate-Source Voltage			Vgss	±20	V
Continuous Drain Current (Note 6) V _{GS} = 10V	Steady State	T _A = +25°C T _A = +70°C	ID	7.0 5.6	А
	t < 10s	T _A = +25°C T _A = +70°C	ld	9.0 7.2	А
Maximum Continuous Body Diode Forward Current (Note 6)			ls	7.0	А
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)			I _{DM}	70	А

Thermal Characteristics

Characteristic	Symbol	Value	Unit		
Total Bower Dissignation (Note 5)	T _A = +25°C	D	1.3	W	
Total Power Dissipation (Note 5)	T _A = +70°C	PD	0.8		
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	Davi	98	°C/W	
Thermal Resistance, Junction to Ambient (Note 5)	t < 10s	Reja	59	C/W	
Total Power Dissipation (Note 6)	T _A = +25°C	PD	1.8	W	
Total Fower Dissipation (Note 0)	T _A = +70°C	FD	1.1		
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	Davi	71	°C/W	
Thermal resistance, sunction to Amblent (Note 6)	t < 10s	Røja	43		
Thermal Resistance, Junction to Case (Note 6)	Rejc	11.8			
Operating and Storage Temperature Range		T _{J,} T _{STG}	-55 to +150	°C	

Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

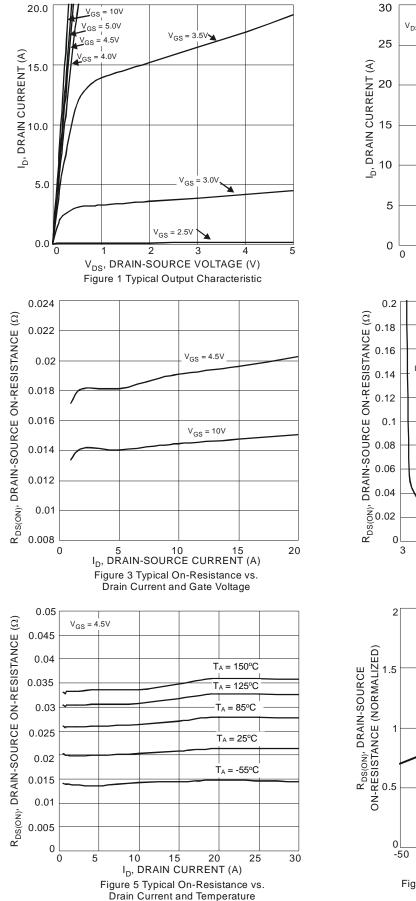
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV _{DSS}	40		_	V	$V_{GS} = 0V, I_D = 250 \mu A$
Zero Gate Voltage Drain Current	I _{DSS}	_		1	μA	$V_{DS} = 40V, V_{GS} = 0V$
Gate-Source Leakage	lgss	_		±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)			•	•	•	
Gate Threshold Voltage	VGS(TH)	1		3	V	$V_{DS} = V_{GS}$, $I_D = 250 \mu A$
Static Drain-Source On-Resistance	Deserve	_	15	24	mΩ	$V_{GS} = 10V, I_D = 6A$
Static Drain-Source On-Resistance	R _{DS(ON)}	_	20	32	11122	V _{GS} = 4.5V, I _D = 5A
Diode Forward Voltage	Vsd	_	0.7	1.0	V	V _{GS} = 0V, I _S = 1.0A
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	Ciss	_	1060	—		$V_{DS} = 20V, V_{GS} = 0V,$ f = 1.0MHz
Output Capacitance	Coss	_	84	—	pF	
Reverse Transfer Capacitance	Crss	_	58	_		
Gate Resistance	Rg	_	1.6	—	Ω	V _{DS} = 0V, V _{GS} = 0V, f = 1.0MHz
Total Gate Charge (V _{GS} = 4.5V)	Qg	_	8.8	20		
Total Gate Charge (V _{GS} = 10V)	Qg	_	19.1	43	nC	V _{DS} = 20V, I _D = 8A
Gate-Source Charge	Q _{gs}	_	3.0	7.5	nc	
Gate-Drain Charge	Q _{gd}	_	2.5	6		
Turn-On Delay Time	tD(ON)	_	5.3	_		
Turn-On Rise Time	tR	_	7.1	_		$V_{DD} = 25V, R_L = 2.5\Omega$ $V_{GS} = 10V, R_G = 3\Omega$
Turn-Off Delay Time	tD(OFF)	_	15.1	—	ns	
Turn-Off Fall Time	tF	_	4.8	—	1	
Body Diode Reverse Recovery Time	trr	_	10.5	—	ns	I _F = 8A, di/dt = 100A/µs
Body Diode Reverse Recovery Charge	Qrr	_	4.15	_	nC	IF = 8A, di/dt = 100A/µs

Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
 Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
 Short duration pulse test used to minimize self-heating effect.

8. Guaranteed by design. Not subject to product testing.

Notes:





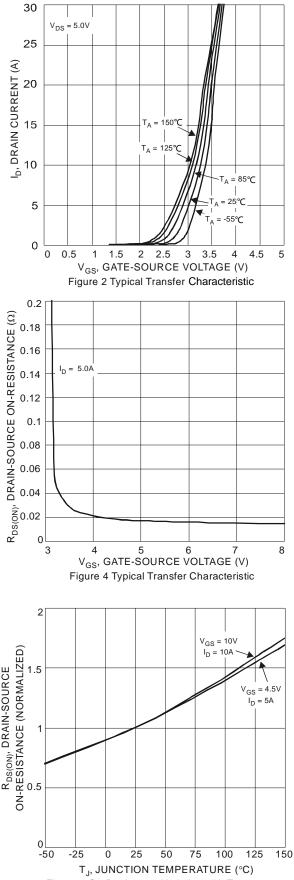
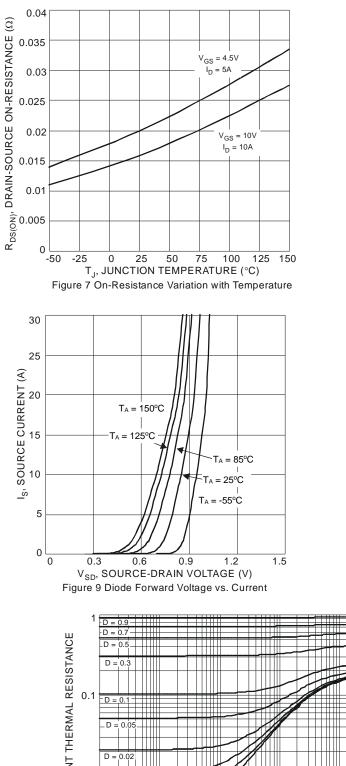


Figure 6 On-Resistance Variation with Temperature





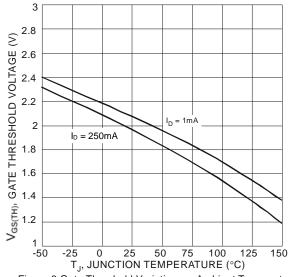
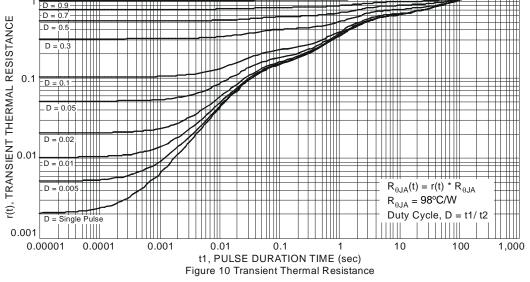


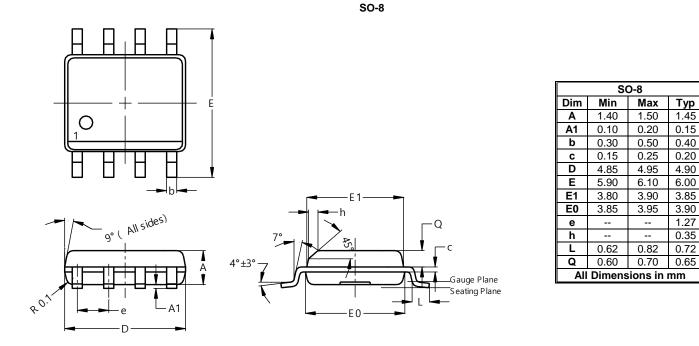
Figure 8 Gate Threshold Variation vs. Ambient Temperature





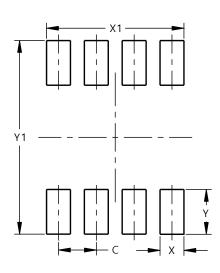
Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.



Suggested Pad Layout

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Dimensions	Value (in mm)
С	1.27
Х	0.802
X1	4.612
Y	1.505
Y1	6.50

SO-8



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