



DMN63D1LVQ

Product Summary

BV _{DSS}	Rds(on) Max	I _D Max T _A = +25°C
601/	2Ω @ Vgs = 10V	477mA
60V	3Ω @ V _{GS} = 5V	401mA

Description and Applications

This MOSFET is designed to meet the stringent requirements of automotive applications. It is qualified to AEC-Q101, supported by a PPAP and is ideal for use in:

- Motor controls
- Power-management functions
- Backlighting

DUAL N-CHANNEL ENHANCEMENT MODE MOSFET

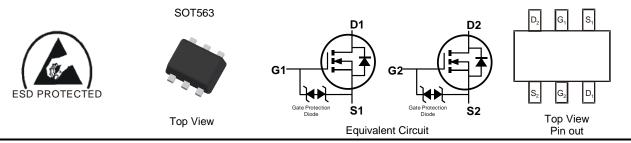
Features and Benefits

- Dual N-Channel MOSFET
- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- ESD Protected
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen Antimony Free. "Green" Device (Note 3)
- The DMN63D1LVQ is suitable for automotive applications requiring specific change control; this part is AEC-Q101 qualified, PPAP capable, and manufactured in IATF 16949 certified facilities.

https://www.diodes.com/quality/product-definitions/

Mechanical Data

- Package: SOT563
- Package Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Terminals: Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.006 grams (Approximate)



Ordering Information (Note 4)

Part Number	Paakaga	Packing			
Fart Number	Package	Qty.	Carrier		
DMN63D1LVQ-7	SOT563	3,000	Reel		
DMN63D1LVQ-13	SOT563	10,000	Reel		

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.

 See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information

Date Code Key		[D63	$\overline{Y}M$	Ϋ́M Ϋ́=	= Product = Date Coo Year (ex: K Month (ex:	de Marking (= 2023)	0				
Year	2023	2024	2025	2026	2027	2028	2029	2030	2031	2032	2033	2034
Code	K	L	М	N	Р	R	S	Т	U	V	W	Х
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D
					1	of 7						May 2023

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Maximum Ratings (@TA = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit	
Drain-Source Voltage	Vdss	60	V		
Gate-Source Voltage	Vgss	±20	V		
Continuous Drain Current (Note 6) V _{GS} = 10V		T _A = +25°C T _A = +70°C	ID	477 381	mA
Maximum Continuous Body Diode Forward Curren	t (Note 6)	Is	477	mA	
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%	%)	Ідм	0.9	А	

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Total Power Dissipation (Note 5)		PD	450	mW
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	R _{0JA}	283	°C/W
Total Power Dissipation (Note 6)		PD	940	mW
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	Reja	133	°C/W
Operating and Storage Temperature Range		TJ, TSTG	-55 to +150	°C

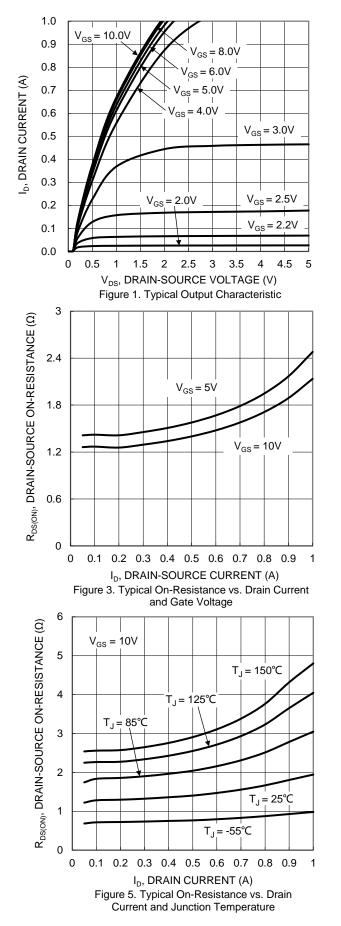
Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

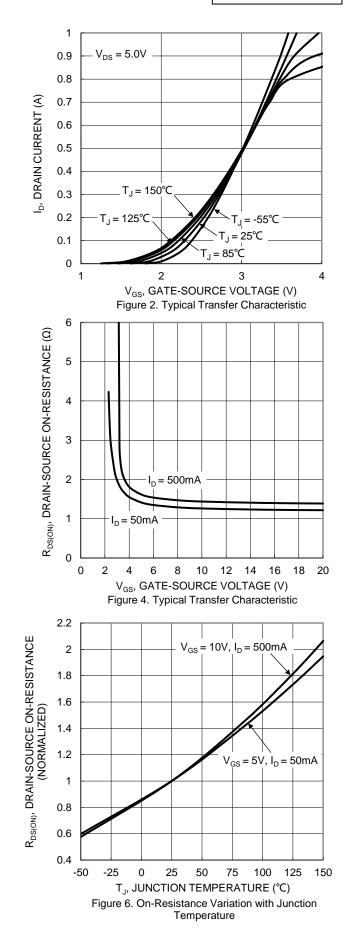
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)					•	
Drain-Source Breakdown Voltage	BVDSS	60	—	—	V	$V_{GS} = 0V, I_{D} = 10\mu A$
Zero Gate Voltage Drain Current	IDSS	_	—	1.0	μA	$V_{DS} = 60V, V_{GS} = 0V$
Gate-Source Leakage	lgss	_	_	±10	μA	$V_{GS} = \pm 20V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	VGS(TH)	1.0	_	2.5	V	$V_{DS} = 10V, I_D = 1mA$
Static Drain-Source On-Resistance	Proven		1.4	2.0	Ω	$V_{GS} = 10V, I_D = 500mA$
Static Drain-Source On-Resistance	R _{DS(ON)}	_	1.5	3.0	12	$V_{GS} = 5V, I_D = 50mA$
Diode Forward Voltage	Vsd		0.8	1.2	V	V _{GS} = 0V, I _S = 115mA
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	Ciss	_	41	—	pF	
Output Capacitance	Coss	_	4.5	—	pF	V _{DS} = 30V, V _{GS} = 0V f = 1.0MHz
Reverse Transfer Capacitance	Crss		2.7	—	pF	1 = 1.00012
Gate Resistance	Rg		224	—	Ω	$f = 1MHz$, $V_{GS} = 0V$, $V_{DS} = 0V$
Total Gate Charge (V _{GS} = 4.5V)	Qg	—	0.51	—	nC	
Total Gate Charge (V _{GS} = 10V)	Qgs	_	1.04	—	nC	V _{DS} = 15V,
Gate-Source Charge	Qgs	—	0.16	—	nC	I _D = 200mA
Gate-Drain Charge	Qgd		0.18	—	nC	
Turn-On Delay Time	tD(ON)		6.9	—	ns	
Turn-On Rise Time	t _R		5.8	—	ns	$V_{DD} = 30V, V_{GS} = 10V,$
Turn-Off Delay Time	tD(OFF)		37.8	—	ns	R _G = 150Ω, I _D = 200mA
Turn-Off Fall Time	tF	_	14.3	—	ns	

 Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
Short duration pulse test used to minimize self-heating effect.
Guaranteed by design. Not subject to product testing. Notes:



DMN63D1LVQ

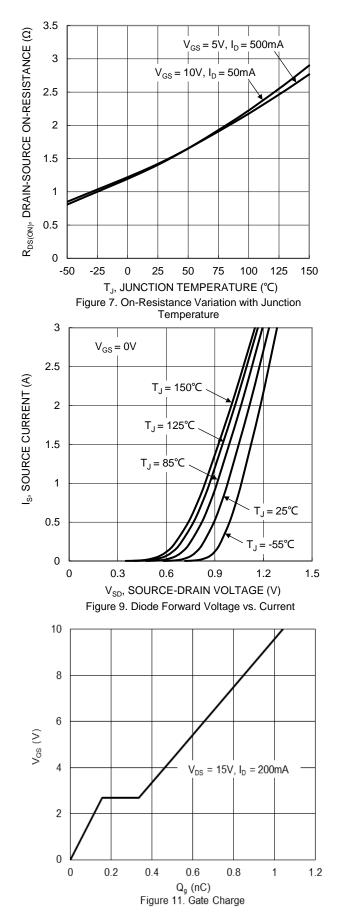


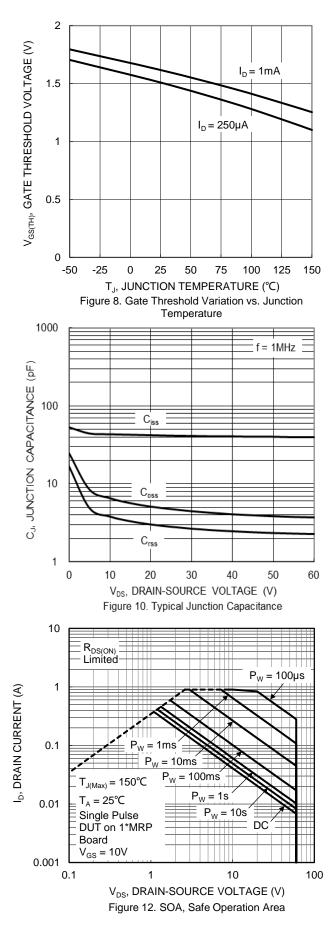


DMN63D1LVQ Document number: DS45181 Rev. 2 - 2



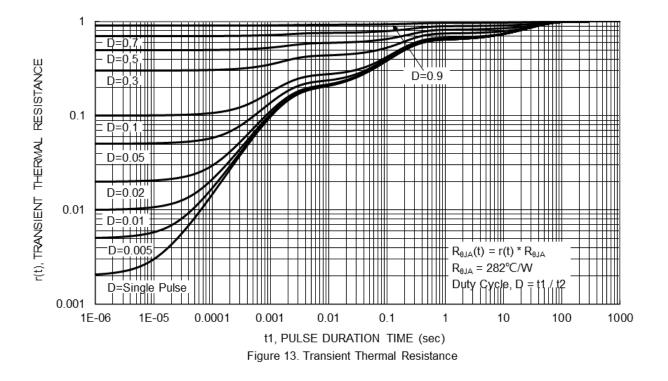
DMN63D1LVQ





DMN63D1LVQ Document number: DS45181 Rev. 2 - 2 4 of 7 www.diodes.com

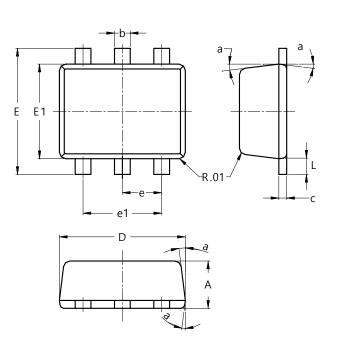






Package Outline Dimensions

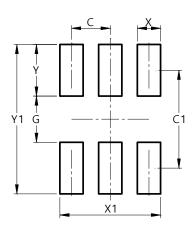
Please see http://www.diodes.com/package-outlines.html for the latest version.



SOT563							
Dim	Min	Max	Тур				
Α	0.55	0.60					
b	0.15	0.30	0.20				
C	0.10	0.18	0.11				
D	1.50	1.70	1.60				
Е	1.55	1.70	1.60				
E1	1.10	1.25	1.20				
е			0.50				
e1	0.90	1.10	1.00				
L	0.10	0.30	0.20				
а	8°	9°	7°				
All Dimensions in mm							

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



Dimensions	Value (in mm)
С	0.500
C1	1.270
G	0.600
Х	0.300
X1	1.300
Y	0.670
Y1	1.940

SOT563

SOT563



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