

## Product Summary

BV <sub>DSS</sub>	R <sub>DS(ON)</sub>	Package	I <sub>D</sub> T <sub>C</sub> = +25°C
800V	2.0Ω@V <sub>GS</sub> = 10V	ITO220AB (Type TH)	7A

## Description

This new generation MOSFET features low on-resistance and fast switching, making it ideal for high efficiency power management applications.

## Applications

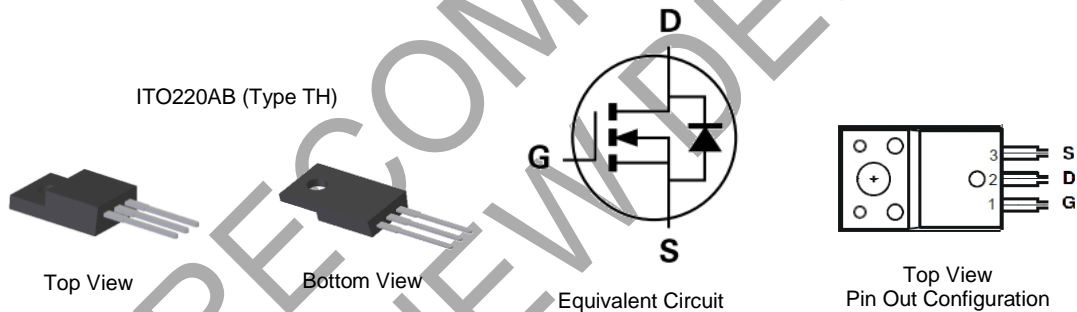
- Motor controls
- Backlighting
- DC-DC converters
- Power management functions

## Features

- Low Input Capacitance
- High BV<sub>DSS</sub> Rating for Power Application
- Low Input/Output Leakage
- **Lead-Free Finish; RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please [contact us](mailto:contact@diodes.com) or your local Diodes representative.**  
<https://www.diodes.com/quality/product-definitions/>

## Mechanical Data

- Package: ITO220AB
- Package Material: Molded Plastic, "Green" Molding Compound, UL Flammability Classification Rating 94V-0
- Terminals: Matte Tin Finish Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (E3)
- Terminal Connections: See Diagram Below
- Weight: 1.85 grams (Approximate)

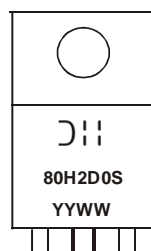


## Ordering Information (Note 4)

Part Number	Package	Packing	
		Qty.	Carrier
DMN80H2D0SCTI	ITO220AB (Type TH)	50 Pieces	Tube

- Notes:
1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
  2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
  3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
  4. For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

## Marking Information



D||| = Manufacturer's Marking  
 80H2D0S = Product Type Marking Code  
 YYWW = Date Code Marking  
 YY or YY = Last Two Digits of Year (ex: 22 = 2022)  
 WW or WW = Week Code (01 to 53)

**Maximum Ratings** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Drain-Source Voltage	V <sub>DSS</sub>	800	V	
Gate-Source Voltage	V <sub>GSS</sub>	±30	V	
Continuous Drain Current (Note 9), V <sub>GS</sub> = 10V	Steady State	T <sub>C</sub> = +25°C	7	A
		T <sub>C</sub> = +100°C	4	A
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%) (Note 9)	I <sub>DM</sub>	28	A	
Avalanche Current (Note 6)	L = 30mH	I <sub>AS</sub>	2.0	A
Avalanche Energy (Note 6)	L = 30mH	E <sub>AS</sub>	60	mJ
Peak Diode Recovery dv/dt (Note 7)	dv/dt	2.8	V/ns	

**Thermal Characteristics**

Characteristic	Symbol	Max	Unit	
Power Dissipation (Note 5)	P <sub>D</sub>	T <sub>C</sub> = +25°C	41	W
		T <sub>C</sub> = +100°C	16	W
Thermal Resistance, Junction to Ambient (Note 6)	R <sub>θJA</sub>	49.5	°C/W	
Thermal Resistance, Junction to Case (Note 5)	R <sub>θJC</sub>	3	°C/W	
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C	

**Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
<b>OFF CHARACTERISTICS (Note 8)</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	800	—	—	V	V <sub>GS</sub> = 0V, I <sub>D</sub> = 250µA
Zero Gate Voltage Drain Current T <sub>J</sub> = +25°C	I <sub>DSS</sub>	—	—	25	µA	V <sub>DS</sub> = 800V, V <sub>GS</sub> = 0V
Gate-Source Leakage	I <sub>GSS</sub>	—	—	±100	nA	V <sub>GS</sub> = ±30V, V <sub>DS</sub> = 0V
<b>ON CHARACTERISTICS (Note 8)</b>						
Gate Threshold Voltage	V <sub>GS(TH)</sub>	2.0	3.3	4.0	V	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250µA
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>	—	1.4	2.0	Ω	V <sub>GS</sub> = 10V, I <sub>D</sub> = 2.5A
Diode Forward Voltage	V <sub>SD</sub>	—	0.86	1.5	V	V <sub>GS</sub> = 0V, I <sub>S</sub> = 7.0A
<b>DYNAMIC CHARACTERISTICS (Note 6)</b>						
Input Capacitance	C <sub>iss</sub>	—	1253	—	pF	V <sub>DS</sub> = 25V, V <sub>GS</sub> = 0V, f = 1.0MHz
Output Capacitance	C <sub>oss</sub>	—	115	—		
Reverse Transfer Capacitance	C <sub>rss</sub>	—	11	—		
Gate Resistance	R <sub>g</sub>	—	1.5	—	Ω	V <sub>DS</sub> = 0V, V <sub>GS</sub> = 0V, f = 1.0MHz
Total Gate Charge	Q <sub>g</sub>	—	35.4	—	nC	V <sub>GS</sub> = 10V, V <sub>DS</sub> = 560V, I <sub>D</sub> = 7A
Gate-Source Charge	Q <sub>gs</sub>	—	5.9	—		
Gate-Drain Charge	Q <sub>gd</sub>	—	16.4	—		
Turn-On Delay Time	t <sub>D(ON)</sub>	—	20.5	—	ns	V <sub>GS</sub> = 10V, V <sub>DD</sub> = 350V, R <sub>G</sub> = 25Ω, I <sub>D</sub> = 7A
Turn-On Rise Time	t <sub>F</sub>	—	35.8	—		
Turn-Off Delay Time	t <sub>D(OFF)</sub>	—	104	—		
Turn-Off Fall Time	t <sub>F</sub>	—	42.6	—		
Body Diode Reverse Recovery Time	t <sub>RR</sub>	—	419	—	ns	dI/dt = 100A/µs, V <sub>GS</sub> = 0V, I <sub>F</sub> = 7A
Body Diode Reverse Recovery Charge	Q <sub>RR</sub>	—	4324	—	µC	

- Notes:
- Device mounted on an infinite heatsink.
  - Guaranteed by design. Not subject to production testing.
  - I<sub>SD</sub> ≤ 4.5A, di/dt ≤ 200A/µs, V<sub>DD</sub> ≤ BV<sub>DSS</sub>, starting T<sub>J</sub> = +25°C.
  - Short duration pulse test used to minimize self-heating effect.
  - Drain current limited by maximum junction temperature.

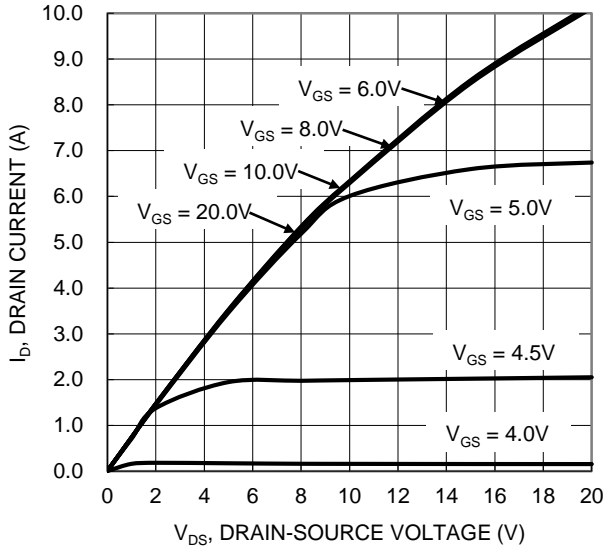


Figure 1. Typical Output Characteristic

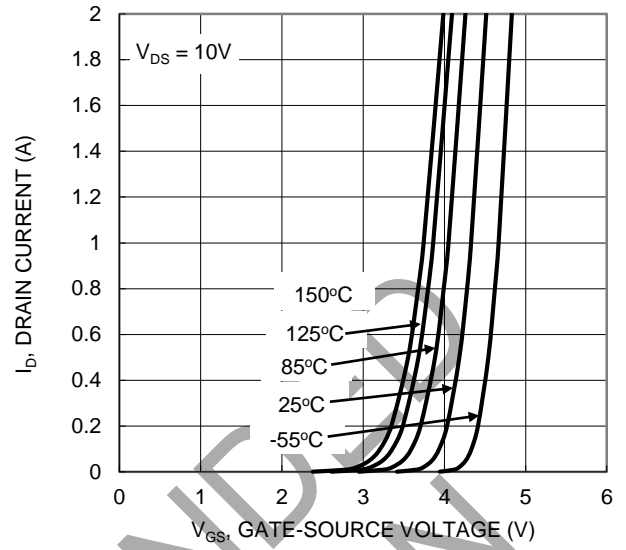


Figure 2. Typical Transfer Characteristic

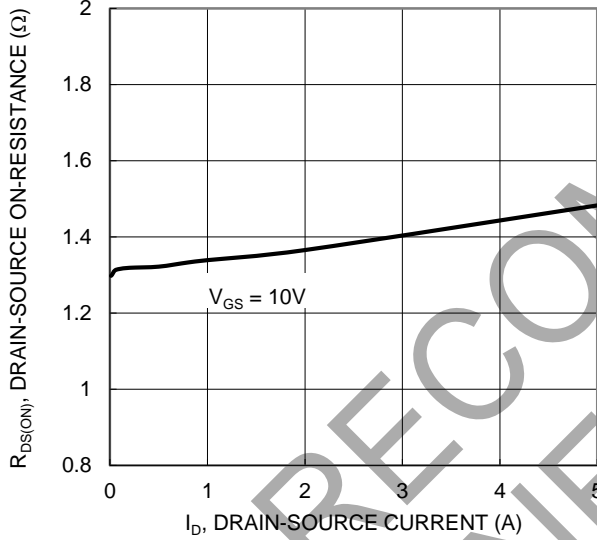


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

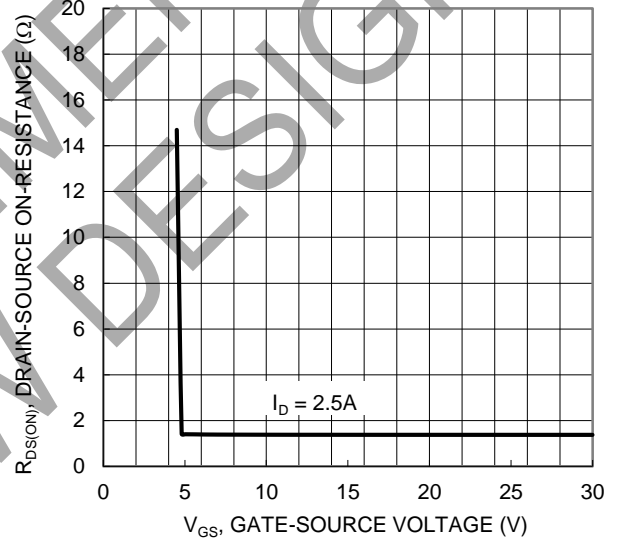


Figure 4. Typical Transfer Characteristic

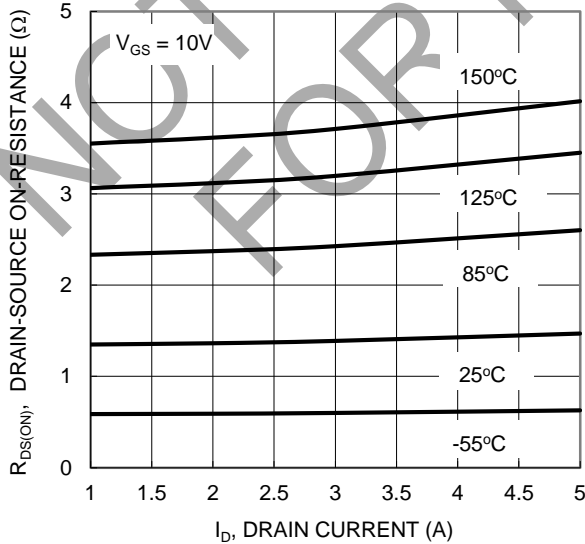


Figure 5. Typical On-Resistance vs. Drain Current and Temperature

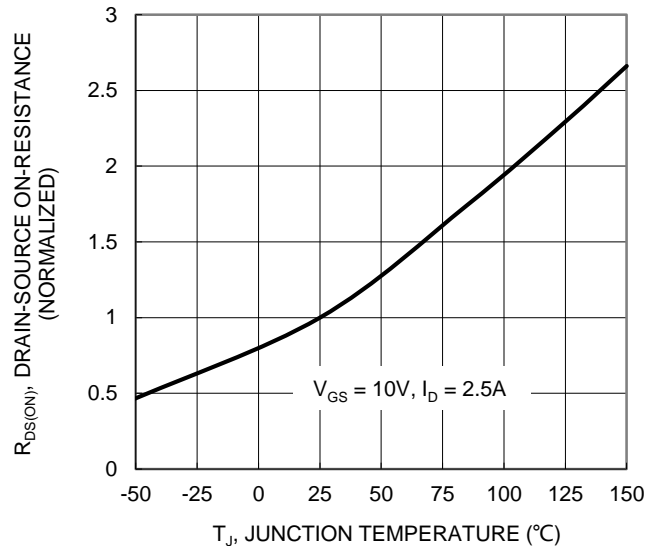


Figure 6. On-Resistance Variation with Temperature

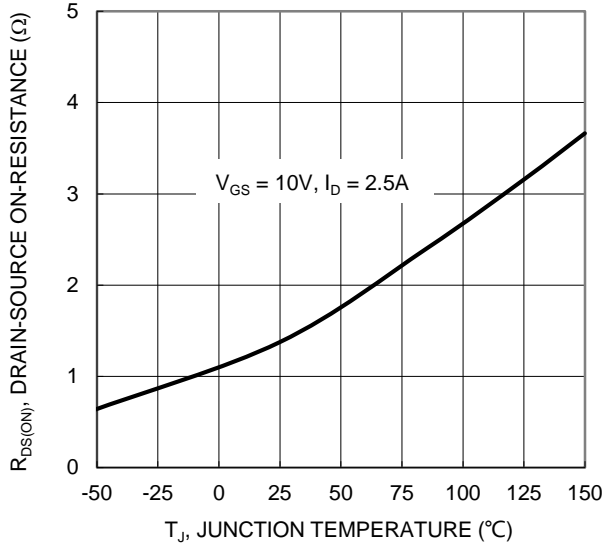


Figure 7. On-Resistance Variation with Temperature

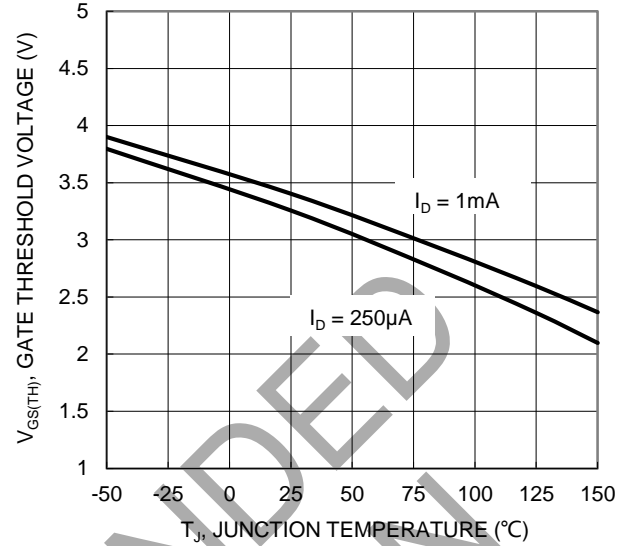


Figure 8. Gate Threshold Variation vs. Junction Temperature

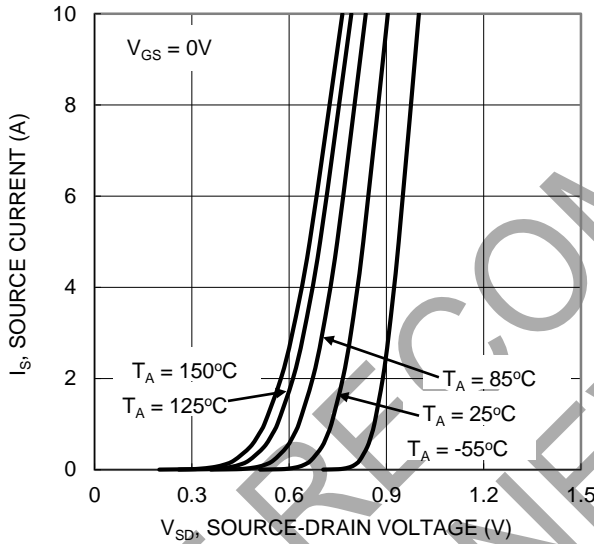


Figure 9. Diode Forward Voltage vs. Current

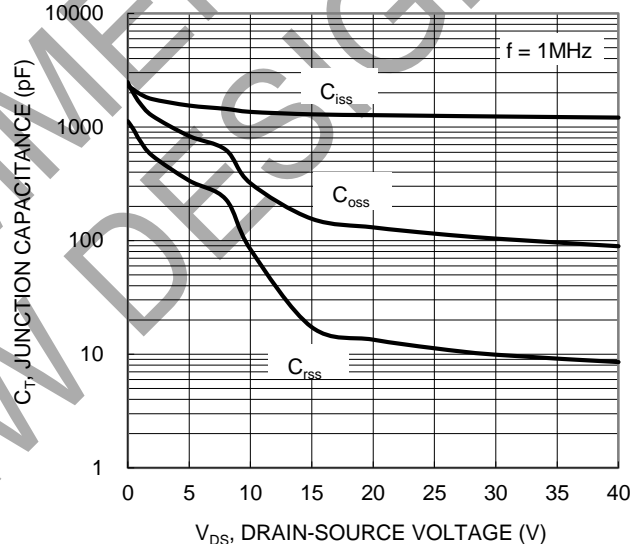


Figure 10. Typical Junction Capacitance

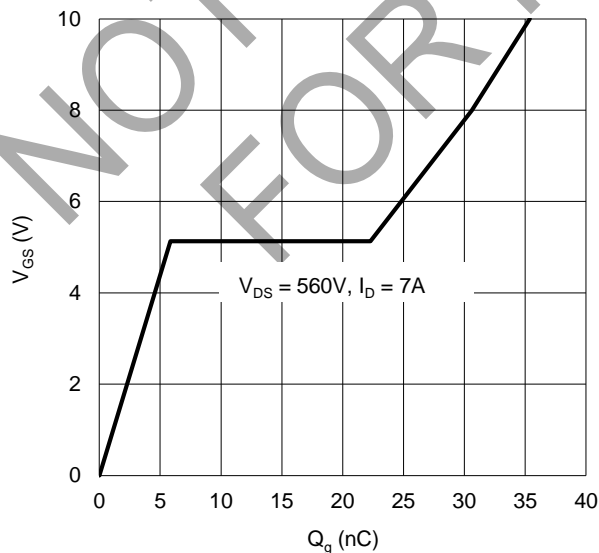


Figure 11. Gate Charge

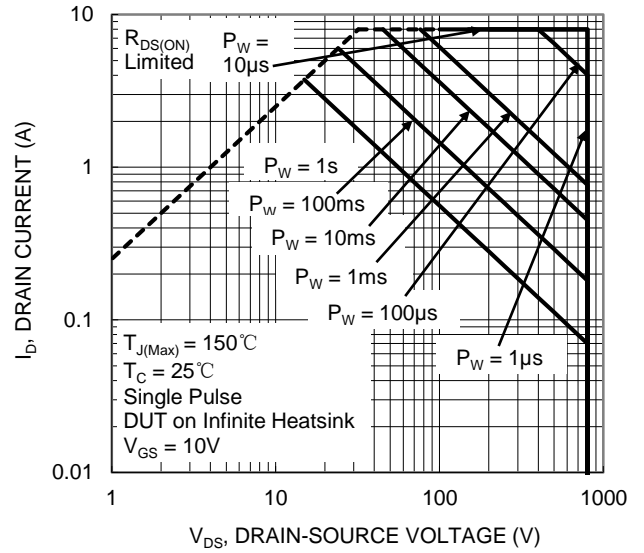


Figure 12. SOA, Safe Operation Area

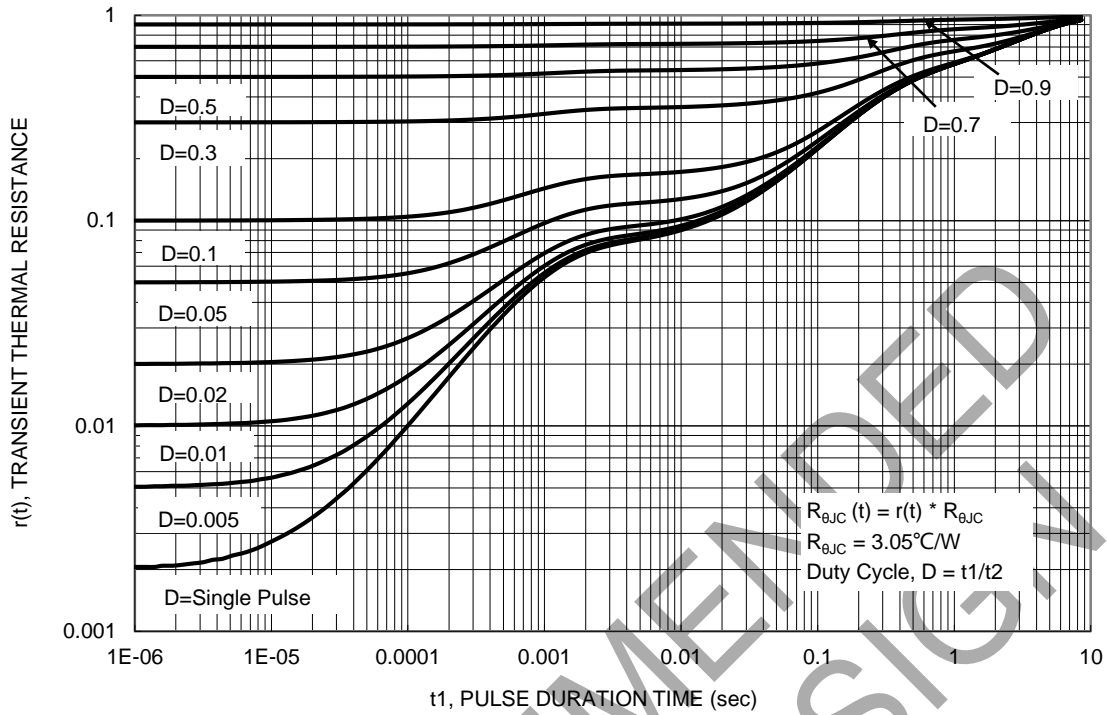


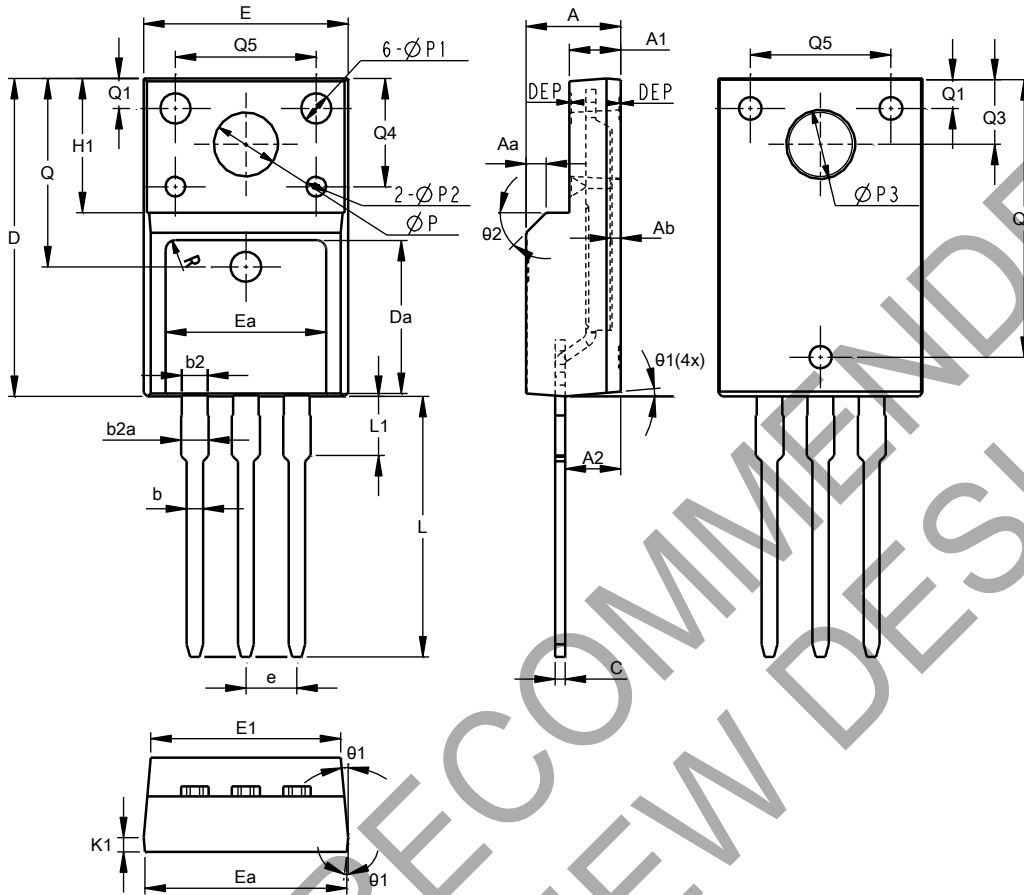
Figure 13. Transient Thermal Resistance

NOT RECOMMENDED FOR NEW DESIGN

**Package Outline Dimensions**

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

**ITO220AB (Type TH)**



ITO220AB (Type TH)			
Dim	Min	Max	Typ
A	4.50	4.90	4.70
A1	2.34	2.74	2.54
A2	2.63	2.89	2.76
Aa	1.00 REF		
Ab	0.30	0.60	0.56
b	0.75	0.90	0.80
b2	1.23	1.38	1.28
b2a	1.25	1.45	1.35
c	0.45	0.60	0.50
D	15.47	16.27	15.87
Da	7.55	8.05	7.80
e	2.54 BSC		
E	9.86	10.46	10.16
E1	9.26	9.66	9.46
Ea	7.70	8.30	8.00
Eb	9.76	10.34	10.04
H1	6.70 REF		
L	12.58	13.38	12.98
L1	2.81	3.05	2.93
K1	0.65	0.75	0.70
Q	9.40 REF		
Q1	1.00	2.00	1.50
Q2	13.50	14.30	13.90
Q3	3.15	3.45	3.30
Q4	5.15	5.65	5.40
Q5	6.70	7.30	7.00
ØP	3.06	3.40	3.18
ØP1	1.40	1.60	1.50
ØP2	0.95	1.05	1.00
ØP3	3.30	3.60	3.45
θ1	3°	7°	5°
θ2	-	45°	-
R	0.50 REF		
DEP	0.05	0.15	0.10
All Dimensions in mm			

NOT RECOMMENDED FOR NEW DESIGN

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