

## Product Summary

BV <sub>DSS</sub>	R <sub>DS(ON)</sub> Max	I <sub>D</sub> T <sub>A</sub> = +25°C
-30V	70mΩ @V <sub>GS</sub> = -10V	-3.4A
	130mΩ @V <sub>GS</sub> = -4.5V	-2.5A

## Description

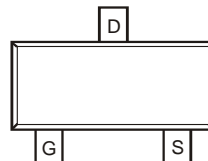
This MOSFET is designed to minimize on-state resistance (R<sub>DS(ON)</sub>) yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

## Applications

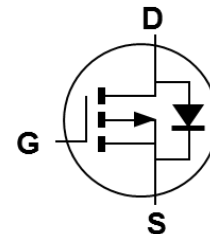
- Load switches
- Power management functions



Top View



Pin Configuration



Equivalent Circuit

## Features and Benefits

- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please [contact us](https://www.diodes.com/quality/product-definitions/) or your local Diodes representative.**  
<https://www.diodes.com/quality/product-definitions/>

## Mechanical Data

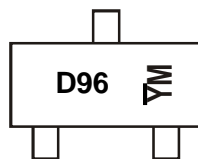
- Package: SOT23
- Package Material: Molded Plastic, "Green" Molding Compound  
UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish — Matte Tin Annealed over Copper Leadframe  
Solderable per MIL-STD-202, Method 208 **e3**
- Terminal Connections: See Diagram
- Weight: 0.008 grams (Approximate)

## Ordering Information (Note 4)

Part Number	Package	Packing	
		Qty.	Carrier
DMP3096L-7	SOT23	3,000	Tape & Reel
DMP3096L-13	SOT23	10,000	Tape & Reel

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
  2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
  3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
  4. For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

## Marking Information



D96 = Product Type Marking Code  
 YM = Date Code Marking  
 Y = Year (ex: J = 2022)  
 M = Month (ex: N = November)

### Date Code Key

Year	2022	2023	2024	2025	2026	2027	2028	2029	2030	2031	2032	2033
Code	J	K	L	M	N	O	P	R	S	T	U	V

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

**Maximum Ratings** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units	
Drain-Source Voltage	V <sub>DSS</sub>	-30	V	
Gate-Source Voltage	V <sub>GSS</sub>	±20	V	
Continuous Drain Current (Note 6) V <sub>GS</sub> = -10V	I <sub>D</sub>	Steady State T <sub>A</sub> = +25°C	-3.4	A
		T <sub>A</sub> = +70°C	-2.7	
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)	I <sub>DM</sub>	-22	A	

**Thermal Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 5)	P <sub>D</sub>	0.8	W
Thermal Resistance, Junction to Ambient @T <sub>A</sub> = +25°C (Note 5)	R <sub>θJA</sub>	158	°C/W
Power Dissipation (Note 6)	P <sub>D</sub>	1.2	W
Thermal Resistance, Junction to Ambient @T <sub>A</sub> = +25°C (Note 6)	R <sub>θJA</sub>	100	°C/W
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

**Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
<b>OFF CHARACTERISTICS (Note 7)</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	-30	—	—	V	V <sub>GS</sub> = 0V, I <sub>D</sub> = -250µA
Zero Gate Voltage Drain Current	I <sub>BSS</sub>	—	—	-800	nA	V <sub>DS</sub> = -30V, V <sub>GS</sub> = 0V
Gate-Source Leakage	I <sub>GSS</sub>	—	—	±100	nA	V <sub>GS</sub> = ±20V, V <sub>DS</sub> = 0V
<b>ON CHARACTERISTICS (Note 7)</b>						
Gate Threshold Voltage	V <sub>GS(TH)</sub>	-1.0	—	-2.1	V	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = -250µA
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>	—	48	70	mΩ	V <sub>GS</sub> = -10V, I <sub>D</sub> = -3.8A
			83	130		V <sub>GS</sub> = -4.5V, I <sub>D</sub> = -3.0A
Diode Forward Voltage	V <sub>SD</sub>	—	-0.8	-1.26	V	V <sub>GS</sub> = 0V, I <sub>S</sub> = -2.7A
<b>DYNAMIC CHARACTERISTICS (Note 8)</b>						
Input Capacitance	C <sub>ISS</sub>	—	366	—	pF	V <sub>DS</sub> = -25V, V <sub>GS</sub> = 0V, f = 1.0MHz
Output Capacitance	C <sub>OSS</sub>	—	51	—	pF	
Reverse Transfer Capacitance	C <sub>RSS</sub>	—	39	—	pF	
Gate Resistance	R <sub>G</sub>	—	9.2	—	Ω	V <sub>GS</sub> = 0V, V <sub>DS</sub> = 0V, f = 1MHz
Total Gate Charge (V <sub>GS</sub> = -4.5V)	Q <sub>g</sub>	—	3.8	—	nC	V <sub>DS</sub> = -15V, I <sub>D</sub> = -3.8A
Total Gate Charge (V <sub>GS</sub> = -10V)	Q <sub>g</sub>	—	7.5	—		
Gate-Source Charge	Q <sub>gs</sub>	—	1.0	—		
Gate-Drain Charge	Q <sub>gd</sub>	—	1.1	—		
Turn-On Delay Time	t <sub>d(on)</sub>	—	3.2	—	ns	V <sub>DS</sub> = -15V, V <sub>GS</sub> = -10V, I <sub>D</sub> = -1A, R <sub>G</sub> = 6.0Ω
Rise Time	t <sub>r</sub>	—	8.2	—		
Turn-Off Delay Time	t <sub>d(off)</sub>	—	21.7	—		
Fall Time	t <sub>f</sub>	—	13.1	—		

- Notes:
- Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
  - Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
  - Short duration pulse test used to minimize self-heating effect.
  - Guaranteed by design. Not subject to production testing.

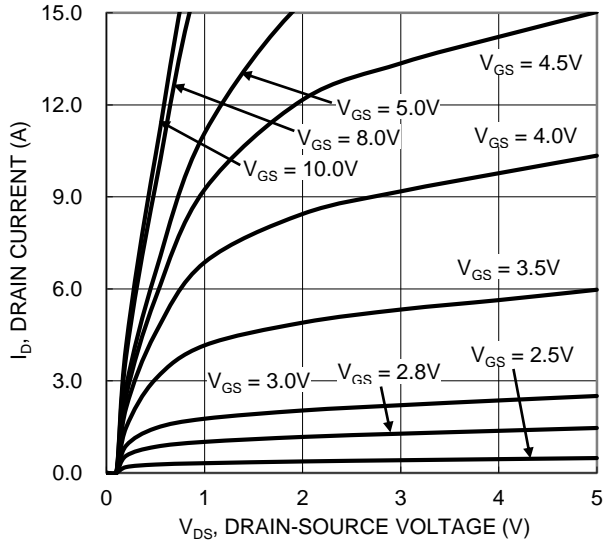


Figure 1. Typical Output Characteristic

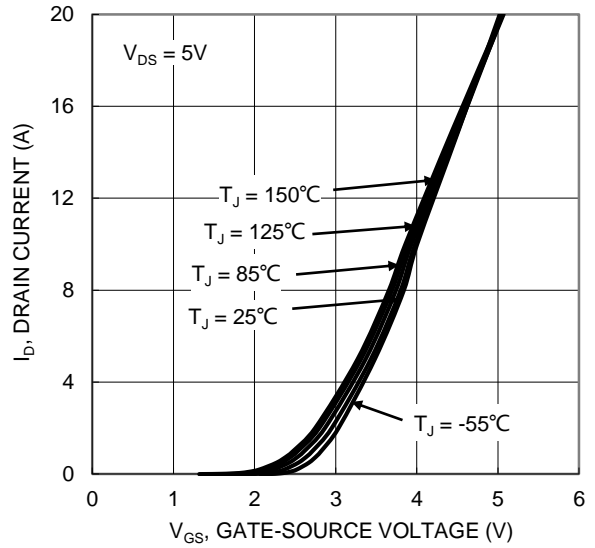


Figure 2. Typical Transfer Characteristic

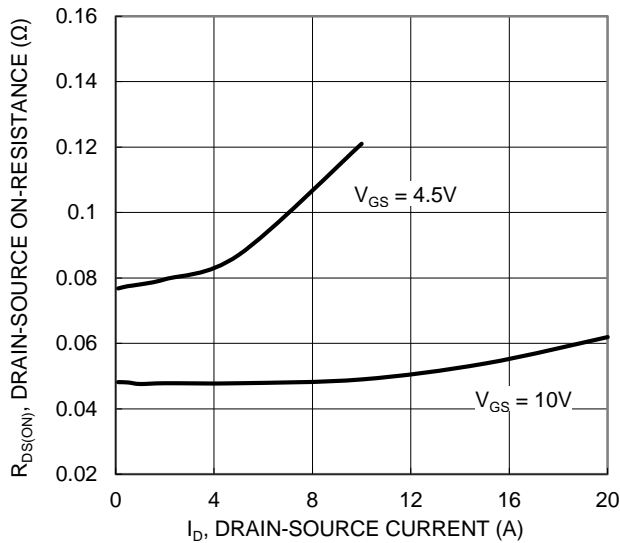


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

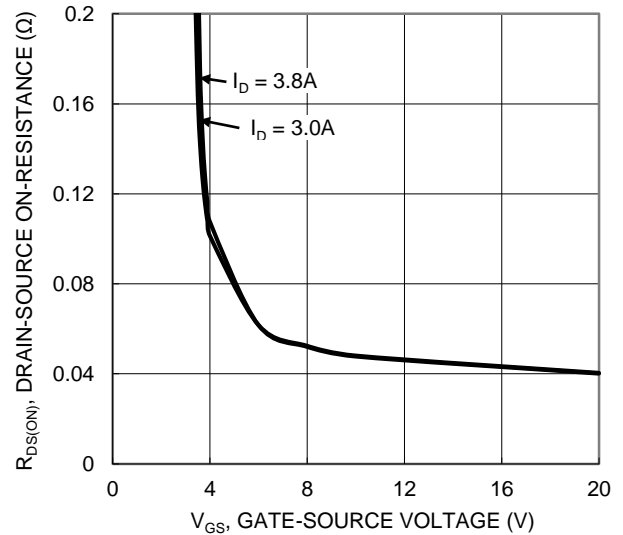


Figure 4. Typical Transfer Characteristic

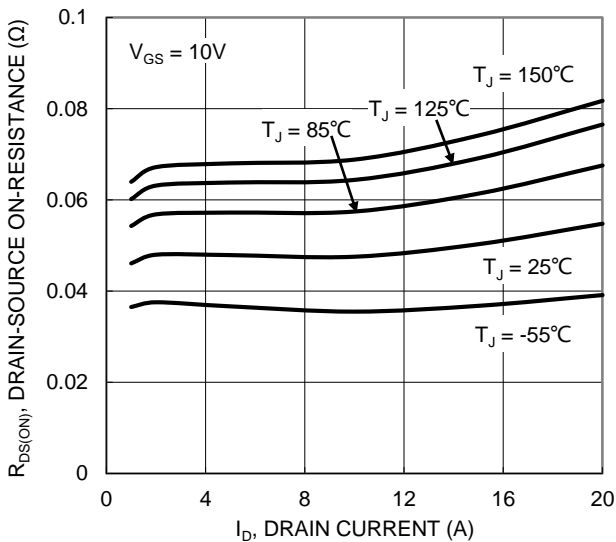


Figure 5. Typical On-Resistance vs. Drain Current and Junction Temperature

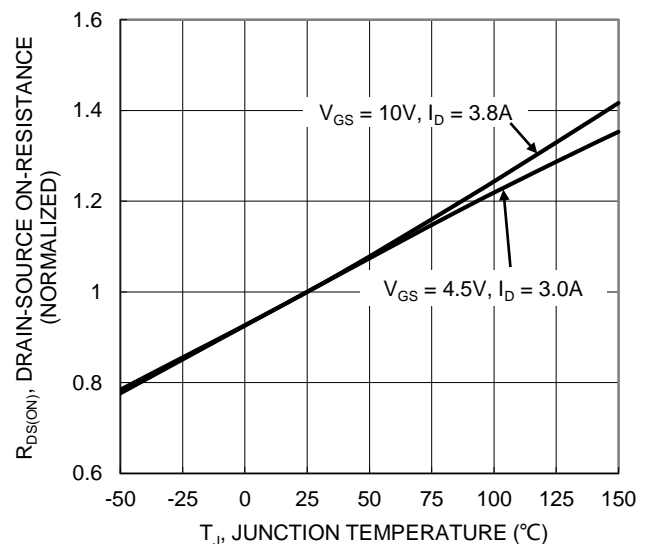


Figure 6. On-Resistance Variation with Junction Temperature

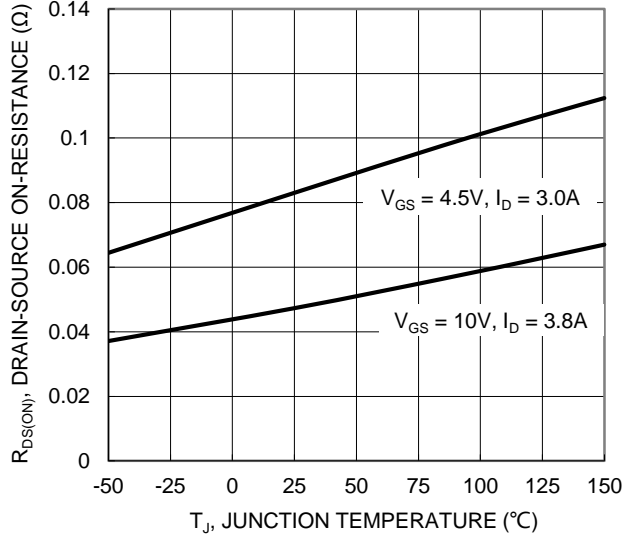


Figure 7. On-Resistance Variation with Junction Temperature

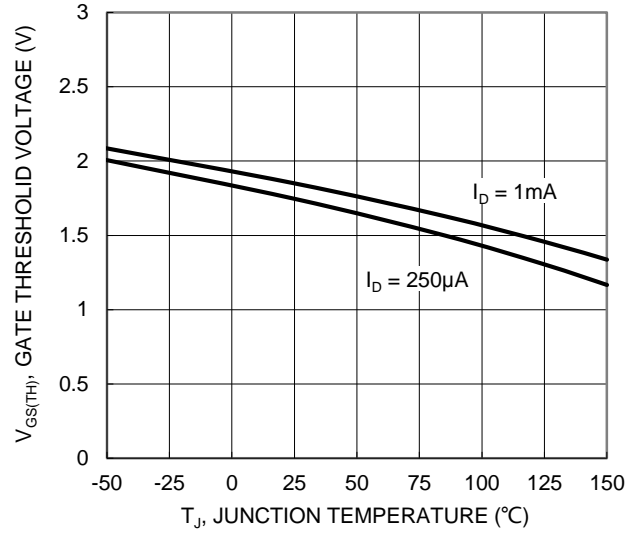


Figure 8. Gate Threshold Variation vs. Junction Temperature

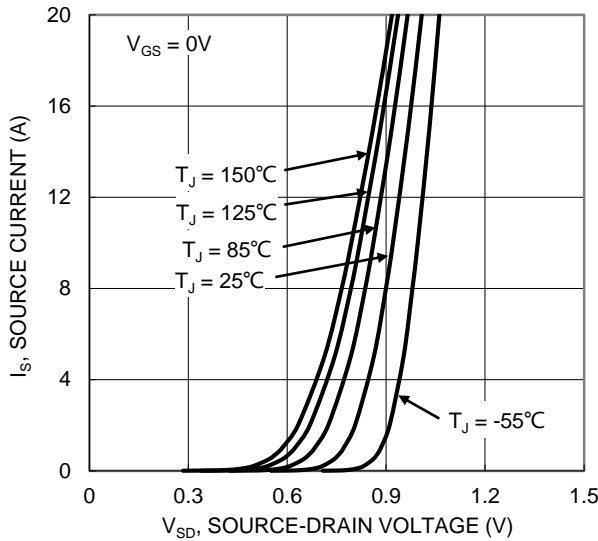


Figure 9. Diode Forward Voltage vs. Current

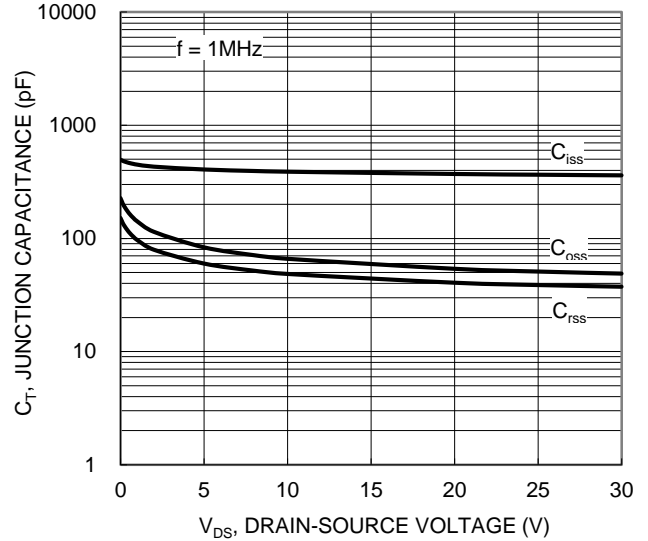


Figure 10. Typical Junction Capacitance

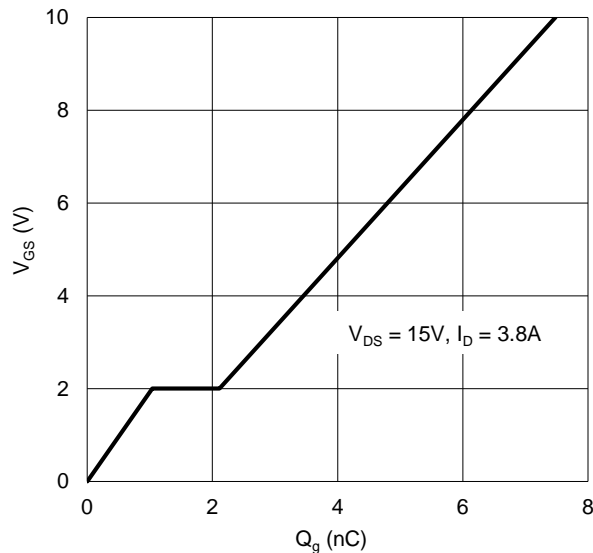


Figure 11. Gate Charge

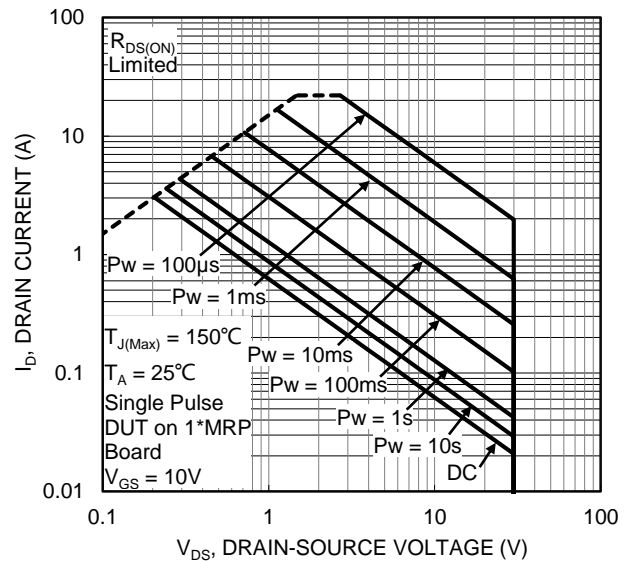
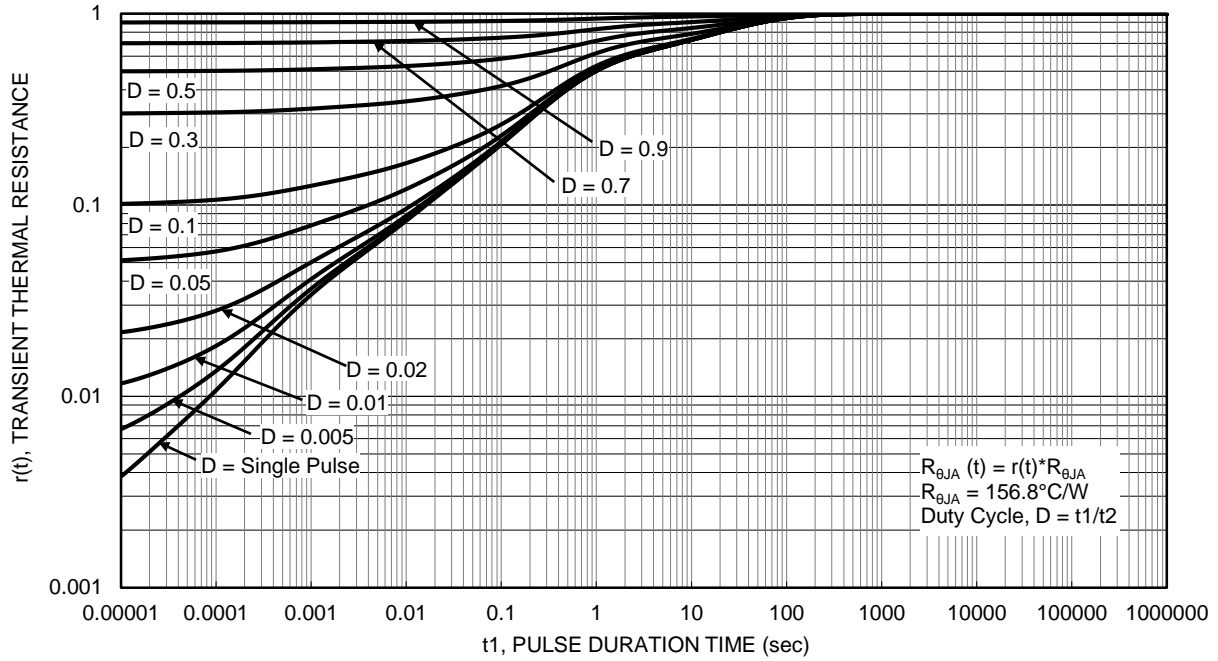


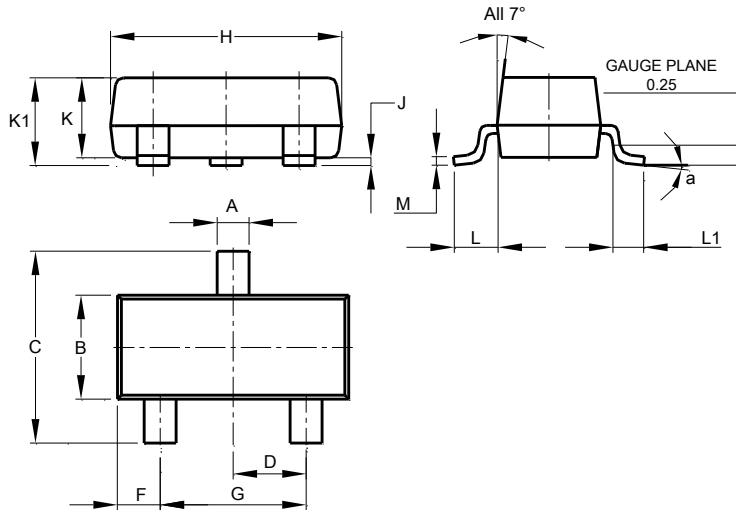
Figure 12. SOA, Safe Operation Area



## Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

### SOT23

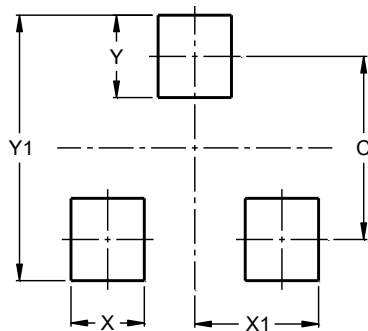


SOT23			
Dim	Min	Max	Typ
A	0.37	0.51	0.40
B	1.20	1.40	1.30
C	2.30	2.50	2.40
D	0.89	1.03	0.915
F	0.45	0.60	0.535
G	1.78	2.05	1.83
H	2.80	3.00	2.90
J	0.013	0.10	0.05
K	0.890	1.00	0.975
K1	0.903	1.10	1.025
L	0.45	0.61	0.55
L1	0.25	0.55	0.40
M	0.085	0.150	0.110
a	0°	8°	--
All Dimensions in mm			

## Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

### SOT23



Dimensions	Value (in mm)
C	2.0
X	0.8
X1	1.35
Y	0.9
Y1	2.9

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