

# NOT RECOMMENDED FOR NEW DESIGN CONTACT US



DMP58D0SV

#### **DUAL P-CHANNEL ENHANCEMENT MODE FIELD EFFECT TRANSISTOR**

#### **Features**

- Low On-Resistance
- ESD Protected Gate to 500V
- Low Input Capacitance
- Fast Switching Speed
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e.: parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please refer to the related automotive grade (Q-suffix) part.
   A listing can be found at

https://www.diodes.com/products/automotive/automotive-products/.

 This part is qualified to JEDEC standards (as references in AEC-Q) for High Reliability.

https://www.diodes.com/quality/product-definitions/

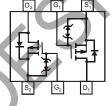
#### **Mechanical Data**

- Package: SOT-563
- Package Material: Molded Plastic, "Green" Molding Compound.
   UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020D
- Terminals: Finish Matte Tin annealed over Copper leadframe.
   Solderable per MIL-STD-202, Method 208 (2)
- Terminal Connections: See Diagram
- Marking Information: See Page 4
- Ordering Information: See Page 4
- Weight: 0.006 grams (Approximate)









TOP VIEW

TOP VIEW

Internal Schematic

## Maximum Ratings (@TA = +25°C, unless otherwise specified.)

С	haracteristic	Symbol	Value	Units
Drain-Source Voltage		VDSS	-50	V
Drain-Gate Voltage (Note 4		VDGR	-50	V
Gate-Source Voltage	Continuous	Vgss	±20	V
Drain Current (Note 5)	Continuous	I <sub>D</sub>	-160	mA

## Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units
Total Power Dissipation (Note 5)	PD	400	mW
Thermal Resistance, Junction to Ambient (Note 5)	RθJA	313	°C/W
Operating and Storage Temperature Range	TJ, TSTG	-55 to +150	°C

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4.  $R_{GS} \le 20 K\Omega$ .
- 5. Device mounted on FR-4 PCB, 1inch x 0.85inch x 0.062inch; pad layout as shown on Diodes Incorporated's suggested pad layout document AP02001, which can be found on our website at http://www.diodes.com/datasheets/ap02001.pdf.

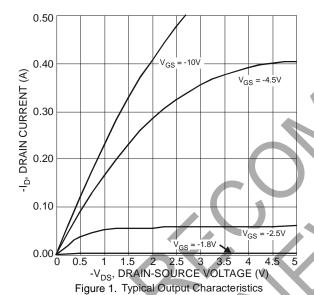


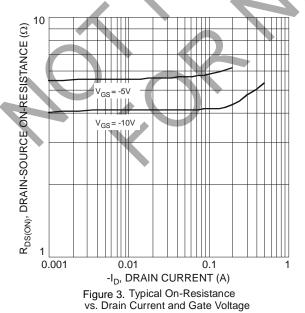
### Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 6)						
Drain-Source Breakdown Voltage	BVDSS	-50	_	_	V	$V_{GS} = 0V, I_{D} = -250\mu A$
Zero Gate Voltage Drain Current	IDSS	_	_	-1	μΑ	$V_{DS} = -50V$ , $V_{GS} = 0V$
Gate-Body Leakage	I <sub>GSS</sub>	_	_	±5	μΑ	$V_{GS} = \pm 20V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 6)						
Gate Threshold Voltage	V <sub>GS(TH)</sub>	-0.8		-2.1	V	$V_{DS} = V_{GS}$ , $I_D = -250\mu A$
Static Drain-Source On-Resistance	RDS(ON)		6	8	Ω	$V_{GS} = -5V$ , $I_{D} = -0.1A$
Forward Transconductance	grs	0.05	_	_	S	$V_{DS} = -25V, I_{D} = -0.1A$
DYNAMIC CHARACTERISTICS						
Input Capacitance	Ciss	_	27	_	pF	
Output Capacitance	Coss	_	4	_	pF∢	$V_{DS} = -25V$ , $V_{GS} = 0V$ , $f = 1.0MHz$
Reverse Transfer Capacitance	Crss	_	1.4	_	рF	

Note:

6. Short duration pulse test used to minimize self-heating effect.





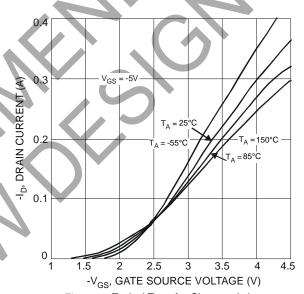


Figure 2. Typical Transfer Characteristics

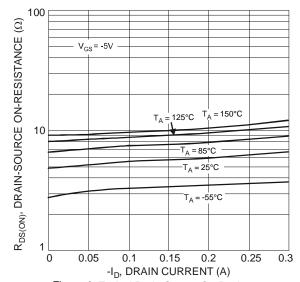


Figure 4. Typical Drain-Source On-Resistance vs. Drain Current and Temperature



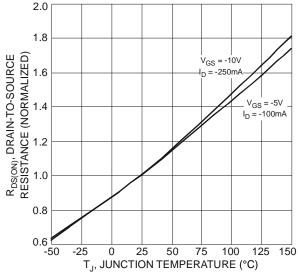


Figure 5. On-Resistance Variation with Temperature

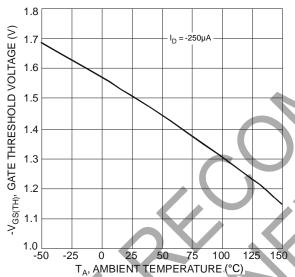
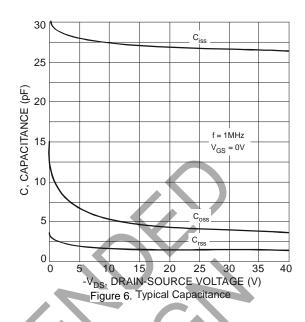


Figure 7. Gate Threshold Variation vs. Ambient Temperature



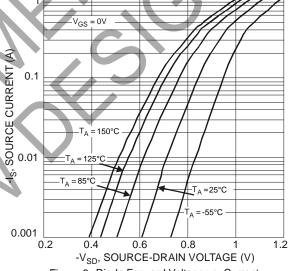


Figure 8. Diode Forward Voltage vs. Current

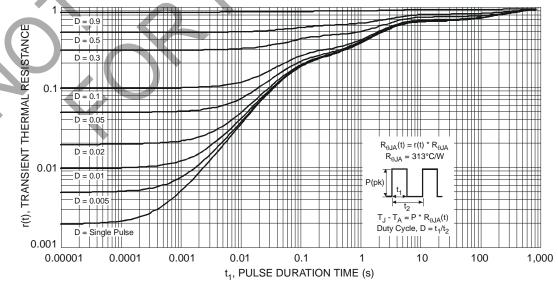


Figure 9. Transient Thermal Response

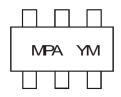


# Ordering Information (Note 7)

Part Number	Package	Packing		
Fait Nullibel	rackage	Qty.	Carrier	
DMP58D0SV-7	SOT-563	3000	Tape & Reel	

Note: 7. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

**Marking Information** (Note 8)



MPA = Product Type Marking Code YM = Date Code Marking Y = Year (ex: J = 2022) M = Month (ex: 6 = June)

Date Code Key

Year	2007		2022	2023	2024	2025	2026	2027	2028	2029	2030	2031
Code	U		J	K	L	M	N	0	Р	Ŕ	S	Т
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec

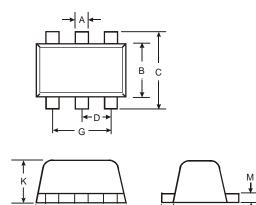
8. Package is non-polarized. Parts may be on reel in orientation illustrated, 180° rotated, or mixed (both ways). Note:



# **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

#### SOT-563

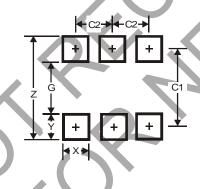


SOT-563						
Dim	Min	Max	Тур			
Α	0.15	0.30	0.20			
В	1.10	1.25	1.20			
C	1.55	1.70	1.60			
D	-		0.50			
G	0.90	1.10	1.00			
Н	1.50	1.70	1.60			
K	0.55	0.60	0.60			
4	0.10	0.30	0.20			
M	0.10	0.18	0.11			
All Dimensions in mm						

# **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT-563



Dimensions	Value (in mm)
Z	2.2
G	1.2
Х	0.375
Y	0.5
C1	1.7
C2	0.5



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